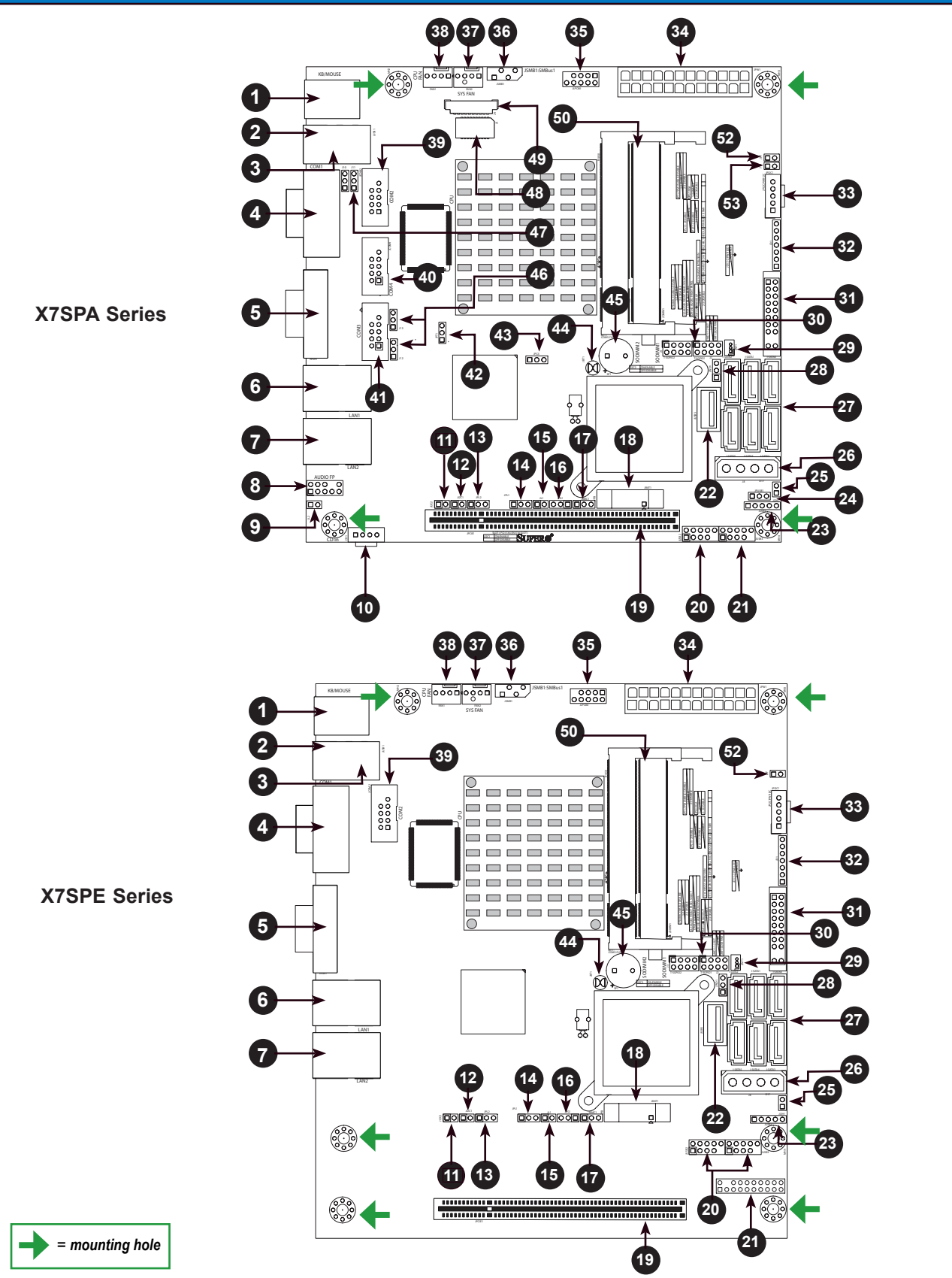


Motherboard Layout and Features



Jumpers, Connectors and LED Indicators

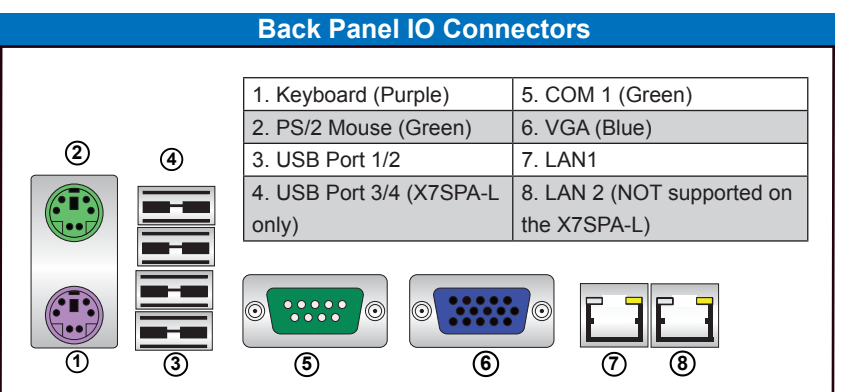
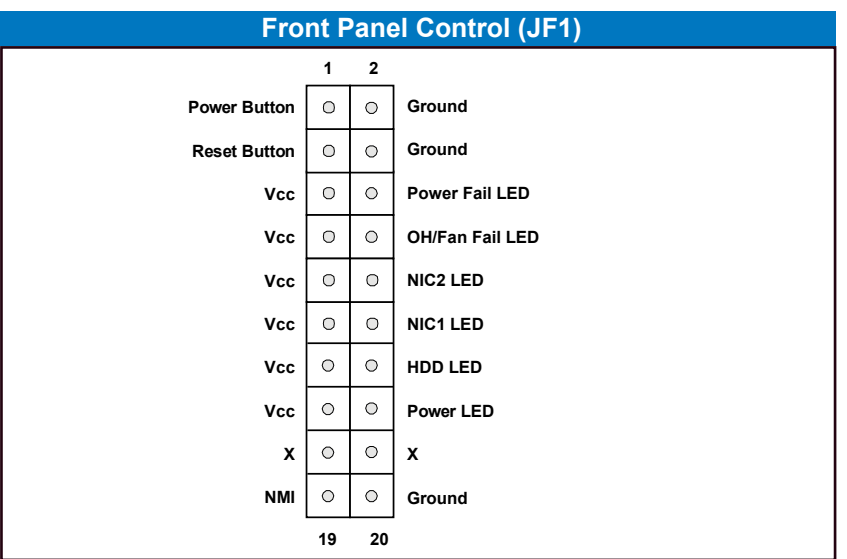
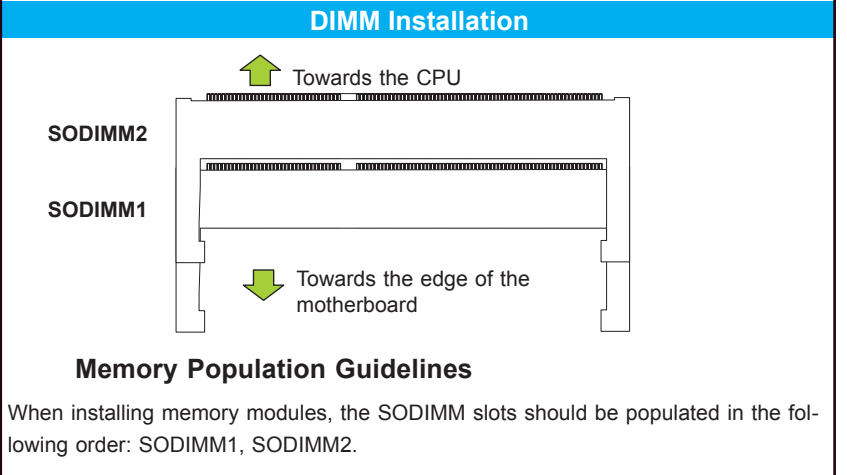
Connectors		
1	KB/Mouse	PS/2 Keyboard/Mouse
2, 3	USB 1/2, USB 3/4	Back Panel USB Ports (USB 3/4: X7SPA-L only)
4	COM1	Back Panel Serial Port
5	VGA	Video/Graphics Connector
6	LAN1	RJ45 Connector for LAN1
7	LAN2	RJ45 Connector for LAN2 (Not available on the X7SPA-L)
8	J5	Front Panel Audio Header Connector (X7SPE : not supported)
10	CD1	CD/DVD Drive Audio Input Header (Available on X7SPA-L Only)
15	JL1	Chassis Intrusion Header
18	JBAT1	Onboard Battery
19	JPCIE1	PCI-E 1.1 x4 Gen1 (in x16 physical) Slot 7 (Slot 6 on X7SPE series)
20	USB 5/6, 7/8	Front Panel USB headers
21	JTPM	TPM Header (X7SPE-H-D525/X7SPE-HF-D525 Only)
22	USB 10	Type A on-board USB Port
23	USB 9	Front Panel USB header
25	JOH	Overheat Warning LED header
26	J8	Power Connector for Add-on devices
27	SATA 0,1,2,3,4,5	SATA Connectors (SATA 2/3 is not available on the X7SPA-L)
29	JWF1	SATA Disk on Module (DOM) Power
30	T-SGPIO-0/1	Serial General Purpose IO headers (for SATA)
31	JF1	FP Control Panel Header
32	JD1	External Buzzer/Speaker/Power LED Pins 1-3 (Power LED), Pins 4-7 (External Speaker)
33	JPI2C	PWR supply (I ² C) System Management Bus
34	JPW1	ATX 24-Pin Power Connector
35	Unused	Factory Test Point
36	JSMB1	System Management Bus header
38, 37	Fans 1, 2	Fan 1: CPU Fan, Fan 2: Chassis Fan Header
39, 40, 41	COM2, COM4, COM3	Serial Port 2, 4 and 3 Headers (COM 3/4 X7SPA-L only)
45	SPK	Onboard Speaker/Buzzer
48	J5	18-bpp LVDS LCD Monitor Port
49	J6	Inverter Connector
50	DIMM 1, DIMM 2	SO-DIMM Memory Slots

Jumpers			
9	JL2	AC97/HD Audio Selector (Front Panel)	Open (HD Audio), Closed (AC97)
11, 12	J1 ² C2/J1 ² C1	SMB to PCI Slots	Open/Open (Disabled)
13	JPL2	LAN2 Enable/Disable	Pins 1-2 (Enabled)
14	JPL1	LAN1 Enable/Disable	Pins 1-2 (Enabled)
16	JPG1**	On-board VGA Enable/Disable	Pins 1-2 (Enable on-board VGA)
17	JPB**	BMC Enable/Disable	Pins 1-2 (Enabled)
24	JPUSB1	USB Wake-up Enable	Pins 1-2 (Enabled)
28	JWD1	Watch Dog Timer	Pins 1-2 (Reset)
42	JPT1	Trusted Platform Module Enable	Pins 1-2 (Enabled)
43	JPC3*	COM3 Selection	Pins 1-2 (RS232), Pins 2-3 (TTL)
44	JBT1	CMOS Clear	Short contact pads to reset CMOS
46	J12/J13*	Pin 1 DCD/P5V Select (COM3/COM4)	Pins 1-2 (DCD), Pins 2-3 (P5V)
47	J10/J11*	Pin 1 DCD/P5V Select (COM1/COM2)	Pins 1-2 (DCD), Pins 2-3 (P5V)
52	JPF	Power Force On	Open (Disabled)
53	J14	Reserved (Unused)	Reserved (Unused)

*OEM option for the X7SPA-H, X7SPA-HF, and X7SPE series.
 ** X7SPA-HF, X7SPE-HF and X7SPE-HF-D525 only.

Memory Support

The X7SPA-L, X7SPA-H, X7SPA-HF, X7SPE-H and X7SPE-HF supports up to 4GB of unbuffered Non-ECC DDR2 SODIMMs (667/800 MHz) while the X7SPE-H-D525, X7SPA-H-D525, X7SPA-HF-D525 and X7SPE-HF-D525 supports up to 4GB of unbuffered Non-ECC DDR3 SODIMMs (800MHz).
 Note: For memory optimization, use only SODIMM modules that have been validated by Supermicro. For the latest memory updates, please refer to our website at <http://www.supermicro.com/products/motherboard>.



Note: Graphics shown in this quick reference guide are for illustration only. Your components may or may not look exactly the same as drawings shown in this guide.

Note: Refer to Chapter 2 of the User Manual for detailed information on jumpers, connectors, and LED indicators.

Note: Refer to Chapter 2 of the User Manual for detailed information on memory support and CPU/motherboard installation instructions.