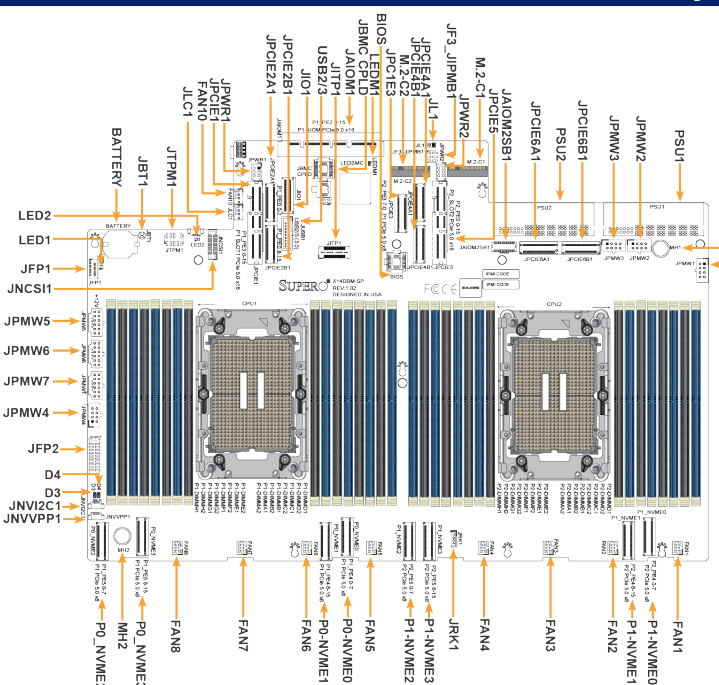


# SUPERMICR<sup>®</sup> SuperServer 222H-TN Quick Reference Guide

## Board Layout



| Connector             | Description  |
|-----------------------|--|
| Battery (BT1)         | Onboard Battery  |
| FAN1-FAN8, FAN10      | Eight-pin (8-pin) cooling fan headers (FAN1-FAN8) and one 4-pin cooling fan header (FAN10) |
| JA1OM1 (P1_PE2 0-15)  | Supernano Advanced Input/Output Module (AIOM) PCIe 5.0 x16                                 |
| JA1OM2 (P1_PE2 0-15)  | Supernano Advanced Input/Output Module (AIOM2) sideband connector                          |
| J1 (BT1)              | System BMC internal I/O header   |
| JFP1                  | Front Control Panel header   |
| JFP2                  | Front Control Panel header with USB and VGA support  |
| JB1 (LAN/USB/VGA)     | Low-profile I/O Slim SAS I/O connector used for dedicated BMC LAN/USB/VGA connections      |
| J1 (BT1)              | Chassis Intrusion header   |
| PCIE1 (P1_PE3 0-15)   | (P1-SLOT1) PCIe 5.0 x16 slot supported by CPU1   |
| PCIE2A1 (P1_PE0 0-15) | (P1) PCIe 5.0 x8 M.2 connector supported by CPU1   |
| PCIE2B1 (P1_PE0 8-15) | (P1) PCIe 5.0 x8 M.2 connector supported by CPU1   |
| PCIE3                 | (P1) PCIe 5.0 x8 M.2 connector supported by CPU1   |
| PCIE4A1 (P2_PE3 7-15) | (P2) PCIe 5.0 x8 M.2 connector supported by CPU2   |
| PCIE4B1 (P2_PE3 8-15) | (P2) PCIe 5.0 x8 M.2 connector supported by CPU2   |
| PCIE5 (P2_PE3 0-15)   | (P2-SLOT2) PCIe 5.0 x16 slot supported by CPU2   |
| PCIE6A1 (P2_PE2 0-15) | M.2 (P2) PCIe 5.0 x8 connector supported by CPU2   |
| PCIE6B1 (P2_PE2 8-15) | M.2 (P2) PCIe 5.0 x8 connector supported by CPU2   |
| PCIE7 (P2_PE2 0-15)   | M.2 (P2) PCIe 5.0 x8 connector supported by CPU2   |
| JPMW1-JPMW4           | 12V power connectors used for system backplane/GPU devices (via J12V)                      |
| JPMW5-JPMW7           | 5V power connectors used for system backplane/GPU devices (via J5V)                        |
| JPMW8                 | 5V power connector for riser card use  |
| PSU1/PSU2             | Power Supply Unit/Unit2 for system power use (for 1U systems) (see the third note)         |
| JNCSI                 | NC-SI (Network Controller Sideband Interface) connector (see the second note)              |
| JNV1C1                | NVMe SMBus (System Management Bus) with hot-plug support                                   |
| JNV1C2                | NVMe VPP SMBus (System Management Bus) with hot-plug support                               |
| JNV1C3                | Trusted Platform Module/Port 80 connector  |
| M.2-C1/M.2-C2         | M.2 M-Key PCIe 5.0 x2 slots (2280/22110)   |
| PTM1                  | PCIe 5.0 x8 connectors supported by CPU1 with NVMe Controller (P1_PE3 0-7)                 |
| PTM2                  | PCIe 5.0 x8 connectors supported by CPU1 with NVMe Controller (P1_PE3 8-15)                |
| PTM3                  | PCIe 5.0 x8 connectors supported by CPU1 with NVMe Controller (P1_PE3 0-7)                 |
| PTM4                  | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM5                  | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM6                  | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM7                  | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM8                  | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM9                  | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM10                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM11                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM12                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM13                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM14                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM15                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM16                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM17                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM18                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM19                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM20                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM21                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM22                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM23                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM24                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM25                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM26                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM27                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM28                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM29                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM30                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM31                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM32                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM33                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM34                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM35                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM36                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM37                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM38                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM39                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM40                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM41                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM42                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM43                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM44                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM45                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM46                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM47                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM48                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM49                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM50                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM51                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM52                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM53                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM54                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM55                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM56                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM57                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM58                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM59                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM60                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM61                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM62                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM63                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM64                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM65                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM66                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM67                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM68                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM69                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM70                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM71                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM72                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM73                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM74                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM75                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM76                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM77                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM78                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM79                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM80                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM81                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM82                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM83                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM84                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM85                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM86                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM87                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM88                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM89                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM90                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM91                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM92                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM93                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM94                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM95                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM96                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM97                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM98                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |
| PTM99                 | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 8-15)                |
| PTM100                | PCIe 5.0 x8 connectors supported by CPU2 with NVMe Controller (P2_PE3 0-7)                 |

## CPU Installation

- Put processor into bracket – attention to the lineup Pin and key on both processor and carrier.
- Put processor carrier module into HS.
- Put processor heatsink module into MB.

**Processor Carrier Assembly (with CPU mounted on the Processor Clip)**

**Heatsink**

**CPU Socket**

**Threaded Fastener**

**Rotating Wire**

**Peek Nut**

**Rotating Wire (Unlatched)**

**Rotating Wire (Latched)**

**Use a torque of 12lbf-in**

**T30 Torx Driver**

## Memory Support

| Intel <sup>®</sup> Xeon <sup>®</sup> 6700/6500-series DDR5 Memory Population Table (2 Processors and 32 DIMMs Installed) |  |
|--|--|
| <b>2 Processor DIMM Count (Recommended)</b>  | <b>Memory Population Sequence</b>  |
| <b>2 Processors and 2 DIMMs</b>  | P1-DIMMA1<br>P2-DIMMA1   |
| <b>2 Processors and 8 DIMMs</b>  | P1-DIMMA1/P1-DIMMC1/P1-DIMME1/P1-DIMMG1<br>P2-DIMMA1/P2-DIMMC1/P2-DIMME1/P2-DIMMG1   |
| <b>2 Processors and 8 DIMMs</b>  | P1-DIMMB1/P1-DIMMH1/P1-DIMMF1/P1-DIMMH1<br>P2-DIMMB1/P2-DIMMH1/P2-DIMMF1/P2-DIMMH1   |
| <b>2 Processors and 16 DIMMs</b>   | P1-DIMMA1/P1-DIMMA2/P1-DIMMC1/P1-DIMMC2/P1-DIMME1/P1-DIMME2/P1-DIMMG1/P1-DIMMG2<br>P2-DIMMA1/P2-DIMMA2/P2-DIMMC1/P2-DIMMC2/P2-DIMME1/P2-DIMME2/P2-DIMMG1/P2-DIMMG2   |
| <b>2 Processors and 16 DIMMs</b>   | P1-DIMMB1/P1-DIMMB2/P1-DIMMD1/P1-DIMMD2/P1-DIMMF1/P1-DIMMF2/P1-DIMMH1/P1-DIMMH2<br>P2-DIMMB1/P2-DIMMB2/P2-DIMMD1/P2-DIMMD2/P2-DIMMF1/P2-DIMMF2/P2-DIMMH1/P2-DIMMH2   |
| <b>2 Processors and 16 DIMMs (*)</b>   | P1-DIMMA1/P1-DIMMB1/P1-DIMMC1/P1-DIMMD1/P1-DIMME1/P1-DIMMF1/P1-DIMMH1/P1-DIMMH2<br>P2-DIMMA1/P2-DIMMB1/P2-DIMMC1/P2-DIMMD1/P2-DIMME1/P2-DIMMF1/P2-DIMMH1/P2-DIMMH2   |
| <b>2 Processors and 32 DIMMs (*)</b>   | P1-DIMMA1/P1-DIMMA2/P1-DIMMB1/P1-DIMMB2/P1-DIMMC1/P1-DIMMC2/P1-DIMMD1/P1-DIMMD2/P1-DIMME1/P1-DIMME2/P1-DIMMG1/P1-DIMMG2/P1-DIMMH1/P1-DIMMH2<br>P2-DIMMA1/P2-DIMMA2/P2-DIMMB1/P2-DIMMB2/P2-DIMMC1/P2-DIMMC2/P2-DIMMD1/P2-DIMMD2/P2-DIMME1/P2-DIMME2/P2-DIMMF1/P2-DIMMF2/P2-DIMMH1/P2-DIMMH2 |

## System Information

**Pull-out tag with BMC unique password underneath.**

Each system comes with a unique default password for the ADMIN user. This can be found on a sticker on the motherboard and a sticker underneath the service tag on chassis. If necessary, the password can be reset by the Supermicro IPMICFG tool.

For more information, please visit <https://www.supermicro.com/en/solutions/management-software/bmc-resources>.

## Caution and Product Resources

**SAFETY INFORMATION:**

IMPORTANT: See installation instructions and safety warning before connecting system to power supply.

[http://www.supermicro.com/about/policies/safety\\_information.cfm](http://www.supermicro.com/about/policies/safety_information.cfm)

**CAUTION:**

Always be sure all power supplies for this system have the same power output. If mixed power supplies are installed, the system will not operate.

**CAUTION:**

This unit has redundant power sources. Please disconnect all the power cords before servicing.

**WARNING:**

To reduce risk of electric shock/damage to equipment, disconnect power from server by disconnecting all power cords from electrical outlets. If any CPU socket empty, install protective plastic CPU cap.

**PRODUCT RESOURCES:**

For more information go to: <http://www.supermicro.com/support>

## Front View

| No. | Description                                |
|-----|--|
| 1   | Front Control Panel                        |
| 2   | Service/Asset Tag with BMC Unique Password |

## Rear View

| No. | Description  |
|-----|--|
| 1   | Slot 1-2: 2 optional PCIe 5.0 x8 (in x16) or PCIe 5.0 x16 full-height, full-length slots   |
| 2   | Slot 3-4: 2 optional PCIe 5.0 x8 (in x16) or PCIe 5.0 x16 full-height, full-length slots   |
| 3   | Slot 5: optional PCIe 5.0 x8 or x16 Slot (CPU1)  |
| 4   | Slot 6: optional PCIe 5.0 x8 Slot (CPU1)   |
| 5   | Slot 7: optional PCIe 5.0 x8 or x16 Slot (CPU1)  |
| 6   | Slot 8: optional PCIe 5.0 x8 Slot (CPU1)   |
| 7   | Slot A1: AIOM/OCP 3.0 Networking Slot (CPU1)   |
| 8   | Slot A2: AIOM/OCP 3.0 Networking Slot (CPU2)   |
| 9   | VGA Port   |
| 10  | 2x USB 3.0 Ports   |
| 11  | Dedicated BMC LAN Port   |
| 12  | Redundant Titanium Power Supplies  |
| 13  | Power Supply LED Solid Green<br>Power Supply LED Blinking Green<br>Power Supply LED Solid Amber<br>Power Supply LED Blinking Amber |

\* Slot 6 is disabled if Slot 5 is configured as x16 \*\* Slot 8 is disabled if Slot 7 is configured as x16

LBL-2770-T-QRG Rev 1.0

## NVMe Drive Bay Cable Routing

| # | Description | SMC Part Number    |
|---|-------------|--------------------|
| 4 | NVMe        | CBL-MCIO-1229M5LFL |
| 5 | NVMe        | CBL-MCIO-1234M5L   |
| 6 | NVMe        | CBL-MCIO-1237M5-M  |
| 7 | NVMe        | CBL-MCIO-1237M5-M  |

| #  | Description | SMC Part Number    |
|----|-------------|--------------------|
| 1  | NVMe        | CBL-GN24-1255CM5Y  |
| 2  | NVMe        | CBL-MCIO-1225M5L   |
| 3  | NVMe        | CBL-MCIO-1225M5L   |
| 4  | NVMe        | CBL-MCIO-1229M5LFL |
| 5  | NVMe        | CBL-MCIO-1234M5L   |
| 6  | NVMe        | CBL-MCIO-1237M5-M  |
| 7  | NVMe        | CBL-MCIO-1237M5-M  |
| 12 | I2C (BMC)   | CBL-CDAT-1062      |
| 13 | I2C (BMC)   | CBL-CDAT-1062      |

| #  | Description | SMC Part Number    |
|----|-------------|--------------------|
| 1  | NVMe        | CBL-GN24-1255CM5Y  |
| 2  | NVMe        | CBL-MCIO-1225M5L   |
| 3  | NVMe        | CBL-MCIO-1225M5L   |
| 4  | NVMe        | CBL-MCIO-1229M5LFL |
| 5  | NVMe        | CBL-MCIO-1234M5L   |
| 6  | NVMe        | CBL-MCIO-1237M5-M  |
| 7  | NVMe        | CBL-MCIO-1237M5-M  |
| 8  | NVMe        | CBL-MCIO-1432M5FLE |
| 9  | NVMe        | CBL-MCIO-1229M5L   |
| 10 | NVMe        | CBL-MCIO-1432M5FLE |
| 11 | NVMe        | CBL-MCIO-1234M5L   |
| 12 | I2C (BMC)   | CBL-CDAT-1062      |
| 13 | I2C (BMC)   | CBL-CDAT-1062      |

## Storage AOC Drive Bay Cable Routing

| # | Description | SMC Part Number    |
|---|-------------|--------------------|
| 2 | SAS         | CBL-SAST-1276F-100 |
| 6 | I2C (BMC)   | CBL-CDAT-1062      |

| # | Description | SMC Part Number    |
|---|-------------|--------------------|
| 1 | SAS         | CBL-SAST-1264F-100 |
| 2 | SAS         | CBL-SAST-1276F-100 |
| 5 | I2C (BMC)   | CBL-CDAT-1062Y-45  |

| # | Description | SMC Part Number    |
|---|-------------|--------------------|
| 1 | SAS         | CBL-SAST-1264F-100 |
| 2 | SAS         | CBL-SAST-1276F-100 |
| 3 | SAS         | CBL-SAST-1296F-100 |
| 4 | I2C (BMC)   | CBL-CDAT-1062T-62  |

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