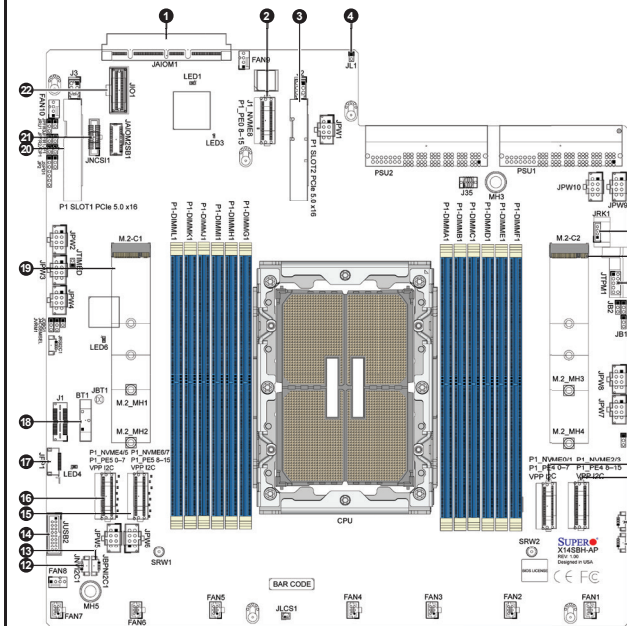


# SUPERMICR® SuperServer SYS-212HA-TN Quick Reference Guide

## Board Layout



| Item | Description   | Item | Description   |
|------|---|------|---|
| 1    | JAIOM1: PCIe 5.0 x16 AIOM Slot Connector                | 12   | JNVPC1: NVMe I2C Header Channel 1                       |
| 2    | P1_NVME 8: PCIe 5.0 x8 M.2 Connector (P1_PE0 8-15)      | 13   | JBNPFC1: BPN I2C Header Channel 1                       |
| 3    | P1_SLOT2: PCIe 5.0 x16 4C Slot Connector                | 14   | JUSB2: USB 3.2 Gen1 Header                              |
| 4    | JL1: Intrusion Header                                   | 15   | P1_NVME 6/7: PCIe 5.0 x8 M.2 Connector (P1_PE4 8-15)    |
| 5    | JRK1: Intel VROC RAID Key Header                        | 16   | P1_NVME 4/5: PCIe 5.0 x8 M.2 Connector (P1_PE4 0-7)     |
| 6    | M.2-C2: M.2 PCIe 5.0 Interface (M-key 25110/22110/2280) | 17   | JFP1: Front Control Panel Header                        |
| 7    | JTPM1: TPM Connector                                    | 18   | BT1: Onboard CMOS Battery                               |
| 8    | P1_NVME 0/1: PCIe 5.0 x8 M.2 Connector (P1_PE4 0-7)     | 19   | M.2-C1: M.2 PCIe 5.0 Interface (M-key 25110/22110/2280) |
| 9    | P1_NVME 2/3: PCIe 5.0 x8 M.2 Connector (P1_PE4 8-15)    | 20   | P1_SLOT1: PCIe 5.0 x16 4C Slot Connector                |
| 10   | JNVPC2: NVMe I2C Header Channel 2                       | 21   | JNCSI1: NC-SI Header                                    |
| 11   | JBNPFC2: BPN I2C Header Channel 2                       | 22   | JIO1: System I/O Board Connector (VGA, USB, BMC NIC)    |

## Memory Support

### DIMM Population Guide

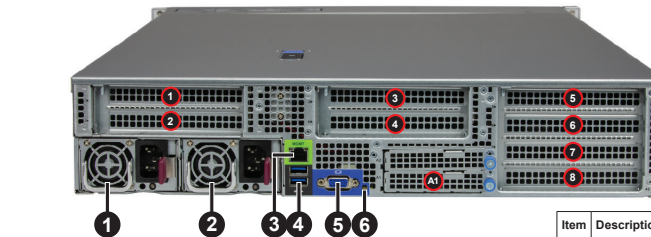
| Type     | Channel |    |    |    |    |    |    |    |    |    |    |    |
|----------|---------|----|----|----|----|----|----|----|----|----|----|----|
|          | L1      | K1 | J1 | I1 | H1 | G1 | A1 | B1 | C1 | D1 | E1 | F1 |
| 1 DIMM   |         |    |    |    |    |    |    |    |    |    |    |    |
| 8 DIMMs  | V       | V  | V  | V  | V  | V  | V  | V  | V  | V  | V  | V  |
| 12 DIMMs | V       | V  | V  | V  | V  | V  | V  | V  | V  | V  | V  | V  |

Note: Please refer to user manual for memory module [rank] and [DRAM density] requirements when using Intel® Xeon® 6 CPU.

### Front View & Interface

| Item | Description     |
|------|-----------------|
| 1    | Power Button    |
| 2    | UID Button      |
| 3    | Power LED       |
| 4    | NIC LED         |
| 5    | Power Fail LED  |
| 6    | HDD LED         |
| 7    | NIC1 LED        |
| 8    | Information LED |

## Rear View



| Item | Description    |
|------|----------------|
| 1    | Power Supply 1 |
| 2    | Power Supply 2 |
| 3    | BMC LAN Port   |
| 4    | USB Ports      |
| 5    | VGA Port       |
| 6    | UID Button     |

## System Information

**Pull-out tag with BMC unique password and Serial Number Label underneath.**

Each system comes with a unique default password for the ADMIN user. This can be found on a sticker on the motherboard and a sticker underneath the service tag on chassis. If necessary, the password can be reset by the Supermicro IPMICFG tool.

For more information, please visit <https://www.supermicro.com/en/solutions/management-software/bmc-resources/>

## Caution and Product Resources

**SAFETY INFORMATION:**  
IMPORTANT: See installation instructions and safety warning before connecting system to power supply.  
[http://www.supermicro.com/about/policies/safety\\_information.cfm](http://www.supermicro.com/about/policies/safety_information.cfm)

**CAUTION:**  
Always be sure all power supplies for this system have the same power output. If mixed power supplies are installed, the system will not operate.

**WARNING:**  
To reduce risk of electric shock/damage to equipment, disconnect power from server by disconnecting all power cords from electrical outlets. If any CPU socket empty, install protective plastic CPU cap.

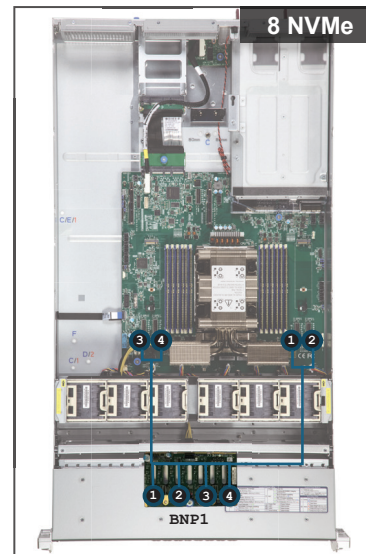
**PRODUCT RESOURCES:**  
For more information go to: <http://www.supermicro.com/support>

LBL-4036-T-QRG



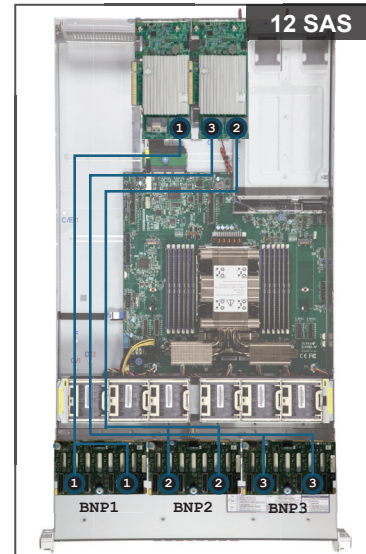
Rev. 1.0

## NVMe Drive Cable Routing

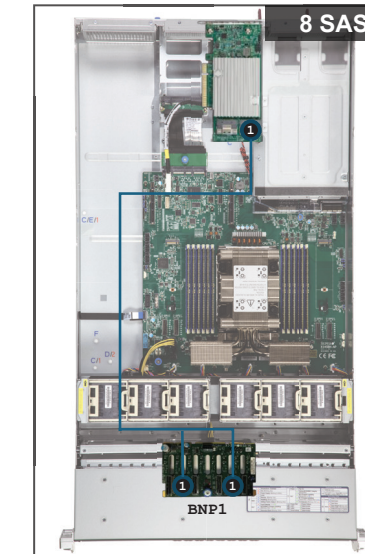


| Connector on Board/Card | Backplane/Riser and Port         | Drive Bay | SMC Cable P/N   |
|-------------------------|----------------------------------|-----------|-----------------|
| P1_NVME0/1              | BPN1 NVME1 (BPN-NVME5-HS219N-S8) | 0-1       | CBL-MCIO-1240M5 |
| P1_NVME2/3              | BPN1 NVME2 (BPN-NVME5-HS219N-S8) | 2-3       | CBL-MCIO-1240M5 |
| P1_NVME4/5              | BPN1 NVME3 (BPN-NVME5-HS219N-S8) | 4-5       | CBL-MCIO-1240M5 |
| P1_NVME6/7              | BPN1 NVME4 (BPN-NVME5-HS219N-S8) | 6-7       | CBL-MCIO-1240M5 |

## Storage AOC Drive Cable Routing



| Connector on Board/Card | Backplane/Riser and Port             | Drive Bay | SMC Cable P/N      |
|-------------------------|--------------------------------------|-----------|--------------------|
| SAS AOC1 CN0            | BPN1 CN1 & CN2 (BPN-NVME5-HS219N-S8) | 0-7       | CBL-SAST-1296F-100 |
| SAS AOC2 CN0            | BPN2 CN1 & CN2 (BPN-NVME5-HS219N-S8) | 8-15      | CBL-SAST-1296F-100 |
| SAS AOC2 CN1            | BPN3 CN1 & CN2 (BPN-NVME5-HS219N-S8) | 16-23     | CBL-SAST-1296F-100 |



| Connector on Board/Card | Backplane/Riser and Port             | Drive Bay | SMC Cable P/N      |
|-------------------------|--------------------------------------|-----------|--------------------|
| SAS AOC CN0             | BPN1 CN1 & CN2 (BPN-NVME5-HS219N-S8) | 0-7       | CBL-SAST-1296F-100 |

LBL-4036-B-QRG  
<http://www.supermicro.com>



Rev. 1.0