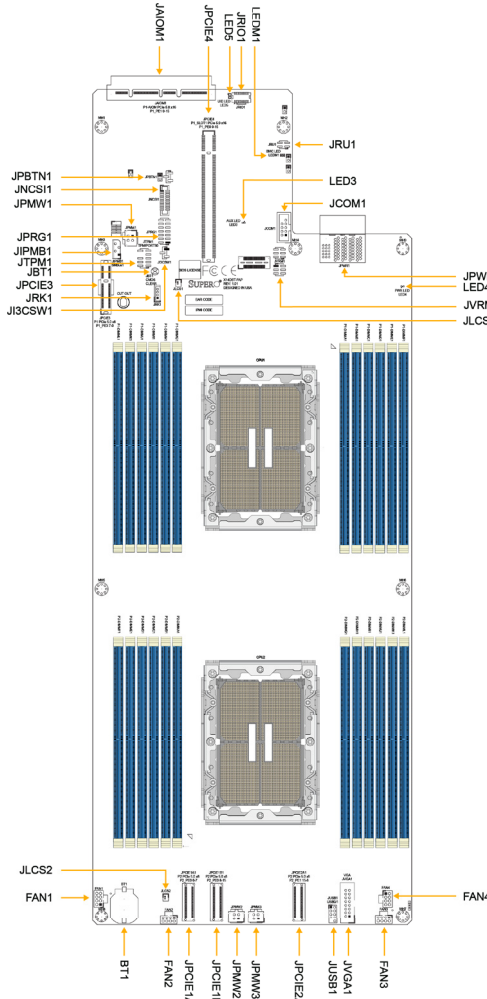


SUPERMICR® SuperServer® SYS-222FT-HEA-LCC/ALC Quick Reference Guide

Board Layout



LED	Description	Status
LEDM1 (BMC LED)	BMC Heartbeat LED	Blinking Green: BMC Normal (Active), Solid Green: (During BMC Reset or during a Cold Reboot)
LED3 (AUX LED)	AUX LED	LED On: +3.3 V AUX Power On
LED4 (LEDPWR)	Onboard Power LED	LED On: Onboard Power On
LED5	Unit Identifier (UID) LED	Solid Blue: Unit Identification LED

Jumper	Description	Default Setting
JBT1	CMOS Clear	Open (normal)
JPPR1	Enables PFR Debug	Off (Pin 1-1): Normal (Default), On (Pin 1-2): Enables PFR_DEBUG
JPPR2	Updates PFR	Off (Pin 1-1): Normal (Default), Debug Key On (Pin 1-2): PFR Update, Production Key
JPPR3	Disables PFR Function	Off (Pin 1-1): Normal (Default), On (Pin 1-2): Disable PFR

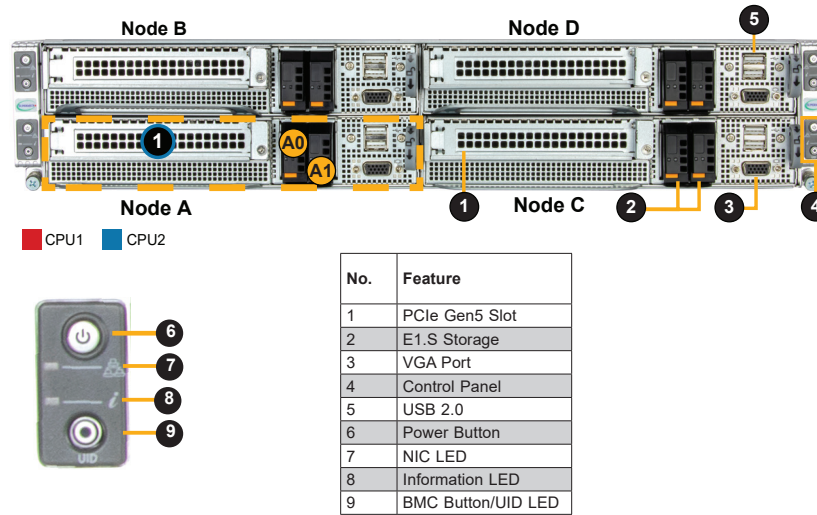
Connector	Description
BT1	Onboard Battery
FAN1-FAN4	Four 8-pin Cooling Fan Header
JAIOM1 (P1_PE1 0-15)	Advanced Input/Output Module (AIOM) PCIe 5.0 x16 Connector for I/O Support
JCOM1	Serial (COM) Header
JI3CSW1	I ³ C Switch (Pins: 1-2: I3C Functionality, 2-3: MBP Functionality)
JIPMB1	4-pin BMC External I ² C Header
JLCS1/JLCS2	Coolant Leakage Sensors
JPBTM1	Power Button
JPCIE1A1 (P2_PE0 0-7)	MCIO (P2) PCIe 5.0 x8 Connector
JPCIE1B1 (P2_PE0 8-15)	MCIO (P2) PCIe 5.0 x8 Connector
JPCIE2A1 (P2_PE1 15-8)	MCIO (P2) PCIe 5.0 x8 Connector
JPCIE3 (P1_PE3 7-0)	PCIe 5.0 x8 Connector for M.2 Risers Card
JPCIE4 (P1_PE0 0-15)	PCIe 5.0 x16 Connector for AOC
JPMW1-JPMW3	Three 4-pin +12 V Power Connectors
JPRG1	Programming Header for Motherboard Complex Programmable Logic Devices (CPLD)
JPWR1	Power Connector
JNSCI1	Network Controller Sideband Interface (NC-SI) connector
JRIO1	I/O Module Connector
JRK1	Intel VROC RAID Key Header
JRU1	UID LED and System_Reset Button Select Jumper
JTPM1	Trusted Platform Module/Port 80 connector
JUSB1 (USB0/1 (3.0))	USB Header
JVGA1	VGA port
JVRM1	VRM Header

Memory Support

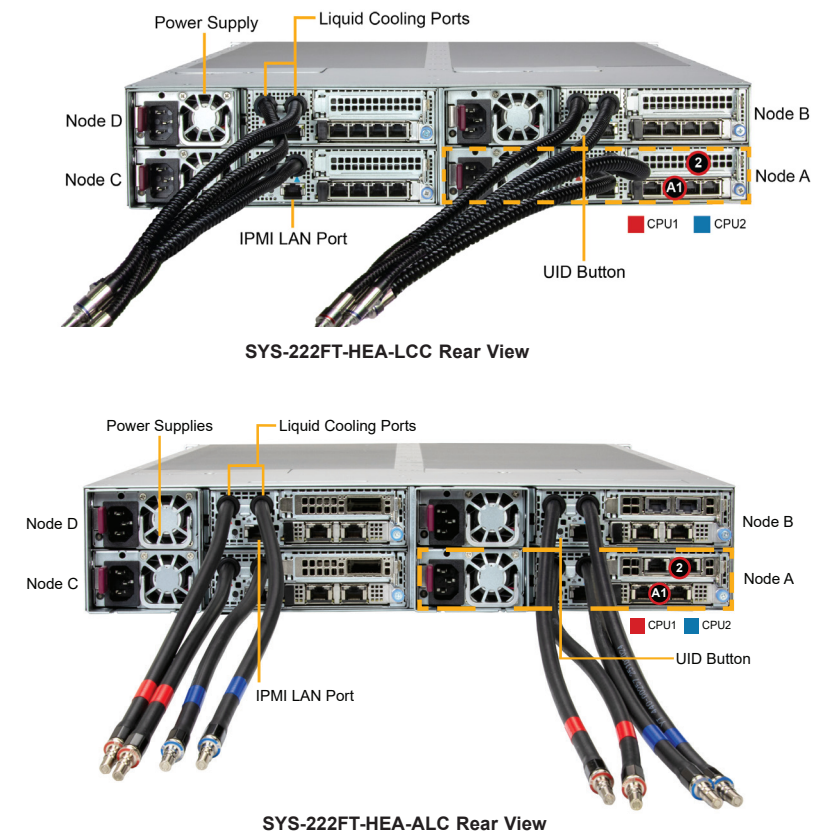
Intel® Xeon® 6900-Series Processors with P-Cores DDR5 Memory Population (2 Processors and 24 DIMMs Installed, 1DPC)	
2 Processor DIMM Counts (Recommended)	Memory Population Sequence (1DPC)
2 Processors and 2 DIMMs	P1-DIMMA1 P2-DIMMA1
2 Processors and 16 DIMMs	P1-DIMMC1/P1-DIMMD1/P1-DIMME1/P1-DIMMF1/P1-DIMMI1/P1-DIMMJ1/P1-DIMMK1/P1-DIMML1 P2-DIMMC1/P2-DIMMD1/P2-DIMME1/P2-DIMMF1/P2-DIMMI1/P2-DIMMJ1/P2-DIMMK1/P2-DIMML1
2 Processors and 16 DIMMs	P1-DIMMA1/P1-DIMMB1/P1-DIMMD1/P1-DIMME1/P1-DIMMF1/P1-DIMMI1/P1-DIMMJ1/P1-DIMMK1 P2-DIMMA1/P2-DIMMB1/P2-DIMMD1/P2-DIMME1/P2-DIMMF1/P2-DIMMI1/P2-DIMMJ1/P2-DIMMK1
2 Processors and 16 DIMMs	P1-DIMMA1/P1-DIMMB1/P1-DIMMC1/P1-DIMMF1/P1-DIMMI1/P1-DIMMJ1/P1-DIMMK1/P1-DIMML1 P2-DIMMA1/P2-DIMMB1/P2-DIMMC1/P2-DIMMF1/P2-DIMMI1/P2-DIMMJ1/P2-DIMMK1/P2-DIMML1
2 Processors and 24 DIMMs	P1-DIMMA1/P1-DIMMB1/P1-DIMMC1/P1-DIMMD1/P1-DIMME1/P1-DIMMF1/P1-DIMMI1/P1-DIMMJ1/P1-DIMMK1/P1-DIMML1 P2-DIMMA1/P2-DIMMB1/P2-DIMMC1/P2-DIMMD1/P2-DIMME1/P2-DIMMF1/P2-DIMMI1/P2-DIMMJ1/P2-DIMMK1/P2-DIMML1

Intel® Xeon® 6900-Series Processors with P-Cores DDR5 Memory Support					
Type	Ranks Per DIMM, Data Width (Stack)	DIMM Capacity (GB)			Speed (MT/s); Voltage (V); Slots per Channel (SPC) and DIMMs per Channel (DPC)
		DRAM Density			
		16 Gb	24 Gb	32 Gb	
RDIMM	1Rx4	32 Gb	48 Gb	-	6400, 6000, 5600, 5200, 4800 (DDR5-6400 rated RDIMMs only)
	2Rx8	32 Gb	48 Gb	-	
	2Rx4	64 Gb	96 Gb	128 Gb	
3DS RDIMM	8Rx4	256 Gb	-	-	8800, 8000, 7200 (MRDIMM-8800 only)
	4Rx4	-	-	256 Gb	
MRDIMM	2Rx4	32 Gb	48 Gb	-	8800, 8000, 7200 (MRDIMM-8800 only)
	2Rx4	64 Gb	96 Gb	128 Gb	
	4Rx8	64 Gb	96 Gb	-	
	4Rx4 (2U)	128 Gb	-	-	
	4Rx4 (2U)	-	-	256 Gb	

Front View



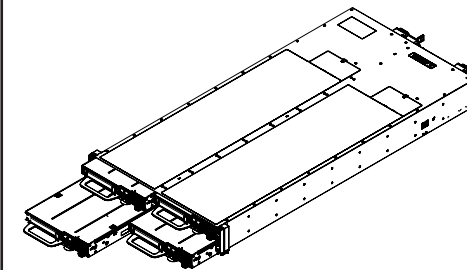
Rear View



System Features: Rear (Per Node)	
Feature	Description
Power Supply	One Titanium Level power supply module for each independent computing node
Liquid Cooling Ports	Two liquid cooling ports for each node
UID Button	One Unit ID (UID) button with LED per node
IPMI LAN Port	One IPMI LAN port for remote management per node

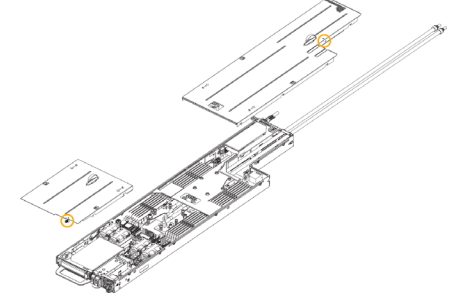
Removing a Node

Each of the four nodes may be removed from the main chassis. Each node is configurable on its own.



Removing Top Cover

Each node features a removable top cover, which allows easy access to the inside of the chassis.



Cold Plate Torque Specification

Part Number and Description	SMU CPU Cold Plate Torque Specification
SMC LCS-SLCM-X0009 (LCM for FlexTwin DP X14 CPU, CPC QD, RoHS)	Intel T30 8 in-lbf for 4 sockets
SMC by CoolIT LCS-SLCM-X00025 (LCM, X14, FlexTwin, CPU ColdPlate, RoHS)	Intel T30 8 in-lbf for 4 sockets
SMC by CoolIT LCS-SLCM-X0022 (LCM, X14, FlexTwin, CPU+DIMM+VRM Cold Plate Module, RoHS)	

X14 FlexTwin Landing Pages

SYS-222FT-HEA-ALC



SYS-222FT-HEA-LCC



System Information

Each system comes with a unique default password for the ADMIN user. This can be found on a sticker on the motherboard and another on the chassis service tag. If necessary, the password can be reset. For more information, please visit <https://www.supermicro.com/en/solutions/management-software/bmc-resources>

SAFETY INFORMATION

IMPORTANT: See installation instructions and safety warning before connecting system to power supply. https://www.supermicro.com/about/policies/safety_inforation.cfm

WARNING

To reduce risk of electrical shock/damage to equipment, disconnect power from server by disconnecting all power cords from electrical outlets. If any CPU socket is empty, install protective plastic CPU cap.

CAUTION

Always be sure all power supplies for this system have the same power output. If mixed power supplies are installed, the system will not operate. For more information, go to <https://www.supermicro.com/support>

MNL-2810-QRG-100

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