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- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment to an outlet on a circuit different from that to which the receiver is connected.
- Consult the authorized dealer or an experienced radio/TV technician for help.

California Best Management Practices Regulations for Perchlorate Materials: This Perchlorate warning applies only to products containing CR (Manganese Dioxide) Lithium coin cells. “Perchlorate Material-special handling may apply. See www.dtsc.ca.gov/hazardouswaste/perchlorate”

WARNING: Handling of lead solder materials used in this product may expose you to lead, a chemical known to the State of California to cause birth defects and other reproductive harm.

Manual Revision 1.0
Release Date: July 16, 2013

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Preface

This manual is written for system integrators, PC technicians and knowledgeable PC users. It provides information for the installation and use of the SUPER® X10SLD-F/X10SLD-HF motherboard.

About This Motherboard

The SUPER® X10SLD-F/X10SLD-HF supports a single Intel® Xeon E3-1200 V3 processor in an LGA 1150 (H3) socket. With the Intel® C224/C226 Express chipset built in, the X10SLD-F/X10SLD-HF motherboard supports Intel® Active Management Technology (iAMT), offering great system enhancement to 8-node Micro-Cloud server platforms. Please refer to our website (http://www.supermicro.com/products/) for processor and memory support updates. This product is intended to be installed and serviced by professional technicians.

Note: The Intel® C224 PCH is for the X10SLD-F; while the C226 is for the X10SLD-HF only.

Manual Organization

Chapter 1 describes the features, specifications and performance of the motherboard, and provides detailed information on the C224/C226 Express chipset.

Chapter 2 provides hardware installation instructions. Read this chapter when installing the processor, memory modules and other hardware components into the system. If you encounter any problems, see Chapter 3, which describes troubleshooting procedures for video, memory and system setup stored in the CMOS.

Chapter 4 includes an introduction to the BIOS, and provides detailed information on running the CMOS Setup utility.

Appendix A provides BIOS Error Beep Codes.

Appendix B lists software program installation instructions.

Appendix C contains UEFI BIOS Recovery instructions.
Conventions Used in the Manual:

Special attention should be given to the following symbols for proper installation and to prevent damage done to the components or injury to yourself:

**Warning:** Critical information to prevent damage to the components or injury to yourself.

**Important:** Important information given to ensure proper system installation or to relay safety precautions.

**Note:** Additional Information given to differentiate between models or to provide instructions for correct system setup.
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Chapter 1

Introduction

1-1 Overview

Checklist

Congratulations on purchasing your computer motherboard from an acknowledged leader in the industry. Supermicro boards are designed with the utmost attention to detail to provide you with the highest standards in quality and performance.

Please check that the following items have all been included with your motherboard. If anything listed here is damaged or missing, contact your retailer.

The following items are included in the retail box.

- One (1) Supermicro Motherboard
- One (1) Quick Reference Guide

Note: For your system to work properly, please follow the links below to download all necessary drivers/utilities and the user's manual for your motherboard.

SMCI product manuals: http://www.supermicro.com/support/manuals/

If you have any questions, please contact our support team at support@supermicro.com.
Note: All graphics shown in this manual were based upon the latest PCB Revision available at the time of publishing of the manual. The motherboard you've received may or may not look exactly the same as the graphics shown in this manual.
Important Notes to the User

- See Chapter 2 for detailed information on jumpers, I/O ports and JF1 front panel connections.

- "■" indicates the location of "Pin 1".

- Jumpers not indicated are for OEM requests and internal use only.

- When SW1 (Onboard Power LED Indicator) is on, system power is on. Unplug the power cable before installing or removing any components.
X10SLD-F/X10SLD-HF Quick Reference
### X10SLD-F/X10SLD-HF Headers/Connectors

<table>
<thead>
<tr>
<th>Connector</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Battery (VBAT)</td>
<td>Onboard Battery</td>
</tr>
<tr>
<td>IPMI_LAN</td>
<td>IPMI_Dedicated LAN</td>
</tr>
<tr>
<td>JKVM1</td>
<td>USB 2.0 (x2)/VGA (Monitor)/COM Connector for Remote Console Redirection or Remote Network Interface</td>
</tr>
<tr>
<td>JSD1</td>
<td>SATA DOM (Device_On_Module) Power Connector</td>
</tr>
<tr>
<td>JTPM1</td>
<td>Trusted Platform Module/Port 80 Connector</td>
</tr>
<tr>
<td>Micro-LP PCI-E Slot</td>
<td>SMC-Proprietary Micro Low-Profile (LP) PCI-Express 3.0 x8 Slot</td>
</tr>
<tr>
<td>(CPU) Slot1 PCI-E Slot</td>
<td>PCI-Express 3.0 x8 Slot 1 supported by the CPU</td>
</tr>
<tr>
<td>I-SATA4</td>
<td>(Intel PCH) Serial_Link SATA Port4</td>
</tr>
<tr>
<td>(S-)SATA/SAS0-3</td>
<td>Serial_Link SATA/SAS Ports 0-3 (SATA3/SAS2 is only for add-on cards)</td>
</tr>
<tr>
<td>S-SGPIO1</td>
<td>Serial Link General Purpose Input/Output Header 1</td>
</tr>
<tr>
<td>SW1</td>
<td>Power Switch/LED Indicator</td>
</tr>
<tr>
<td>UID</td>
<td>Unit Identifier (UID) Button (JUIDB1)</td>
</tr>
<tr>
<td>USB0 (3.0)</td>
<td>Internal Type A USB 3.0 Port 0</td>
</tr>
</tbody>
</table>

### X10SLD-F/X10SLD-HF Jumpers

<table>
<thead>
<tr>
<th>Jumper</th>
<th>Description</th>
<th>Default</th>
</tr>
</thead>
<tbody>
<tr>
<td>JBT1</td>
<td>CMOS Clear</td>
<td>(See Chpt. 2)</td>
</tr>
<tr>
<td>JPG1</td>
<td>VGA Enable</td>
<td>Pins 1-2 (Enabled)</td>
</tr>
<tr>
<td>JPME1</td>
<td>ME Recovery</td>
<td>Pins 1-2 (Normal)</td>
</tr>
<tr>
<td>JPME2</td>
<td>Manufacture Mode Select</td>
<td>Pins 1-2 (Normal)</td>
</tr>
<tr>
<td>JWD1</td>
<td>Watch Dog Enable</td>
<td>Pins 1-2 (NMI)</td>
</tr>
</tbody>
</table>

### X10SLD-F/X10SLD-HF LED Indicators

<table>
<thead>
<tr>
<th>LED</th>
<th>Description</th>
<th>Color/State</th>
<th>Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>LE5</td>
<td>BMC Heartbeat LED</td>
<td>Green: Blinking</td>
<td>BMC Functions Normally</td>
</tr>
<tr>
<td>LED5</td>
<td>Power Fail/Fan Fail/Overheat LED</td>
<td>Red: On</td>
<td>Power Fail, Fan Fail or Overheating</td>
</tr>
<tr>
<td>LED6</td>
<td>UID LED</td>
<td>Blue: On</td>
<td>Unit Identified</td>
</tr>
</tbody>
</table>
## Motherboard Features

<table>
<thead>
<tr>
<th><strong>CPU</strong></th>
<th>Single Intel Xeon E3-1200 V3 processor in an LGA1150 (H3) socket.</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Memory</strong></td>
<td>Four (4) SDRAM slots support up to 32 GB of DDR3 Unbuffered ECC 1600/1333/1066 MHz memory</td>
</tr>
<tr>
<td><strong>DIMM sizes</strong></td>
<td>Dual-channel memory</td>
</tr>
<tr>
<td><strong>UDIMM</strong></td>
<td>1 GB, 2 GB, 4GB, and 8GB</td>
</tr>
<tr>
<td><strong>Chipset</strong></td>
<td>Intel® C224/C226 Express</td>
</tr>
<tr>
<td><strong>Expansion Slots</strong></td>
<td>One (1) Micro Low-Profile (LP) SMC-Proprietary PCI-Exp 3.0 x8 Slot, One (1) (CPU) PCI-Exp 3.0 x8 Slot (Slot 1),</td>
</tr>
<tr>
<td><strong>I/O Devices</strong></td>
<td><strong>SATA/SAS Connections</strong></td>
</tr>
<tr>
<td></td>
<td>SATA 3.0 (for the X10SLD-HF), SATA 2.0 (for the X10SLD-F)</td>
</tr>
<tr>
<td></td>
<td>• One (1) SATA w/DOM Power Connector from Intel PCH (I-SATA4) for the X10SLD-HF</td>
</tr>
<tr>
<td></td>
<td>• One (1) SATA 2.0 (3Gb/s) w/DOM Power Connector from Intel PCH (I-SATA4) for the X10SLD-F</td>
</tr>
<tr>
<td></td>
<td>SATA 3.0 (6Gb/s) Two (2) SATA 3.0 (6Gb/s) on the backplane from Intel PCH (C224/C226 Exp.)</td>
</tr>
<tr>
<td></td>
<td>SATA 3.0/SAS 2.0 (6Gb/s) Four (4) SATA 3.0 (6Gb/s) on the backplane from an add-on card (AOC) (S-SATA/SAS 0-3)</td>
</tr>
<tr>
<td><strong>RAID Settings</strong></td>
<td>RAID 0, 1, 5, 10 (MS Windows®) RAID 0, 1, 10 (Linux)</td>
</tr>
<tr>
<td><strong>Internal USB Devices</strong></td>
<td>One (1) USB 3.0 Type A header USB0 (3.0)</td>
</tr>
<tr>
<td><strong>KVM Connector on the IO Backplane</strong></td>
<td>One (1) USB (x2)/VGA (Monitor)/COM connector</td>
</tr>
<tr>
<td><strong>BMC Controller</strong></td>
<td>AST 2400 BMC controller for IPMI and graphics support</td>
</tr>
<tr>
<td>IPMI_LAN on the IO Backplane</td>
<td>One (1) IPMI-Dedicated LAN for remote console interface</td>
</tr>
<tr>
<td>------------------------------</td>
<td>-----------------------------------------------------</td>
</tr>
<tr>
<td><strong>Super I/O</strong></td>
<td></td>
</tr>
<tr>
<td>Nuvoton NCT6776D</td>
<td></td>
</tr>
<tr>
<td>BIOS</td>
<td>128 Mb AMI BIOS® SPI Flash BIOS</td>
</tr>
<tr>
<td></td>
<td>Play and Plug (PnP0, DMI 2.3, PCI 2.3, ACPI 1.0/2.0/3.0, USB Keyboard and SMBIOS 2.5</td>
</tr>
<tr>
<td><strong>Power Configuration</strong></td>
<td>ACPI/ACPM Power Management</td>
</tr>
<tr>
<td></td>
<td>Main Switch Override Mechanism</td>
</tr>
<tr>
<td></td>
<td>Power-on mode for AC power recovery</td>
</tr>
<tr>
<td><strong>PC Health Monitoring</strong></td>
<td><strong>CPU Monitoring</strong></td>
</tr>
<tr>
<td></td>
<td>Onboard voltage monitors for CPU core, +3.3V, +5V, +12V, +3.3V Stdby, +5V Stdby, VBAT, Memory</td>
</tr>
<tr>
<td></td>
<td>CPU 3-phase switching voltage regulator</td>
</tr>
<tr>
<td></td>
<td>CPU/System overheat LED and control</td>
</tr>
<tr>
<td></td>
<td>CPU Thermal Trip support</td>
</tr>
<tr>
<td></td>
<td>Thermal Monitor 2 (TM2) support</td>
</tr>
<tr>
<td></td>
<td>vPro 9.0/AMT 9.0 support</td>
</tr>
<tr>
<td><strong>System Management</strong></td>
<td>PECI (Platform Environment Configuration Interface) 2.0 support</td>
</tr>
<tr>
<td></td>
<td>System resource alert via SuperDoctor® III</td>
</tr>
<tr>
<td></td>
<td>Intel Node Manager support</td>
</tr>
<tr>
<td></td>
<td>SuperDoctor® III, Watch Dog, NMI</td>
</tr>
<tr>
<td><strong>CD Utilities</strong></td>
<td>BIOS flash upgrade utility</td>
</tr>
<tr>
<td><strong>Other</strong></td>
<td>ROHS 6/6 (Full Compliance, Lead Free)</td>
</tr>
<tr>
<td><strong>Dimensions</strong></td>
<td>Proprietary form factor (15.95” x 4.75”) (405.13 mm x 120.84 mm)</td>
</tr>
</tbody>
</table>
System Block Diagram

Note: This is a general block diagram and may not exactly represent the features on your motherboard. See the Motherboard Features pages for the actual specifications of each motherboard.
Chapter 1: Introduction

1-2 Chipset Overview

The X10SLD-F/X10SLD-HF supports a single Intel® Xeon E3-1200 V3 processor in the LGA 1150 (H3) Socket. Built upon the functionality and the capability of the C224/C226 Express chipset, the motherboard provides substantial enhancement to system performance and storage capability for high performance platforms in a sleek package.

The high-speed Direct Media Interface (DMI) featured in the Intel C224/C226 Express chipset supports high-speed Direct Media Interface (DMI) for chip-to-chip true isochronous communication, providing up to 4 Gb/s of software-transparent data transfer rate on each read/write direction. In addition, the X10SLD-F/X10SLD-HF also features a TCO timer which allows the system to recover from a software/hardware lock and perform tasks, including Function Disable and Intruder Detect.

Intel C224/C226 Express Chipset Features

- Direct Media Interface (up 16 or 20 Gb/s transfer, Full Duplex)
- Intel® Matrix Storage Technology and Intel Rapid Storage Technology
- Dual NAND Interface
- Intel I/O Virtualization (VT-d) Support
- Intel Trusted Execution Technology Support
- PCI Express 3.0 Interface (up to 8.0 GT/s)
- SATA Controller (up to 6Gb/sec)
- Advanced Host Controller Interface (AHCI)
- Intel Active Management Technology (iAMT) 9.0 and vPRO 9.0 support
- Intel Node Manager support
1-3 Special Features

Recovery from AC Power Loss

Basic I/O System (BIOS) provides a setting for you to determine how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off, (in which case you must press the power switch to turn it back on), or for it to automatically return to a power-on state. See the Advanced BIOS Setup section to change this setting. The default setting is Last State.

1-4 PC Health Monitoring

This section describes the PC health monitoring features of the board. All have an onboard System Hardware Monitoring chip that supports PC health monitoring. An onboard voltage monitor will scan these onboard voltages continuously: CPU Vcore, 12V, 3.3V, 3.3VSB, 5VSB, and Battery voltages. Once a voltage becomes unstable, a warning is given, or an error message is sent to the screen.

Fan Status Monitor with Firmware Control

PC health monitoring supported by IPMI can check the RPM status of the cooling fans. The onboard CPU and chassis fans are controlled by Thermal Management via IPMI Firmware.

Environmental Temperature Control

The thermal control sensor monitors the CPU temperature in real time and will turn on the thermal control fan whenever the CPU temperature exceeds a user-defined threshold. The overheat circuitry runs independently from the CPU. Once the thermal sensor detects that the CPU temperature is too high, it will automatically turn on the thermal fans to prevent the CPU from overheating. The onboard chassis thermal circuitry can monitor the overall system temperature and alert the user when the chassis temperature is too high.

Note: To avoid possible system overheating, please be sure to provide adequate airflow to your system.

System Resource Alert

This feature is available when the system is used with SuperDoctor III in the Windows OS environment or used with SuperDoctor II in Linux. SuperDoctor is used to notify the user of certain system events. For example, you can also
configure SuperDoctor to provide you with warnings when the system temperature, CPU temperatures, voltages and fan speeds go beyond predefined thresholds.

1-5 ACPI Features

ACPI stands for Advanced Configuration and Power Interface. The ACPI specification defines a flexible and abstract hardware interface that provides a standard way to integrate power management features throughout a PC system, including its hardware, operating system and application software. This enables the system to automatically turn on and off peripherals such as CD-ROMs, network cards, hard disk drives and printers.

In addition to enabling operating system-directed power management, ACPI also provides a generic system event mechanism for Plug and Play, and an operating system-independent interface for configuration control. ACPI leverages the Plug and Play BIOS data structures, while providing a processor architecture-independent implementation that is compatible with Windows 7, Windows 8, and Windows 2008 Operating Systems.

1-6 Power Supply

As with all computer products, a stable power source is necessary for proper and reliable operation. It is even more important for processors that have high CPU clock rates.

This motherboard accommodates SMC-proprietary power supplies. To provide adequate power to your system, be sure to use power supplies recommended by Supermicro for your system.
Notes
Chapter 2

Installation

2-1 Standardized Warning Statements

The following statements are industry-standard warnings, provided to warn the user of situations which have the potential for bodily injury. Should you have questions or experience difficulty, contact Supermicro's Technical Support department for assistance. Only certified technicians should attempt to install or configure components. Read this section in its entirety before installing or configuring components in the Supermicro chassis.

Battery Handling

Warning!

There is a danger of explosion if the battery is replaced incorrectly. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer’s instructions.

電池の取り扱い

電池交換が正しく行われなかった場合、破裂の危険性があります。交換する電池はメーカーが推奨する型、または同等のものを使用下さい。使用済電池は製造元の指示に従って処分して下さい。

警告

电池更换不当会有爆炸危险。请只使用同类电池或制造商推荐的功能相当的电池更换原有电池。请按制造商的说明处理废旧电池。

警告

電池更換不當會有爆炸危險。請使用製造商建議之相同或功能相當的電池更換原有電池。請按照製造商的說明指示處理廢棄電池。

Warnung

Attention

Danger d'explosion si la pile n’est pas remplacée correctement. Ne la remplacer que par une pile de type semblable ou équivalent, recommandée par le fabricant. Jeter les piles usagées conformément aux instructions du fabricant.

¡Advertencia!

Existe peligro de explosión si la batería se reemplaza de manera incorrecta. Reemplazar la batería exclusivamente con el mismo tipo o el equivalente recomendado por el fabricante. Desechar las baterías gastadas según las instrucciones del fabricante.

경고!

배터리가 올바르게 교체되지 않으면 폭발의 위험이 있습니다. 기존 배터리와 동일하거나 제조사에서 권장하는 동등한 종류의 배터리로만 교체해야 합니다. 제조사의 안내에 따라 사용된 배터리를 처리하여 주십시오.

Waarschuwing

Er is ontploffingsgevaar indien de batterij verkeerd vervangen wordt. Vervang de batterij slechts met hetzelfde of een equivalent type die door de fabrikant aanbevolen wordt. Gebruikte batterijen dienen overeenkomstig fabrieksvoorschriften afgevoerd te worden.
Product Disposal

Warning!
Ultimate disposal of this product should be handled according to all national laws and regulations.

製品の廃棄
この製品を廃棄処分する場合、国の関係する全ての法律・条例に従い処理する必要があります。

警告
本产品的废弃处理应根据所有国家的法律和规章进行。

Warnung
Die Entsorgung dieses Produkts sollte gemäß allen Bestimmungen und Gesetzen des Landes erfolgen.

¡Advertencia!
Al deshacerse por completo de este producto debe seguir todas las leyes y reglamentos nacionales.

Attention
La mise au rebut ou le recyclage de ce produit sont généralement soumis à des lois et/ou directives de respect de l'environnement. Renseignez-vous auprès de l'organisme compétent.

シロクプムオリ
警告！
シロクプスフミオウは、国の関係する全ての法律に従って処理する必要があります。

주의
이 제품을 폐기할 때는 모든 국가의 법률 및 규정에 따라 처리해야 합니다.
2-2 Static-Sensitive Devices

Electrostatic-Discharge (ESD) can damage electronic components. To avoid damaging your system board, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

Precautions

• Use a grounded wrist strap designed to prevent static discharge.

• Touch a grounded metal object before removing the board from the antistatic bag.

• Handle the board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.

• When handling chips or modules, avoid touching their pins.

• Put the motherboard and peripherals back into their antistatic bags when not in use.

• For grounding purposes, make sure your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.

• Use only the correct type of onboard CMOS battery. Do not install the onboard battery upside down to avoid possible explosion.

Unpacking

The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the board, make sure that the person handling it is static protected.
2-3 Processor and Heatsink Installation

Warning: When handling the processor package, avoid placing direct pressure on the label area of the fan.

Important:

- Always connect the power cord last, and always remove it before adding, removing or changing any hardware components. Please install the processor into the CPU socket before you install the CPU heatsink.

- If you are buying a CPU separately, make sure that you use an Intel-certified multidirectional heatsink only.

- Please install the system board into the chassis before installing the CPU heatsink.

- When you receive a system board without a processor pre-installed, make sure that the plastic CPU socket cap is in place, and none of the socket pins are bent; otherwise, contact your retailer immediately.

- Refer to the Supermicro website for CPU updates and support.

Installing the LGA1150 Processor

1. Gently press down the load plate handle and push it outward (to the right side) to unlock it.

2. Once the load plate handle is unlocked, gently lift the handle to open the load plate.
1. Once the load plate is open, use your thumb and your index finger to hold the CPU at the north center edge and the south center edge of the CPU.

![North Center Edge and South Center Edge](image)

2. Align the CPU keys, which are the semicircular cutouts on the sides of the CPU (shown below), against the socket keys, the semicircular notches on the sides of the CPU socket. Align CPU Pin 1, the triangle at the bottom left of the CPU, against Pin 1 of the socket, the triangle marker at the bottom left of the CPU socket.

![CPU Keys and Socket Keys](image)

3. Once they are aligned, carefully lower the CPU straight down into the socket. (To avoid damaging the CPU or the socket, do not drop the CPU on the socket. Do not rub the CPU against the surface or against any pins of the socket.)

4. With the CPU seated inside the socket, inspect the four corners of the CPU to ensure that it is properly installed.
5. Once the CPU is properly installed, use your thumb to gently push the load plate handle down to the handle lock and lock it.

6. When the CPU is securely locked into the CPU socket, the plastic cap will be automatically loosened from the load plate. Use your thumb and index finger to remove the plastic cap from the socket.

**Warning:** You can install the CPU inside the socket only in one direction. Make sure that it is properly inserted into the CPU socket before closing the load plate. If it doesn’t close properly, do not force it, as that may damage your CPU. Instead, open the load plate again and check whether the CPU is properly aligned and securely seated inside the socket.
Installing a Passive CPU Heatsink

1. Do not apply thermal grease to the heatsink or the CPU die; the required amount has already been applied.

2. Place the heatsink on top of the CPU so that the four mounting holes are aligned with those on the motherboard and the underlying heatsink bracket.

3. Screw in two diagonal screws (the #1 and #2 screws in the figure below) until just snug. To avoid possible damage to the CPU, do not over tighten the screws.

4. Finish the installation by fully tightening all four screws.

Recommended Supermicro heatsink: SNK-P0046P heatsink with BKT-0028L bottom bracket
Removing the Heatsink

Warning: We do not recommend that the CPU or the heatsink be removed. However, if you do need to uninstall the heatsink, please follow the instructions below to remove the heatsink to avoid damaging the CPU or the CPU socket.

1. Unscrew the heatsink screws from the motherboard in the sequence as shown in the figure below.

2. Gently wriggle the heatsink to loosen it from the CPU. (Do not use excessive force)

3. Once the CPU is loosened, remove the CPU from the CPU socket.

4. Clean the surface of the CPU and the heatsink, removing any thermal grease. Reapply the proper amount of fresh thermal grease on the surface before reinstalling the CPU and the heatsink.
2-4 Installing DDR3 Memory

*Note:* Check the Supermicro website for recommended memory modules.

**CAUTION**

Exercise extreme care when installing or removing DIMM modules to prevent any possible damage.

**DIMM Installation**

1. Insert the desired number of DIMMs into the memory slots, starting with DIMMA2 (Channel A, Slot 2, see the next page for the location). For the system to work properly, please use the memory modules of the same type and speed in the same motherboard.

2. Push the release tabs outwards on both ends of the DIMM slot to unlock it.

3. Align the key of the DIMM module with the receptive point on the memory slot.

4. Align the notches on both ends of the module against the receptive points on the ends of the slot.

5. Use two thumbs together to press the notches on both ends of the module straight down into the slot until the module snaps into place.

6. Press the release tabs to the lock positions to secure the DIMM module into the slot.
Chapter 2: Installation

Removing Memory Modules
Reverse the steps above to remove the DIMM modules from the motherboard.

Memory Support

The X10SLD-F/-HF supports up to 32GB of Unbuffered (UDIMM) DDR3 ECC 1600/1333/1066 MHz in 4 memory slots. Populating these DIMM modules with a pair of memory modules of the same type and same size will result in interleaved memory, which will improve memory performance. Please refer to the table below:

Memory Population Guidelines
Please follow the table below when populating the X10SLD-F/-HF.

<table>
<thead>
<tr>
<th>DDR3 Unbuffered ECC (UDIMM) Memory</th>
</tr>
</thead>
<tbody>
<tr>
<td>DIMM Slots per Channel</td>
</tr>
<tr>
<td>------------------------</td>
</tr>
<tr>
<td>2</td>
</tr>
<tr>
<td>2</td>
</tr>
</tbody>
</table>

Notes

- Be sure to use memory modules of the same type, speed, and frequency on the same motherboard. Mixing of memory modules of different types and speeds may damage the motherboard.

- Due to memory allocation to system devices, the amount of memory that remains available for operational use will be reduced when 4 GB of RAM is used. The reduction in memory availability is disproportional. See the following table for details.
For Microsoft Windows users: Microsoft implemented a design change in the Windows XP with Service Pack 2 (SP2) and Windows Vista. This change is specific to the behavior of Physical Address Extension (PAE) mode which improves driver compatibility. For more information, please read the following article at Microsoft’s Knowledge Base website at: http://support.microsoft.com/kb/888137.

### Possible System Memory Allocation & Availability

<table>
<thead>
<tr>
<th>System Device</th>
<th>Size</th>
<th>Physical Memory Remaining (-Available) (4 GB Total System Memory)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Firmware Hub flash memory (System BIOS)</td>
<td>1 MB</td>
<td>3.99</td>
</tr>
<tr>
<td>Local APIC</td>
<td>4 KB</td>
<td>3.99</td>
</tr>
<tr>
<td>Area Reserved for the chipset</td>
<td>2 MB</td>
<td>3.99</td>
</tr>
<tr>
<td>I/O APIC (4 Kbytes)</td>
<td>4 KB</td>
<td>3.99</td>
</tr>
<tr>
<td>PCI Enumeration Area 1</td>
<td>256 MB</td>
<td>3.76</td>
</tr>
<tr>
<td>PCI Express (256 MB)</td>
<td>256 MB</td>
<td>3.51</td>
</tr>
<tr>
<td>PCI Enumeration Area 2 (if needed) -Aligned on 256-MB boundary-</td>
<td>512 MB</td>
<td>3.01</td>
</tr>
<tr>
<td>VGA Memory</td>
<td>16 MB</td>
<td>2.85</td>
</tr>
<tr>
<td>TSEG</td>
<td>1 MB</td>
<td>2.84</td>
</tr>
<tr>
<td>Memory available to OS and other applications</td>
<td></td>
<td>2.84</td>
</tr>
</tbody>
</table>
2-5 Motherboard Installation

All motherboards have standard mounting holes to fit different types of chassis. Make sure that the locations of all the mounting holes for both motherboard and chassis match. Although a chassis may have both plastic and metal mounting fasteners, metal ones are highly recommended because they ground the motherboard to the chassis. Make sure that the metal standoffs click in or are screwed in tightly. Then use a screwdriver to secure the motherboard onto the motherboard tray.

Caution: 1) To avoid damaging the motherboard and its components, please do not use a force greater than 8 lb/inch on each mounting screw during motherboard installation. 2) Some components are very close to the mounting holes. Please take precautionary measures to avoid damaging these components when installing the motherboard to the chassis.
Installing the Motherboard

1. Install the I/O shield into the back of the chassis.

2. Locate the mounting holes on the motherboard. (See the previous page.)

3. Locate the matching mounting holes on the chassis. Align the mounting holes on the motherboard against the mounting holes on the chassis.

4. Install standoffs in the chassis as needed.

5. Install the motherboard into the chassis carefully to avoid damaging other motherboard components.

6. Using the Phillips screwdriver, insert a Phillips head #6 screw into a mounting hole on the motherboard and its matching mounting hole on the chassis.

7. Repeat Step 5 to insert #6 screws into all mounting holes.

8. Make sure that the motherboard is securely placed in the chassis.

Note: Images displayed are for illustration only. Your chassis or components might look different from those shown in this manual.
2-6 Connectors/IO Ports

The I/O ports are color coded in conformance with the PC 99 specification. See the figure below for the colors and locations of the various I/O ports.

Backplane I/O Panel

<table>
<thead>
<tr>
<th>Backplane I/O Panel</th>
<th>A. USB2.0 (x2)/VGA/COM (JKVM1)</th>
<th>B. IPMI_LAN</th>
<th>C. Power Switch (SW1)</th>
<th>D. UID Button &amp; UID LED</th>
</tr>
</thead>
</table>

A.

B.

C.

D.
Unit Identifier Switch/UID LED Indicators

A Unit Identifier (UID) button and a LED Indicator are located on the motherboard. The UID button is located at JUIDB1, and LED is located at LED6 on the backplane. When you press the UID button, the UID LED Indicator will be turned on. Press the UID button again to turn off the LED Indicator. The UID Indicator provides easy identification of a system unit that may be in need of service.

**Note:** UID can also be triggered via IPMI on the motherboard. For more information on IPMI, please refer to the IPMI User's Guide posted on our website @http://www.supermicro.com.

Power Switch/LED

A power switch/LED indicator is located next to the UID button/LED on the backplane of the motherboard. Use this switch to power on or power off the system.
IPMI_LAN Port

An IPMI_Dedicated Ethernet port is located on the I/O Backpanel to provide network connection via IPMI interface. This port accepts a RJ45 type cable.

**Note:** Please refer to the LED Indicator Section for LAN LED information.

USB 2.0 (x2)/VGA/COM Connector

JKVM1 supports USB 2.0 (x2)/VGA/COM connections on the I/O backplane. See the layout below for the location.

### LAN Port Pin Definition

<table>
<thead>
<tr>
<th>Pin#</th>
<th>Definition</th>
<th>Pin#</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>TD0+</td>
<td>10</td>
<td>TD3+</td>
</tr>
<tr>
<td>2</td>
<td>TD0-</td>
<td>11</td>
<td>TD3-</td>
</tr>
<tr>
<td>3</td>
<td>NC</td>
<td>12</td>
<td>NC</td>
</tr>
<tr>
<td>4</td>
<td>TD1+</td>
<td>13</td>
<td>Ground</td>
</tr>
<tr>
<td>5</td>
<td>TD1-</td>
<td>14</td>
<td>Act LED (Yellow)</td>
</tr>
<tr>
<td>6</td>
<td>NC</td>
<td>15</td>
<td>Link 1000 LED (Amber)</td>
</tr>
<tr>
<td>7</td>
<td>TD2+</td>
<td>16</td>
<td>Link 100 LED (Green)</td>
</tr>
<tr>
<td>8</td>
<td>TD2-</td>
<td>17</td>
<td>Ground</td>
</tr>
<tr>
<td>9</td>
<td>NC</td>
<td>18</td>
<td>Ground</td>
</tr>
<tr>
<td></td>
<td></td>
<td>19</td>
<td>Ground</td>
</tr>
<tr>
<td></td>
<td></td>
<td>20</td>
<td>Ground</td>
</tr>
</tbody>
</table>

(NC= No Connection)

A. IPMI LAN

B. JKVM1 Port
2-7 Connecting Cables

This section provides brief descriptions and pin-out definitions for onboard headers and connectors. Be sure to use the correct cable for each header or connector.

DOM PWR Connector (JSD1)

The Disk-On-Module (DOM) power connector, located at JSD1, provides 5V (Gen1/Gen) power to a solid state DOM storage device connected to one of the SATA ports. See the table on the right for pin definitions.

<table>
<thead>
<tr>
<th>Pin#</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>5V</td>
</tr>
<tr>
<td>2</td>
<td>Ground</td>
</tr>
<tr>
<td>3</td>
<td>Ground</td>
</tr>
</tbody>
</table>

A. JSD1
S-SGPIO 1 Header

A SGPIO (Serial-Link General Purpose Input/Output) header is located on the motherboard. S-SGPIO1 supports Serial_Link (SATA/SAS) interface. See the table on the right for pin definitions.

<table>
<thead>
<tr>
<th>Pin#</th>
<th>Definition</th>
<th>Pin</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>NC</td>
<td>2</td>
<td>NC</td>
</tr>
<tr>
<td>3</td>
<td>Ground</td>
<td>4</td>
<td>Data</td>
</tr>
<tr>
<td>5</td>
<td>Load</td>
<td>6</td>
<td>Ground</td>
</tr>
<tr>
<td>7</td>
<td>Clock</td>
<td>8</td>
<td>NC</td>
</tr>
</tbody>
</table>

A. S-SGPIO1
Universal Serial Bus (USB)

A Type A USB0 (3.0) is located next to the CPU. Connect a cable to the header to provide internal USB support. See the layout below for the location.

<table>
<thead>
<tr>
<th>Pin#</th>
<th>Signals</th>
<th>Pin#</th>
<th>Signals</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>VBUS</td>
<td>8</td>
<td>StdA_SSTX-</td>
</tr>
<tr>
<td>2</td>
<td>D-</td>
<td>9</td>
<td>StdA_SSTX+</td>
</tr>
<tr>
<td>3</td>
<td>D+</td>
<td>10</td>
<td>Ground</td>
</tr>
<tr>
<td>4</td>
<td>Ground</td>
<td>11</td>
<td>Ground</td>
</tr>
<tr>
<td>5</td>
<td>StdA_SSRX-</td>
<td>12</td>
<td>Ground</td>
</tr>
<tr>
<td>6</td>
<td>StdA_SSRX+</td>
<td>13</td>
<td>Ground</td>
</tr>
<tr>
<td>7</td>
<td>GND_DRAIN</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

A. USB0 (3.0)
TPM Header/Port 80 Header

A Trusted Platform Module/Port 80 header is located at JTPM1 to provide TPM support and Port 80 connection. Use this header to enhance system performance and data security. See the table on the right for pin definitions.

<table>
<thead>
<tr>
<th>Pin #</th>
<th>Definition</th>
<th>Pin #</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>LCLK</td>
<td>2</td>
<td>GND</td>
</tr>
<tr>
<td>3</td>
<td>LFRAME#</td>
<td>4</td>
<td>(KEY)</td>
</tr>
<tr>
<td>5</td>
<td>LRESET#</td>
<td>6</td>
<td>+5V (X)</td>
</tr>
<tr>
<td>7</td>
<td>LAD 3</td>
<td>8</td>
<td>LAD 2</td>
</tr>
<tr>
<td>9</td>
<td>+3.3V</td>
<td>10</td>
<td>LAD1</td>
</tr>
<tr>
<td>11</td>
<td>LAD0</td>
<td>12</td>
<td>GND</td>
</tr>
<tr>
<td>13</td>
<td>RSV0</td>
<td>14</td>
<td>RSV1</td>
</tr>
<tr>
<td>15</td>
<td>+3V_DUAL</td>
<td>16</td>
<td>SERIRQ</td>
</tr>
<tr>
<td>17</td>
<td>GND</td>
<td>18</td>
<td>CLKRUN# (X)</td>
</tr>
<tr>
<td>19</td>
<td>RSV2</td>
<td>20</td>
<td>RSV3</td>
</tr>
</tbody>
</table>

A. JTPM1
2-8 Jumper Settings

Explanation of Jumpers

To modify the operation of the motherboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board.

Note: On two-pin jumpers, "Closed" means the jumper is on, and "Open" means the jumper is off the pins.

Clear CMOS (JBT1)

JBT1 is used to clear the saved system setup configuration stored in the NVRAM in the SPI Flash chip. To clear the contents of the CMOS, completely shut down the system, remove the AC power cord and then short JBT1 with a jumper. Remove the jumper before powering on the system again. This will erase all user settings and revert everything to their factory-set defaults.

A. Clear CMOS
VGA Enable

Jumper JPG1 allows the user to enable the onboard VGA connector. The default setting is 1-2 to enable the connection. See the table on the right for jumper settings.

<table>
<thead>
<tr>
<th>Jumper Setting</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>1-2</td>
<td>Enabled (Default)</td>
</tr>
<tr>
<td>2-3</td>
<td>Disabled</td>
</tr>
</tbody>
</table>
**ME Recovery**

Set Jumper JPME1 to select ME Firmware Recovery mode, which will limit system resource for essential function use only without putting restrictions on power use. In the single operation mode, online upgrade will be available via Recovery mode. See the table on the right for pin definitions.

**Manufacturer Mode Select**

Close this jumper (JPME2) to bypass SPI flash security and force the system to use the Manufacturer mode which will allow the user to flash the system firmware from a host server to modify system settings. See the table on the right for jumper settings.

---

**ME Recovery Jumper Settings**

<table>
<thead>
<tr>
<th>Jumper Setting</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>1-2</td>
<td>Normal (Default)</td>
</tr>
<tr>
<td>2-3</td>
<td>ME Recovery</td>
</tr>
</tbody>
</table>

**ME Mode Select Jumper Settings**

<table>
<thead>
<tr>
<th>Jumper Setting</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>1-2</td>
<td>Normal (Default)</td>
</tr>
<tr>
<td>2-3</td>
<td>Manufacture Mode</td>
</tr>
</tbody>
</table>

---

A. JPME1
B. JPME2
Watch Dog Enable/Disable

Watch Dog (JWD1) is a system monitor that can reboot the system when a software application hangs. Close Pins 1-2 to generate a non-maskable interrupt signal for the application that hangs. Close Pins 2-3 to reset the system if an application hangs. See the table on the right for jumper settings. Watch Dog must also be enabled in the BIOS.

<table>
<thead>
<tr>
<th>Watch Dog Jumper Settings</th>
</tr>
</thead>
<tbody>
<tr>
<td>Jumper Setting</td>
</tr>
<tr>
<td>Pins 1-2</td>
</tr>
<tr>
<td>Pins 2-3</td>
</tr>
<tr>
<td>Open</td>
</tr>
</tbody>
</table>

A. Watch Dog Enable
2-9 Onboard Indicators

**IPMI_LAN LEDs**

An IPMI Dedicated LAN is also located above the Backplane on the motherboard. The yellow LED on the right indicates activity, while the green LED on the left indicates the speed of the connection. See the tables at right for more information.

<table>
<thead>
<tr>
<th>IPMI LAN Link LED (Left) &amp; Activity LED (Right)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Color/State</td>
</tr>
<tr>
<td>Link (Left)</td>
</tr>
<tr>
<td>Activity (Right)</td>
</tr>
</tbody>
</table>

**BMC Heartbeat LED**

A BMC Heartbeat LED is located at LE5 on the motherboard. When LE5 is blinking, BMC functions normally. See the table at right for more information.

<table>
<thead>
<tr>
<th>BMC Heartbeat LED Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>Color/State</td>
</tr>
<tr>
<td>Green: Blinking</td>
</tr>
</tbody>
</table>

A. IPMI LAN LEDs

B. BMC Heartbeat LED
PWR Fail/Fan Fail/OH LED

A Power Fail/Fan Fail/Overheat LED is located at LED5 on the motherboard. See the table at right for more information.

<table>
<thead>
<tr>
<th>Color/State</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>Red: On</td>
<td>Power Fail, Fan Fail or Overheating</td>
</tr>
</tbody>
</table>

A. PWR Fail/Fan Fail/OH LED
2-10 SATA Connections

SATA/SAS Connections

I-SATA4 from Intel PCH supports SATA 2.0 on the X10SLD-F and SATA 3.0 on the X10SLD-HF. Additionally, SATA 3.0/SAS 2.0 connectors (S-SATA/SAS 0-3) are also located on the X10SLD-F/HF. These Serial Link connections provide faster data transmission than legacy Parallel ATA. See the table on the right for pin definitions.

**Note:**
1. I-SATA4 is used as an internal SATA2 connector for SATA DOM on the X10SLD-F.
2. SATA3/SAS2 is used for storage add-on cards.

<table>
<thead>
<tr>
<th>SATA Connectors Pin Definitions</th>
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<td>Pin#</td>
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</table>

A. S-SATA/SAS0
B. S-SATA/SAS1
C. S-SATA/SAS2
D. S-SATA/SAS3
E. I-SATA #4 (SATA 2.0 for X10SLD-F), (SATA 3.0 for X10SLD-HF).
3-1  Troubleshooting Procedures

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the ‘Technical Support Procedures’ and/or ‘Returning Merchandise for Service’ section(s) in this chapter. Always disconnect the AC power cord before adding, changing or installing any hardware components.

Before Power On
1. Make sure that the Standby PWR LED is not on. (Note: If it is on, the onboard power is on. Be sure to unplug the power cable before installing or removing the components.)

2. Make sure that there are no short circuits between the motherboard and chassis.

3. Disconnect all ribbon/wire cables from the motherboard, including those for the keyboard and mouse. Also, be sure to remove all add-on cards.

4. Install a CPU and heatsink (-be sure that it is fully seated) and then connect the chassis speaker and the power LED to the motherboard. Check all jumper settings as well.

No Power
1. Make sure that there are no short circuits between the motherboard and chassis.

2. Make sure that all jumpers are set to their default positions.

3. Check if the 115V/230V switch on the power supply is properly set.

4. Turn the power switch on and off to test the system.

5. The battery on your motherboard may be old. Check to make sure that it still supplies ~3VDC. If it does not, replace it with a new one.
No Video
1. If the power is on, but you have no video--in this case, you will need to remove all the add-on cards and cables first.

2. Use the speaker to determine if any beep codes exist. (Refer to Appendix A for details on beep codes.)

3. Remove all memory modules and turn on the system. (If the alarm is on, check the specifications of memory modules, reset the memory or try a different one.)

Memory Errors
1. Make sure that the DIMM modules are properly installed and fully seated in the slots.

2. You should be using unbuffered ECC DDR3 (1.5V/1.35V) 1600/1333/1066 MHz memory recommended by the manufacturer. Also, it is recommended that you use the memory modules of the same type and speed for all DIMMs in the system. Do not use memory modules of different sizes, different speeds and different types on the same motherboard.

3. Check for bad DIMM modules or slots by swapping modules between slots to see if you can locate the faulty ones.

4. Check the switch of 115V/230V power supply.

Losing the System's Setup Configuration
1. Please be sure to use a high quality power supply. A poor quality power supply may cause the system to lose CMOS setup information. Refer to Section 1-6 for details on recommended power supplies.

2. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

3. If the above steps do not fix the Setup Configuration problem, contact your vendor for repairs.
3-2 Technical Support Procedures

Before contacting Technical Support, please make sure that you have followed all the steps listed below. Also, Note that as a motherboard manufacturer, Supermicro does not sell directly to end users, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problem(s) with the specific system configuration that was sold to you.

1. Please go through the 'Troubleshooting Procedures' and 'Frequently Asked Question' (FAQ) sections in this chapter or see the FAQs on our website (http://www.supermicro.com/support/faqs/) before contacting Technical Support.

2. BIOS upgrades can be downloaded from our website at (http://www.supermicro.com/support/bios/).

   Note: Not all BIOS can be flashed. Some cannot be flashed; it depends on the boot block code of the BIOS.

3. If you've followed the instructions above to troubleshoot your system, and still cannot resolve the problem, then contact Supermicro's technical support and provide them with the following information:

   • Motherboard model and PCB revision number

   • BIOS release date/version (this can be seen on the initial display when your system first boots up)

   • System configuration

   • An example of a Technical Support form is on our website at (http://www.supermicro.com/support/contact.cfm).

4. Distributors: For immediate assistance, please have your account number ready when placing a call to our technical support department. We can be reached by e-mail at support@supermicro.com, by phone at: (408) 503-8000, option 2, or by fax at (408)503-8019.
3-3  Frequently Asked Questions

**Question:** What type of memory does my motherboard support?

**Answer:** The X10SLD-F/X10SLD-HF supports up to 32GB of unbuffered ECC DDR3 SDRAM (1.5V/1.35V, 1600/1333/1066 MHz). See Section 2-4 for details on installing memory.

**Question:** How do I update my BIOS?

**Answer:** We do NOT recommend that you upgrade your BIOS if you are not experiencing any problems with your system. Updated BIOS files are located on our website at [http://www.supermicro.com/support/bios/](http://www.supermicro.com/support/bios/). Please check our BIOS warning message and the information on how to update your BIOS on our website. Select your motherboard model and download the BIOS ROM file to your computer. Also, check the current BIOS revision to make sure that it is newer than your BIOS before downloading. You may choose the zip file or the .exe file. If you choose the zipped BIOS file, please unzip the BIOS file onto a bootable device or a USB pen/thumb drive. To flash the BIOS, run the batch file named "ami.bat" with the new BIOS ROM file from your bootable device or USB pen/thumb drive. Use the following format:

F:\> ami.bat  BIOS-ROM-filename.xxx <Enter>

**Note:** Always use the file named “ami.bat” to update the BIOS, and insert a space between "ami.bat" and the filename. The BIOS-ROM-filename will bear the motherboard name (i.e., X10SLD-F/X10SLD-HF) and build version as the extension. For example, "X10SLD-F/X10SLD-HF.115". When completed, your system will automatically reboot.

If you choose the .exe file, please run the .exe file under Windows to create the BIOS flash floppy disk. Insert the floppy disk into the system you wish to flash the BIOS. Then, boot the system to the floppy disk. The BIOS utility will automatically flash the BIOS without any prompts. Please note that this process may take a few minutes to complete. Do not be concerned if the screen is paused for a few minutes.

When the BIOS flashing screen is completed, the system will reboot and will show “Press F1 or F2”. At this point, you will need to load the BIOS defaults. Press <F1> to go to the BIOS setup screen, and press <F3> to load the default settings. Next, press <F4> to save and exit. The system will then reboot.

**Warning:** Do not shut down or reset the system while updating the BIOS to prevent possible system boot failure!
### Important:
The SPI BIOS chip installed on this motherboard is not removable. To repair or replace a damaged BIOS chip, please send your motherboard to RMA at Supermicro for service.

**Question:** I think my BIOS is corrupted. How can I recover my BIOS?
**Answer:** Please see Appendix C-BIOS Recovery for detailed instructions.

**Question:** Why do I get an error message “IASTOR.SYS read error” and "press F6 to install Intel RAID driver" when installing Windows on my motherboard?
**Answer:** To solve this issue, disable the IPMI jumper. Another solution is to use a USB floppy drive instead of the onboard floppy drive. For the IPMI jumper location, please check Chapter 1.

**Question:** What is the heatsink part number for my X10SLD-F/X10SLD-HF motherboard?
**Answer:** For the 1U passive heatsink, ask for SNK-P0046P (back plate is included). For the 2U active heatsink, use SNK-P0046A4.

**Question:** Why can't I recover the BIOS even when I've followed the instructions in the user’s manual for the motherboard?
**Answer:** Please disable the IPMI jumper and try it again. For the jumper location, please check Chapter 1.

### 3-4 Battery Removal and Installation

#### Battery Removal
To remove the onboard battery, follow the steps below:

1. Power off your system and unplug your power cable.
2. Locate the onboard battery as shown below.
3. Using a tool such as a pen or a small screwdriver, push the battery lock outwardly to unlock it. Once unlocked, the battery will pop out from the holder.
4. Remove the battery.

#### Proper Battery Disposal
**Warning:** Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the
regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

**Battery Installation**

1. To install an onboard battery, follow the steps 1 & 2 above and continue below:

2. Identify the battery's polarity. The positive (+) side should be facing up.

3. Insert the battery into the battery holder and push it down until you hear a click to ensure that the battery is securely locked.

**Warning:** When replacing a battery, be sure to only replace it with the same type.

3-5  **Returning Merchandise for Service**

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. For faster service, you may also obtain RMA authorizations online (http://www.supermicro.com/support/rma/). When you return the motherboard to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton, and mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alteration, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.
Chapter 4

BIOS

4-1 Introduction

This chapter describes the AMI BIOS Setup Utility for the X10SLD-F/X10SLD-HF. The ROM BIOS is stored in a Flash EEPROM and can be easily updated. This chapter describes the basic navigation of the AMI BIOS Setup Utility setup screens.

Note: For AMI BIOS Recovery, please refer to the UEFI BIOS Recovery Instructions in Appendix C.

Starting BIOS Setup Utility

To enter the AMI BIOS Setup Utility screens, press the <Delete> key while the system is booting up.

Note: In most cases, the <Delete> key is used to invoke the AMI BIOS setup screen. There are a few cases when other keys are used, such as <F1>, <F2>, etc.

Each main BIOS menu option is described in this manual. The Main BIOS setup menu screen has two main frames. The left frame displays all the options that can be configured. Grayed-out options cannot be configured. Options in blue can be configured by the user. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it. (Note: the AMI BIOS has default text messages built in. Supermicro retains the option to include, omit, or change any of these text messages.)

The AMI BIOS Setup Utility uses a key-based navigation system called "hot keys". Most of the AMI BIOS setup utility "hot keys" can be used at any time during the setup navigation process. These keys include <F1>, <F4>, <Enter>, <ESC>, arrow keys, etc.

Note: Options printed in Bold are default settings.

How To Change the Configuration Data

The configuration data that determines the system parameters may be changed by entering the AMI BIOS Setup utility. This Setup utility can be accessed by pressing <Del> at the appropriate time during system boot.
How to Start the Setup Utility

Normally, the only visible Power-On Self-Test (POST) routine is the memory test. As the memory is being tested, press the <Delete> key to enter the main menu of the AMI BIOS Setup Utility. From the main menu, you can access the other setup screens. An AMI BIOS identification string is displayed at the left bottom corner of the screen, below the copyright message.

Warning: Do not upgrade the BIOS unless your system has a BIOS-related issue. Flashing the wrong BIOS can cause irreparable damage to the system. In no event shall Supermicro be liable for direct, indirect, special, incidental, or consequential damages arising from a BIOS update. If you have to update the BIOS, do not shut down or reset the system while the BIOS is updating. This is to avoid possible boot failure.

4-2 Main Setup

When you first enter the AMI BIOS Setup utility, you will enter the Main setup screen. You can always return to the Main setup screen by selecting the Main tab on the top of the screen. The Main BIOS Setup screen is shown below.
The following Main menu items will display:

**System Date/System Time**

Use this option to change the system date and time. Highlight *System Date* or *System Time* using the arrow keys. Enter new values through the keyboard. Press the <Tab> key or the arrow keys to move between fields. The date must be entered in Day MM/DD/YY format. The time is entered in HH:MM:SS format.

---

**Note:** The time is in the 24-hour format. For example, 5:30 P.M. appears as 17:30:00.

The following BIOS items will also be displayed:

**Supermicro X10SLD-F/HF**

**Version**

**Build Date**

**Memory Information**

**Total Memory**

This displays the total size of memory available in the system.
4-3 Advanced Setup Configurations

Use the arrow keys to select Boot Setup and press <Enter> to access the submenu items:

![Boot Setup Menu]

**Warning:** Take Caution when changing the Advanced settings. An incorrect value, a very high DRAM frequency, or an incorrect DRAM timing setting may make the system unstable. When this occurs, revert to the setting to its manufacture default setting.

▶ Boot Feature

**Quiet Boot**

This feature selects the screen display between POST messages or the OEM logo at bootup. Select Disabled to display the POST messages. Select Enabled to display the OEM logo instead of the normal POST messages. The options are **Enabled** and **Disabled**.

**AddOn ROM Display Mode**

This feature sets the display mode for the Option ROM. Select Keep Current to use the current AddOn ROM display setting. Select Force BIOS to use the Option ROM display mode set by the system BIOS. The options are **Force BIOS** and Keep Current.

**Bootup Num-Lock**

This feature selects the Power-on state for the Numlock key. The options are **Off** and **On**.
Wait For 'F1' If Error

This feature forces the system to wait until the 'F1' key is pressed if an error occurs. The options are Disabled and Enabled.

Interrupt 19 Capture

Interrupt 19 is the software interrupt that handles the boot disk function. When this item is set to Enabled, the ROM BIOS of the host adaptors will "capture" Interrupt 19 at bootup and allow the drives that are attached to these host adaptors to function as bootable disks. If this item is set to Disabled, the ROM BIOS of the host adaptors will not capture Interrupt 19, and the drives attached to these adaptors will not function as bootable devices. The options are Enabled and Disabled.

Re-try Boot

If this item is enabled, the BIOS will automatically reboot the system from a specified boot device after its initial boot failure. The options are Disabled, Legacy Boot, and EFI Boot.

Power Configuration

Watch Dog Function

If enabled, the Watch Dog Timer will allow the system to reboot when it is inactive for more than 5 minutes. The options are Enabled and Disabled.

Power Button Function

This feature controls how the system shuts down when the power button is pressed. Select 4_Seconds_Override for the user to power off the system after pressing and holding the power button for 4 seconds or longer. Select Instant Off to instantly power off the system as soon as the user presses the power button. The options are 4 Seconds Override and Instant Off.

Restore on AC Power Loss

Use this feature to set the power state after a power outage. Select Power-Off for the system power to remain off after a power loss. Select Power-On for the system power to be turned on after a power loss. Select Last State to allow the system to resume its last power state before a power loss. The options are Power-On, Stay-Off and Last State.
CPU Configuration

The following CPU information will be displayed:

- Type of CPU
- CPU Signature
- Microcode Patch
- Max (Maximum) CPU Speed
- Min (Minimum) CPU Speed
- CPU Speed
- Processor Cores
- Intel HT (Hyper-Threading) Technology
- Intel VT-x (Virtualization) Technology
- Intel SMX (Trusted Execution) Technology
- 64-bit
- EIST Technology
- CPU C3 State
- CPU C6 State
- CPU C7 State
- L1 Data Cache
- L1 Code Cache
- L2 Cache
- L3 Cache

Hyper-Threading

Select Enabled to enable Hyper-Threading support to enhance CPU performance. The options are Enabled and Disabled.
**Active Processor Cores**

This feature determines how many CPU cores will be activated for each CPU. When all is selected, all cores in the CPU will be activated. (Please refer to Intel's web site for more information.) The options are All, 1, 2, and 3.

**Limit CPUID Maximum**

Select Enabled to set the maximum CPU ID value and to boot the legacy operating systems that cannot support processors with extended CPUID functions. The options are Enabled and Disabled (for the Windows OS).

**Execute Disable Bit Capability (Available if supported by the OS & the CPU)**

Set to Enabled to enable the Execute Disable Bit which will allow the processor to designate areas in the system memory where an application code can execute and where it cannot, thus preventing a worm or a virus from flooding illegal codes to overwhelm the processor or damage the system during an attack. The default is Enabled. (Refer to Intel and Microsoft Web Sites for more information.)

**Intel® Virtualization Technology (Available when supported by the CPU)**

Select Enabled to use the Intel Virtualization Technology to allow one platform to run multiple operating systems and applications in independent partitions, creating multiple "virtual" systems in one physical computer. The options are Enabled and Disabled.

**Hardware Prefetcher (Available when supported by the CPU)**

If set to Enabled, the hardware prefetcher will prefetch streams of data and instructions from the main memory to the L2 cache to improve CPU performance. The options are Disabled and Enabled.

**Adjacent Cache Line Prefetch (Available when supported by the CPU)**

Select Enabled for the CPU to prefetch both cache lines for 128 bytes as comprised. Select Disabled for the CPU to prefetch both cache lines for 64 bytes. The options are Disabled and Enabled.

**Note:** If there is any change to this setting, you will need to reboot the system for the change to take effect. Please refer to Intel’s web site for detailed information.

**CPU AES**

Select Enable to enable Intel CPU Advanced Encryption Standard (AES) Instructions for CPU to enhance data integrity. The options are Enabled and Disabled.
EIST

EIST (Enhanced Intel SpeedStep Technology) allows the system to automatically adjust processor voltage and core frequency in an effort to reduce power consumption and heat dissipation. Please refer to Intel’s web site for detailed information. The options are Disabled and Enabled.

Turbo Mode

This feature allows processor cores to run faster than the frequency recommended by the manufacturer. The options are Disabled and Enabled. If this feature is set to Enabled, the following items will display:

**CPU Power Limit1 (Available when "Turbo Mode" is set to Enabled)**

Use this feature to set the power limit for CPU1. Use the number keys on your keyboard to enter the value. Enter 0 to use the manufacture default setting.

**CPU Power Limit1 Time (Available when "Turbo Mode" is set to Enabled)**

This item allows the user to determine how long CPU1 should operate at the power limit set by the user for the item above. Use the number keys on your keyboard to enter the value. Enter 0 to use the manufacture default setting.

**CPU Power Limit2 (Available when "Turbo Mode" is set to Enabled)**

Use this feature to set the power limit for CPU2. Use the number keys on your keyboard to enter the value. Enter 0 to use the manufacture default setting.

**DDR Power Limit1 (Available when "Turbo Mode" is set to Enabled)**

Use this feature to set the power limit for DDR1. Use the number keys on your keyboard to enter the value. Enter 0 to use the manufacture default setting.

**DDR Power Limit1 Time (Available when "Turbo Mode" is set to Enabled)**

This item allows the user to determine how long DDR1 should operate at the power limit set by the item above. Use the number keys on your keyboard to enter the value. Enter 0 to use the manufacture default setting.

**DDR Power Limit2 (Available when "Turbo Mode" is set to Enabled)**

Use this feature to set the power limit for DDR2. Use the number keys on your keyboard to enter the value. Enter 0 to use the manufacture default setting.

**1-Core Ratio Limit (Available when "Turbo Mode" is set to Enabled)**

This increases (multiplies) 1 clock speed in the CPU core in relation to the bus speed when one CPU core is active. Press "+" or "-" on your keyboard to change the value. Enter 0 to use the manufacture default setting.
2-Core Ratio Limit (Available when "Turbo Mode" is set to Enabled)

This increases (multiplies) 2 clock speeds in the CPU core in relation to the bus speed when two CPU cores are active. Press "+" or "-" on your keyboard to change the value. Enter 0 to use the manufacture default setting.

3-Core Ratio Limit (Available when "Turbo Mode" is set to Enabled)

This increases (multiplies) 3 clock speeds in the CPU core in relation to the bus speed when three CPU cores are active. Press "+" or "-" on your keyboard to change the value. Enter 0 to use the manufacture default setting.

4-Core Ratio Limit (Available when "Turbo Mode" is set to Enabled)

This increases (multiplies) 4 clock speeds in the CPU core in relation to the bus speed when four CPU cores are active. Press "+" or "-" on your keyboard to change the value. Enter 0 to use the manufacture default setting.

Energy Performance

Use this feature to select an appropriate fan setting to achieve the maximum system performance (with maximum cooling) or maximum energy efficiency (with maximum power saving). The fan speeds are controlled by the firmware management via IPMI 2.0. The options are Performance, Balanced Performance, Balanced Energy, and Energy Efficient.

VR Current Value

Use this feature to set the limit on the current voltage regulator (VR). Press "+" or "-" on your keyboard to change this value. Enter 0 to use the manufacture default setting.

CPU C-States

C-States architecture, a processor power management platform developed by Intel, can further reduce power consumption from the basic C1 (Halt State) state that blocks clock cycles to the CPU. Select Enabled for CPU C-States support. The options are Enabled and Disabled. If this feature is set to Enabled, the following items will display:

Enhanced C1E State (Available when "CPU C-States" is set to Enabled)

Select Enabled to enable Enhanced C1 Power State to boost system performance. The options are Enabled and Disabled.

CPU C3 Report (Available when "CPU C-States" is set to Enabled)

Select Enabled to allow the BIOS to report the CPU C3 State (ACPI C2) to the operating system. During the CPU C3 State, the CPU clock generator is turned off. The options are Enabled and Disabled.
CPU C6 Report (Available when "CPU C-States" is set to Enabled)
Select Enabled to allow the BIOS to report the CPU C6 State (ACPI C3) to the operating system. During the CPU C6 State, the power to all caches is turned off. The options are Enabled and Disabled.

C6 Latency (Available when "CPU C-States" is set to Enabled)
Select Short to set a short delay time(period) during which the BIOS reports CPU C6 State (ACPI C3) to the operating system. Select Long to set a long delay time(period) during which the BIOS reports CPU C6 State (ACPI C3) to the operating system. The options are Short and Long.

CPU C7 Report (Available when "CPU C-States" is set to Enabled)
Select Enabled to allow the BIOS to report the CPU C7 State (ACPI C3) to the operating system. CPU C7 State is a processor-specific low C-State. The options are Disabled, CPU C7, and CPU C7s

C7 Latency (Available when "CPU C-States" is set to Enabled)
Select Short to set a short delay time(period) during which the BIOS reports CPU C7 State (ACPI C3) to the operating system. Select Long to set a long delay time(period) during which the BIOS reports CPU C7 State (ACPI C3) to the operating system. The options are Short and Long.

C1 State Auto Demotion
When this item is enabled, the CPU will conditionally demote C3, C6 or C7 requests to C1 State based on un-cored auto-demote information. The options are Disabled and Enabled.

C3 State Auto Demotion
When this item is enabled, the CPU will conditionally demote C6 or C7 requests to C3 State based on un-cored auto-demote information. The options are Disabled and Enabled.

C-State Pre-Wake
Select Enabled to support C State Pre-Wake State features. The options are Enabled and Disabled

Package C-State limit
Select Auto for the AMI BIOS to automatically set the limit on the C-State package register. The options are C0/C1, C2, C3, C6, C7 and Auto.
LakeTiny Feature
Select Enabled for LakeTiny feature support for C-State configuration. The options are Enabled and Disabled.

ACPI T State
Select Enabled for ACPI T state (processor throttling) feature support. The options are Disabled and Enabled.

Chipset Configuration
Warning: Setting the wrong values in the following sections may cause the system to malfunction.

System Agent (SA) Configuration
The following System Agent (SA) information will be displayed:
- System Agent Bridge Name
- VT-d Capability

VT-d
Select Enabled to enable Intel Virtualization Technology support for Direct I/O VT-d by reporting the I/O device assignments to VMM through the DMAR ACPI Tables. This feature offers fully-protected I/O resource-sharing across the Intel platforms, providing the user with greater reliability, security and availability in networking and data-sharing. The settings are Enabled and Disabled.

PCI-E Configuration
This item displays the information of the (graphics) device installed on a PCI-E slot.

Micro-LP Slot

Micro-LP Slot-Gen X

Slot1

Slot1 - Gen X
This feature allows the user to select PCI-E support for the device installed on CPU Slot 6. The options are Auto, Gen1 (Generation 1), Gen 2 and Gen 3.
Detect Non-Compliance Device
Select Enabled for the AMI BIOS to automatically detect a PCI-E device that is not compliant with the PCI-E standards. The options are Enabled and Disabled.

Program PCI-E ASPM After OpROM
PCI-E ASPM, the Active State Power Management for PCI-Express slots, is a power management protocol used to manage power consumption of serial-link devices installed on PCI-Exp slots during a prolonged off-peak time. If this item is set to Enabled, PCI-E ASMP will be programmed after OpROM. If this item is set to Disabled, the PCI-E ASPM will be programmed before OpROM. The options are Enabled and Disabled.

Micro-LP Slot-ASPM/Slot1-ASPM
Use this feature to set the ASPM (Active State Power Management) level for the device installed on the PCI-E slot specified above. The options are Disabled, Auto, ASPM L0s, ASPM L1, and, ASPM L0sL1.

DMI Link ASPM Control
This feature configures the ASPM (Active State Power Management) settings for the devices/components connected to the DMI Link on the System Agent side. The options are Disabled, L0s, L1, and L0sL1.

PCH Link ASPM Control
Select Enabled to support ASPM (Active State Power Management) for both North Bridge and South Bridge. The options are Disabled and Enabled.

Memory Configuration
This submenu displays the information on the memory modules installed on the motherboard.

- Memory RC Version
- Memory Frequency
- Total Memory
- Memory Voltage
- DIMMA1
- DIMMA2
- DIMMB1
• DIMMB2

• CAS Latency (tCL)

• Minimum Delay Time
  
  • CAS to RAS (tRODmin)
  
  • Row Precharge (tRPmin)
  
  • Active to Precharge (tRASmin)

**Memory Frequency Limiter**

This feature sets the limit of memory frequency for DIMM modules installed on the motherboard. The options are **Auto**, 1067 (MHz), 1333 (MHz), and 1600 (MHz).

**Max TOLUD (Top of Low Usable DRAM)**

This feature sets the maximum TOLUD value, which specifies the "Top of Low Usable DRAM" memory space to be used by internal graphics devices, GTT Stolen Memory, and TSEG, respectively, if these devices are enabled. The options are **Enabled** and **Dynamic**, 1 GB, 1.25 GB, 1.5 GB, 1.75 GB, 2 GB, 2.25 GB, 2.5 GB, 2.75 GB, 3 GB and 3.25 GB.

**Note:** TSEG is a block of memory that is only accessible by the processor while operating in SMM mode.

**Memory Scrambler**

This feature enables or disables memory scrambler support for memory error correction. The settings are **Enabled** and Disabled.

**PCH-IO Configuration**

This item displays the information for PCH-IO Chip.

- Intel PCH Rev ID
- USB Configuration
- USB Devices
EHCI1
Select Enabled to enable EHCI (Enhanced Host Controller Interface) Controller 1 for USB 2.0 support. One EHCI controller must always be enabled. The settings are Enabled and Disabled.

EHCI2
Select Enabled to enable EHCI (Enhanced Host Controller Interface) Controller 2 for USB 2.0 support. One EHCI controller must always be enabled. The settings are Enabled and Disabled.

Legacy USB Support
This feature enables support for legacy USB devices. Select Auto to disable legacy support if USB devices are not present. Select Disable to have USB devices available only for EFI applications. The options are Enabled, Disabled and Auto.

Port 60/64 Emulation
This feature enables or disables I/O port 60h/64h emulation support. This should be enabled for complete USB keyboard legacy support for non-USB-aware operating systems. The options are Disabled and Enabled.

XHCI Hand-Off
This item is a work-around solution for operating systems that do not support XHCI (Extensible Host Controller Interface) hand-off. The XHCI ownership change should be claimed by the XHCI driver. The settings are Enabled and Disabled.

EHCI Hand-Off
This item is for Operating Systems that do not support Enhanced Host Controller Interface (EHCI) hand-off. When this item is enabled, EHCI ownership change will be claimed by the EHCI driver. The settings are Enabled and Disabled.

XHCI Mode
This feature handles the operation mode for the XHCI (Extensible Host Controller Interface) controller. The settings are Smart Auto, Auto, Enabled, Disabled and Manual.

SATA Configuration
When this submenu is selected, the AMI BIOS automatically detects the presence of the SATA Devices and displays the following items:
SATA Controllers
This item Enables or Disables the built-in SATA controllers on the motherboard. The options are Enabled and Disabled.

SATA Mode Selection
This item selects the mode for the installed SATA drives. The options are IDE, AHCI and RAID.

SATA RAID Option ROM/UEFI Driver (Available if the item above - SATA Mode Select is set to AHCI or RAID)
Select Enabled to use the SATA RAID Option ROM/UEFI driver for system boot. The options are Enabled and Disabled.

If the item above - SATA Mode Select is set to AHCI, the following items are displayed:

Serial ATA Port 0~ Port 4
This item displays the information detected on the installed SATA drives on the particular SATA port.
- Model number of drive and capacity
- Software Preserve Support

Port 0 ~ Port 4 Hot Plug
This feature designates the port specified for hot plugging. Set this item to Enabled for hot-plugging support, which will allow the user to replace a SATA disk drive without shutting down the system. The options are Enabled and Disabled.

Port 0 ~ Port 1 SATA Device Type
This feature configures the selected SATA port to support either a solid state drive or hard disk drive. The options are Hard Disk Drive and Solid Sate Drive.

Port 0 ~ Port 4 Spin Up Device
On an edge detect from 0 to 1, set this item to allow the PCH to start a COMRESET initialization sequence to the device. The options are Enabled and Disabled.

If the item above - SATA Mode Select is set to IDE, the following items are displayed:

Serial ATA Port 0~ Port 4
This item displays the information detected on the installed SATA drives on the particular SATA port.
- Model number of drive and capacity
• Software Preserve Support

If the item above – SATA Mode Select is set to RAID, the following items are displayed:

**Serial ATA Port 0~ Port 4**

This item displays the information detected on the installed SATA drives on the particular SATA port.

• Model number of drive and capacity

• Software Preserve Support

**Port 0 ~ Port 4 Hot Plug**

This feature designates this port for hot plugging. Set this item to Enabled for hot-plugging support, which will allow the user to replace a SATA drive without shutting down the system. The options are **Enabled** and Disabled.

**Port 0 ~ Port 1 SATA Device Type**

This feature configures the selected SATA port to support either a solid state drive or hard disk drive. Set this item to Enabled to enable hot-plugging. The options are **Hard Disk Drive** and **Solid State Drive**.

**Port 0 ~ Port 4 Spin Up Device**

On an edge detect from 0 to 1, set this item to allow the PCH to start a COMRE-SET initialization sequence to the device. The options are **Enabled** and **Disabled**.

**PCIe/PCI/PnP Configuration**

This feature allows the user to set the PCI/PnP configurations for the following items:

**Above 4G Decoding**

Select Enabled for 64-bit devices to be decoded above the 4GB address space. If 64-bit PCI decoding is supported by the system. The options are **Disabled** and **Enabled**.

**VGA Palette Snoop**

Select Enabled to support VGA palette register snooping which will allow the PCI cards that do not contain their own VGA color palette to examine the video cards palette and mimic it for proper color display. The options are **Disabled** and **Enabled**.
**Chapter 4: AMI BIOS**

**PERR# Generation**
Select Enabled to allow a PCI device to generate a PERR number for a PCI Bus Signal Error Event. The options are **Disabled** and **Enabled**.

**SERR# Generation**
Select Enabled to allow a PCI device to generate an SERR number for a PCI Bus Signal Error Event. The options are **Disabled** and **Enabled**.

**Slot 1 PCI-E 3.0x8/MICRO-LP PCI-E 3.0x8**
Use this feature to enable or disable PCI slot Option ROM for a slot. This is to boot the computer using a device installed on the slot specified. The options are **Disabled**, **Legacy** and **EFI**.

**CPU Slot6 PCI-E 3.0 x16 OPROM/PCH Slot7 PCI-E 2.0 x4 (in x8) OPROM**
Use this feature to enable or disable PCI-E slot Option ROM to boot the computer using a device installed on the slots specified. The options are **Disabled**, **Legacy** and **EFI**.

**Launch Storage OPROM Policy**
This feature controls how the system executes UEFI (Unified Extensible Firmware Interface), and legacy storage OPROM. Select **Legacy Only** to boot the system using a legacy device installed in a PCI slot. The options are **UEFI Only** and **Legacy Only**.

**Other PCI Device ROM Priority**
This feature selects a PCI device OPROM to launch for system boot if this device is not a network, mass storage, or video device. The options are **UEFI Only** and **Legacy Only**.

**Network Stack**
Select Enabled enable PXE (Preboot Execution Environment) or UEFI (Unified Extensible Firmware Interface) for network stack support. The options are **Enabled** and **Disabled**.

▶ **ACPI Settings**

**High Precision Event Timer**
Select Enabled to activate the High Performance Event Timer (HPET) that produces periodic interrupts at a much higher frequency than a Real-time Clock (RTC) does in synchronizing multimedia streams, providing smooth playback and reducing the dependency on other timestamp calculation devices, such as an x86 RDTSC Instruction embedded in the CPU. The High Performance Event Timer is used to replace the 8254 Programmable Interval Timer. The options are **Enabled** and **Disabled**.
WHEA Support

This feature enables the Windows Hardware Error Architecture (WHEA) support for the Windows 2008 (or a later vision) operating system. The options are **Enabled** and **Disabled**.

### Intel Server Platform Services Configuration

The following status information for this motherboard are displayed:

- ME (Management Engine) BIOS Interface Version
- SPS Version
- ME FW (Firmware) Status Value
- ME FW State
- ME FW Operation State
- ME FW Error Code
- ME NM FW Status Value
- BIOS Booting Mode
- Cores Disabled
- ME FW SKU Information
- End-of-POST Status

### Trusted Computing Configuration (Available when a TPM Device is Detected and TPM Jumper is Enabled)

#### Security Device Support

Select Enable for the AMI BIOS to automatically download the drivers needed to provide Trusted Computing platform support for this machine to ensure data integrity and network security. The options are **Disable** and **Enable**.

#### TPM State

Select Enabled to use TPM (Trusted Platform Module) settings for system data security. The options are Disabled and **Enabled**.
Note: The system will reboot for the change on TPM State to take effect.

Pending Operation

Use this item to schedule a TPM-related operation to be performed by a security device for TPM support. The options are None, Enable Take Ownership, Disable Take Ownership, and TPM Clear.

Note: The computer will reboot to carry out a pending TPM operation and change TPM state for a TPM device.

Current Status Information

This feature indicates the status of the following TPM items:

TPM Enabled Status

TPM Active Status

TPM Owner Status

Intel TXT (LT) Support

Intel TXT (Trusted Execution Technology) helps protect against software-based attacks to ensure the security, confidentiality, and integrity of all data stored in the system. The options are Enabled and Disabled.

Super IO Configuration

Super IO Chip  NCT6776D

Serial Port 1 Configuration

Select Enabled to enable the onboard serial port. The options are Enabled and Disabled.

Change (Serial Port 1) Settings

This option specifies the base I/O port address and the Interrupt Request address of Serial Port 1 and 2. Select Auto to let the BIOS automatically assign the base I/O and IRQ address.

The options for Serial Port 1 are Auto, (IO=3F8h; IRQ=4), (IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12) and (IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12).
Serial Port Console Redirection

COM1/SOL

Use this feature to enable console redirection for COM1 and SOL ports. The options are Enabled and Disabled. The default setting for COM1 is Disabled. The default setting for SOL is Enabled.

Console Redirection Settings

This feature allows the user to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

Terminal Type

This feature allows the user to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are ANSI, VT100, VT100+, and VT-UTF8.

Bits Per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and 115200 (bits per second).

Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 Bits and 8 Bits.

Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are None, Even, Odd, Mark and Space.

Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are 1 and 2.
Flow Control

This feature allows the user to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are None and Hardware RTS/CTS.

VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are Enabled and Disabled.

Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are Disabled and Enabled.

Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are Disabled and Enabled.

Legacy OS Redirection Resolution

Use this feature to select the number of rows and columns used in Console Redirection for legacy OS support. The options are 80x24 and 80x25.

Putty KeyPad

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are VT100, LINUX, XTERMR6, SC0, ESCN, and VT400.

Redirection After BIOS Post

Use this feature to enable or disable legacy console redirection after BIOS POST. When set to Bootloader, legacy console redirection is disabled before booting the OS. When set to Always Enable, legacy console redirection remains enabled when booting the OS. The options are Always Enable and Bootloader.

Serial Port for Out-of-Band Management/Windows Emergency Management Services (EMS)

The submenu allows the user to configure Console Redirection settings to support Out-of-Band Serial Port management.

Console Redirection (for EMS)

Select Enabled to use a COM Port selected by the user for Console Redirection. The options are Enabled and Disabled.
Console Redirection Settings (for EMS)

This feature allows the user to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

Out-of-Band Management Port

The feature selects a serial port used by the Microsoft Windows Emergency Management Services (EMS) to communicate with a remote server. The options are COM1 and SOL.

Terminal Type

This feature allows the user to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII character set. Select VT100+ to add color and function key support. Select ANSI to use the extended ASCII character set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are ANSI, VT100, VT100+, and VT-UTF8.

Bits Per Second

This item sets the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 57600, and 115200 (bits per second).

Flow Control

This feature allows the user to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are None, Hardware RTS/CTS, and Software Xon/Xoff.

Data Bits, Parity, Stop Bits

The status of each item above is displayed.
4-4  Event Logs

Enabling/Disabling Options

**SMBIOS Event Log**
Change this item to enable or disable all features of the SMBIOS Event Logging during system boot. The options are **Enabled** and **Disabled**.

**Erasing Settings**

**Erase Event Log**
If No is selected, data stored in the event log will not be erased. Select Yes, Next Reset, data in the event log will be erased upon next system reboot. Select Yes, Every Reset, data in the event log will be erased upon every system reboot. The options are **No**, **Yes, Next reset**, and **Yes, Every reset**.

**When Log is Full**
Select Erase Immediately for all messages to be automatically erased from the event log when the event log memory is full. The options are **Do Nothing** and **Erase Immediately**.

**SMBIOS Event Long Standard Settings**

**Log System Boot Event**
This option toggles the System Boot Event logging to enabled or disabled. The options are **Disabled** and **Enabled**.
MECI
The Multiple Event Count Increment (MECI) counter counts the number of occurrences a duplicate event must happen before the MECI counter is incremented. This is a numeric value. The default value is 1.

METW
The Multiple Event Time Window (METW) defines number of minutes must pass between duplicate log events before MECI is incremented. This is in minutes, from 0 to 99. The default value is 60.

View SMBIOS Event Log
This section displays the contents of the SMBIOS Event Log.
4-5 IPMI

The following IPMI information will be displayed:

- IPMI Firmware Revision
- IPMI Status

**System Event Log**

This feature is used to change the System Event Log (SEL) configuration.

**Enabling/Disabling Options**

**SEL Components** - Change this item to enable or disable all features of System Event Logging. The options are Enabled and **Disabled**. When this feature is set to Enabled, the following can be configured:

**Erase SEL** - This option erases all logged SEL events. The options are **No**, Yes, On Next reset and Yes, On Every reset.

**When SEL Full**

This option automatically clears the System Event Log memory of all messages when it is full. The options are **Do Nothing** and Erase Immediately.
BMC Network Configuration

LAN Channel 1: This feature allows the user to configure the settings for LAN1 Port.

Update IPMI LAN Configuration
This feature allows the BIOS to implement any IP/MAC address changes at the next system boot. If the option is set to Yes, any changes made to the settings below will take effect when the system is rebooted. The options are No and Yes.

IPMI LAN Selection
This feature displays the IPMI LAN Selection setting. The default setting is Failover.

IPMI Network Link Status
This feature displays the IPMI Network Link status. The default setting is Dedicated LAN.

Configuration Address Source
This feature allows the user to select the source of the IP address for this computer. If Static is selected, you will need to know the IP address of this computer and enter it to the system manually in the field. If DHCP is selected, the BIOS will search for a DHCP (Dynamic Host Configuration Protocol) server in the network that is attached to and request the next available IP address for this computer. The options are DHCP and Static. The following items are assigned IP addresses automatically if DHCP is selected, or can be configured manually if Static is selected.

Station MAC Address
This item displays the Station MAC address for this computer. Mac addresses are 6 two-digit hexadecimal numbers.

Station IP Address
This item displays the Station IP address for this computer. This should be in decimal and in dotted quad form (i.e., 192.168.10.253).

Subnet Mask
This item displays the sub-network that this computer belongs to. The value of each three-digit number separated by dots should not exceed 255.

Gateway IP Address
This item displays the Gateway IP address for this computer. This should be in decimal and in dotted quad form (i.e., 192.168.10.253).
4-6  Boot Settings

Use this feature to configure Boot Settings:

- **Set Boot Priority**
  - 1st Boot Device
  - 2nd Boot Device
  - 3rd Boot Device
  - 4th Boot Device
  - 5th Boot Device
  - 6th Boot Device

- **UEFI Boot Drive BBS Priorities**

- **Delete Boot Option**

- **Delete Driver Option**

**Set Boot Priority**

This option prioritizes the order of bootable devices that the system to boot from. Press [ENTER] on each entry from top to bottom to select devices.

- 1st Boot Device
- 2nd Boot Device
- 3rd Boot Device
- 4th Boot Device
- 5th Boot Device
- 6th Boot Device

**USB Hard Disk Drive BBS Priorities**

- 1st Device
- 2nd Device

**Network Device BBS Priorities**

- 1st Device
UEFI Boot Drive BBS Priorities

- 1st Boot Device

Add New Boot Option

This feature allows the user to add a boot device from which the systems will boot after power-on.

Add Boot Option

Enter the name of the new boot option here.

Path for Boot Option

Enter the path of the new boot option here.

Create

Press Enter here to create the new boot option.

Delete Boot Option

Use this feature to remove a pre-defined boot device from which the system will boot during startup.

The settings are [any pre-defined boot device].

Add New Driver Option

This feature allows the user to add a boot device from which the systems boots during startup.

Add Driver Option

Enter the name of the new boot option here.

Path for Driver Option

Enter the path of the new boot option here.

Create

Press Enter here to create the new boot option.

Delete Driver Option

This feature allows the user to delete a previously defined boot device from which the systems boots during startup.

The settings are [any pre defined boot device].
4-7 Security Settings

This menu allows the user to configure the following security settings for the system.

- If the Administrator password is defined ONLY - this controls access to the BIOS setup ONLY.
- If the User's password is defined ONLY - this password will need to be entered upon each system boot, and will also have Administrator rights in the setup.
- Passwords must be at least 3 and up to 20 characters long.

Administrator Password

Press Enter to create a new, or change an existing Administrator password.
4-8 Save & Exit

Select the Exit tab from the BIOS Setup Utility screen to enter the Exit BIOS Setup screen.

Discard Changes and Exit
Select this option to quit the BIOS Setup without making any permanent changes to the system configuration, and reboot the computer. Select Discard Changes and Exit from the Exit menu and press <Enter>.

Save Changes and Reset
When you have completed the system configuration changes, select this option to leave the BIOS Setup Utility and reboot the computer, so the new system configuration parameters can take effect. Select Save Changes and Exit from the Exit menu and press <Enter>.

Save Options

Save Changes
When you have completed the system configuration changes, select this option to save any changes made. This will not reset (reboot) the system.

Discard Changes
Select this option and press <Enter> to discard all the changes and return to the AMI BIOS Utility Program.
Restore Optimized Defaults
To set this feature, select Restore Defaults from the Exit menu and press <Enter>. These are factory settings designed for maximum system stability, but not for maximum performance.

Save As User Defaults
To set this feature, select Save as User Defaults from the Exit menu and press <Enter>. This enables the user to save any changes to the BIOS setup for future use.

Restore User Defaults
To set this feature, select Restore User Defaults from the Exit menu and press <Enter>. Use this feature to retrieve user-defined settings that were saved previously.

Boot Override
Listed on this section are other boot options for the system (i.e., Built-in EFI shell). Select an option and press <Enter>. Your system will boot to the selected boot option.
Appendix A

BIOS Error Beep Codes

During the POST (Power-On Self-Test) routines, which are performed each time the system is powered on, errors may occur.

**Non-fatal errors** are those which, in most cases, allow the system to continue with bootup. The error messages normally appear on the screen.

**Fatal errors** will not allow the system to continue to bootup. If a fatal error occurs, you should consult with your system manufacturer for possible repairs.

These fatal errors are usually communicated through a series of audible beeps. The numbers on the fatal error list correspond to the number of beeps for the corresponding error.

### A-1 BIOS Error Beep Codes

<table>
<thead>
<tr>
<th>Beep Code/LED</th>
<th>Error Message</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 beep</td>
<td>Refresh</td>
<td>Circuits have been reset. (Ready to power up)</td>
</tr>
<tr>
<td>5 short beeps</td>
<td>Memory error</td>
<td>No memory detected in the system</td>
</tr>
<tr>
<td>OH LED On</td>
<td>System OH</td>
<td>System Overheat</td>
</tr>
</tbody>
</table>
B-1 Installing Software Programs

The Supermicro ftp site contains drivers and utilities for your system at ftp://ftp.supermicro.com. Some of these must be installed, such as the chipset driver.

After accessing the ftp site, go into the CDR_Images directory and locate the ISO file for your motherboard. Download this file to create a CD/DVD of the drivers and utilities it contains. (You may also use a utility to extract the ISO file if preferred.) Another option is to go to the Supermicro Website at http://www.supermicro.com/products/. Find the product page for your motherboard here, where you may download individual drivers and utilities.

After creating a CD/DVD with the ISO files, insert the disk into the CD/DVD drive on your system and the display shown below should appear.

![Driver/Tool Installation Display Screen](image)

**Note 1:** Click the icons showing a hand writing on paper to view the readme files for each item. Click the computer icons to the right of these items to install each item (from top to the bottom) one at a time. **After installing each item, you must re-boot the system before moving on to the next item on the list.** The bottom icon with a CD on it allows you to view the entire contents.

**Note 2:** When making a storage driver diskette by booting into a Driver CD, please set the SATA Configuration to "Compatible Mode" and configure SATA as IDE in the BIOS Setup. After making the driver diskette, be sure to change the SATA settings back to your original settings.
B-2 Configuring SuperDoctor III

The SuperDoctor® III program is a web-based management tool that supports remote management capability. It includes Remote and Local Management tools. The local management is called SD III Client. The SuperDoctor III program allows you to monitor the environment and operations of your system. SuperDoctor III displays crucial system information such as CPU temperature, system voltages and fan status. See the figures below for examples of the SuperDoctor III interface.

**Note 1:** The default User Name and Password for SuperDoctor III is ADMIN/ADMIN.

**Note 2:** When SuperDoctor III is first installed, it adopts the temperature threshold settings that have been set in BIOS. Any subsequent changes to these thresholds must be made within SuperDoctor III, as the SuperDoctor III settings override the BIOS settings. To set the BIOS temperature threshold settings again, you would first need to uninstall SuperDoctor III.

![SuperDoctor® III Interface Display Screen (Health Information)](image)
Appendix B: Software Installation Instructions

SuperDoctor® III Interface Display Screen (Remote Control)

Note: The SuperDoctor III program and User’s Manual can be downloaded from the Supermicro web site at http://www.supermicro.com/products/accessories/software/SuperDoctorIII.cfm. For Linux, we recommend that you use the SuperDoctor II application instead.
Notes
Appendix C

UEFI BIOS Recovery Instructions

Warning! Do not upgrade the BIOS unless your system has a BIOS-related issue. Flashing the wrong BIOS can cause irreparable damage to the system. In no event shall Supermicro be liable for direct, indirect, special, incidental, or consequential damages arising from a BIOS update. If you need to update the BIOS, do not shut down or reset the system while the BIOS is updating to avoid possible boot failure.

C-1 An Overview to the UEFI BIOS

The Unified Extensible Firmware Interface (UEFI) specification provides a software-based interface between the operating system and the platform firmware in the pre-boot environment. The UEFI specification supports an architecture-independent mechanism for add-on card initialization to allow the UEFI OS loader, which is stored in the add-on card, to boot up the system. UEFI offers a clean, hand-off control to a computer system at bootup.

C-2 How to Recover the UEFI BIOS Image (the Main BIOS Block)

An AMIBIOS flash chip consists of a boot sector block and a main BIOS code block (a main BIOS image). The boot sector block contains critical BIOS codes, including memory detection and recovery codes for the user to flash a new BIOS image if the original BIOS image is corrupted. When the system power is on, the boot sector codes execute first. Once it is completed, the main BIOS code will continue with system initialization and bootup.

Note: Follow the BIOS Recovery instructions below for BIOS recovery when the main BIOS block crashes. However, when the BIOS Boot sector crashes, you will need to send the motherboard back to Supermicro for RMA repair.

C-3 To Recover the Boot Sector Using a USB-Attached Device

This feature allows the user to recover a BIOS image using a USB-attached device without additional utilities used. A USB flash device such as a USB Flash Drive, or a USB CD/DVD ROM/RW device can be used for this purpose. However, a USB Hard Disk drive cannot be used for BIOS recovery at this time.
To perform UEFI BIOS recovery using a USB-attached device, follow the instructions below.

1. Using a different machine, copy the "Super.ROM" binary image file into the disc Root "\" Directory of a USB device or a writeable CD/DVD.

   **Note:** If you cannot locate the "Super.ROM" file in your driver disk, visit our website at www.supermicro.com to download the BIOS image into a USB flash device and rename it to "Super ROM" for BIOS recovery use.

2. Insert the USB device that contains the new BIOS image ("Super.ROM") into your USB drive and power on the system.

3. While powering on the system, keep pressing <Ctrl> and <Home> simultaneously on your PS2 keyboard until you hear two short beeps. This may take from a few seconds to one minute.

4. After locating the new BIOS binary image, the system will enter the BIOS Recovery menu as shown below.

   **Note:** At this point, you may decide if you want to start with BIOS Recovery. If you decide to proceed with BIOS Recovery, follow the procedures below.

5. When the screen as shown above displays, using the arrow key, select the item- "Proceed with flash update" and press the <Enter> key. You will see the progress of BIOS Recovery as shown in the screen below.

   **Note:** *Do not interrupt* the process of BIOS flashing until it is completed.
6. After the process of BIOS Recovery is complete, press any key to reboot the system.

7. Using a different system, extract the BIOS package into a bootable USB flash drive.

8. When a DOS prompt appears, enter AMI.BAT BIOSname.### at the prompt.

Note: Do not interrupt this process until BIOS flashing is completed.
9. After seeing the message that BIOS update is completed, unplug the AC power cable from the power supply to clear CMOS, and then plug the AC power cable in the power supply again to power on the system.

10. Press <Del> continuously to enter the BIOS Setup utility.

11. Press <F3> to load default settings.

12. After loading default settings, press <F4> to save the settings and exit the BIOS Setup utility.
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