



**X9DAX-iF**  
**X9DAX-7F**  
**X9DAX-iTF**  
**X9DAX-7TF**

**USER'S MANUAL**

Revision 1.1b

---

The information in this User's Manual has been carefully reviewed and is believed to be accurate. The vendor assumes no responsibility for any inaccuracies that may be contained in this document, and makes no commitment to update or to keep current the information in this manual, or to notify any person or organization of the updates. **Please Note: For the most up-to-date version of this manual, please see our Website at [www.supermicro.com](http://www.supermicro.com).**

Super Micro Computer, Inc. ("Supermicro") reserves the right to make changes to the product described in this manual at any time and without notice. This product, including software and documentation, is the property of Supermicro and/or its licensors, and is supplied only under a license. Any use or reproduction of this product is not allowed, except as expressly permitted by the terms of said license.

**IN NO EVENT WILL SUPER MICRO COMPUTER, INC. BE LIABLE FOR DIRECT, INDIRECT, SPECIAL, INCIDENTAL, SPECULATIVE OR CONSEQUENTIAL DAMAGES ARISING FROM THE USE OR INABILITY TO USE THIS PRODUCT OR DOCUMENTATION, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. IN PARTICULAR, SUPER MICRO COMPUTER, INC. SHALL NOT HAVE LIABILITY FOR ANY HARDWARE, SOFTWARE, OR DATA STORED OR USED WITH THE PRODUCT, INCLUDING THE COSTS OF REPAIRING, REPLACING, INTEGRATING, INSTALLING OR RECOVERING SUCH HARDWARE, SOFTWARE, OR DATA.**

---

Any disputes arising between the manufacturer and the customer shall be governed by the laws of Santa Clara County in the State of California, USA. The State of California, County of Santa Clara shall be the exclusive venue for the resolution of any such disputes. Supermicro's total liability for all claims will not exceed the price paid for the hardware product.

---

**FCC Statement:** This equipment has been tested and found to comply with the limits for a class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the authorized dealer or an experienced radio/TV technician for help.

**California Best Management Practices Regulations for Perchlorate Materials:** This Perchlorate warning applies only to products containing CR (Manganese Dioxide) Lithium coin cells. "Perchlorate Material-special handling may apply. See [www.dtsc.ca.gov/hazardouswaste/perchlorate](http://www.dtsc.ca.gov/hazardouswaste/perchlorate)".

---

**WARNING: Handling of lead solder materials used in this product may expose you to lead, a chemical known to the State of California to cause birth defects and other reproductive harm.**

---

Manual Revision 1.1b

Release Date: December 5, 2014

Unless you request and receive written permission from Super Micro Computer, Inc., you may not copy any part of this document.

Information in this document is subject to change without notice. Other products and companies referred to herein are trademarks or registered trademarks of their respective companies or mark holders.

Copyright © 2014 by Super Micro Computer, Inc.

All rights reserved.

**Printed in the United States of America**

## Preface

This manual is written for system integrators, PC technicians and knowledgeable PC users. It provides information for the installation and use of the X9DAX-iF/-7F/-iTF/-7TF motherboard.

## About This Motherboard

The Super X9DAX-iF/-7F/-iTF/-7TF motherboard supports dual Intel E5-2600(v2) Series Processors (Socket R LGA 2011) that offer QPI (Intel QuickPath Interface) Technology (V.1.1), providing point-to-point connection with a transfer speed of up to 8.0 GT/s. With the C602 chipset built in, the X9DAX-iF/-7F/-iTF/-7TF motherboard provides support for Intel® Management Engine (ME), Rapid Storage Technology, Digital Media Interface (DMI), PCI-E Gen. 3.0, and up to 1866 MHz DDR3 memory, greatly enhancing system performance for high-end workstations. Please refer to our website (<http://www.supermicro.com>) for processor and memory support updates.

## Manual Organization

**Chapter 1** describes the features, specifications, and performance of the motherboard. It also provides detailed information about the Intel C602 chipset.

**Chapter 2** provides hardware installation instructions. Read this chapter when installing the processor, memory modules, and other hardware components into the system. If you encounter any problems, see **Chapter 3**, which describes troubleshooting procedures for video, memory, and system setup stored in the CMOS.

**Chapter 4** includes an introduction to the BIOS, and provides detailed information on running the CMOS Setup utility.

**Appendix A** provides BIOS Error Beep Codes.

**Appendix B** lists software installation instructions.

## **Conventions Used in the Manual**

Pay special attention to the following symbols for proper system installation and to prevent damage to the system or injury to yourself:

**Warning:** Important information given to ensure proper system installation and to prevent damage to system components or injury to yourself.



**Note:** Additional information given to differentiate among various models or provides information for correct system setup.

## Contacting Supermicro

### **Headquarters**

Address: Super Micro Computer, Inc.  
980 Rock Ave.  
San Jose, CA 95131 U.S.A.  
Tel: +1 (408) 503-8000  
Fax: +1 (408) 503-8008  
Email: [marketing@supermicro.com](mailto:marketing@supermicro.com) (General Information)  
[support@supermicro.com](mailto:support@supermicro.com) (Technical Support)  
Web Site: [www.supermicro.com](http://www.supermicro.com)

### **Europe**

Address: Super Micro Computer B.V.  
Het Sterrenbeeld 28, 5215 ML  
's-Hertogenbosch, The Netherlands  
Tel: +31 (0) 73-6400390  
Fax: +31 (0) 73-6416525  
Email: [sales@supermicro.nl](mailto:sales@supermicro.nl) (General Information)  
[support@supermicro.nl](mailto:support@supermicro.nl) (Technical Support)  
[rma@supermicro.nl](mailto:rma@supermicro.nl) (Customer Support)  
Web Site: [www.supermicro.com](http://www.supermicro.com)

### **Asia-Pacific**

Address: Super Micro Computer, Inc.  
3F, No. 150, Jian 1st Rd.  
Zhonghe Dist., New Taipei City 235  
Taiwan (R.O.C)  
Tel: +886-(2) 8226-3990  
Fax: +886-(2) 8226-3992  
Email: [support@supermicro.com.tw](mailto:support@supermicro.com.tw)  
Tel: +886-(2)-8226-3990  
Web Site: [www.supermicro.com.tw](http://www.supermicro.com.tw)

# Table of Contents

## **Preface**

### **Chapter 1 Overview**

1-1	Overview .....	1-1
1-2	Processor and Chipset Overview.....	1-11
1-3	Special Features .....	1-12
1-4	PC Health Monitoring.....	1-12
1-5	ACPI Features.....	1-13
1-6	Power Supply .....	1-13
1-7	Super I/O.....	1-14
	Intel® Intelligent Power Node Manager (IPNM) .....	1-14
	Management Engine (ME) .....	1-14
1-8	Overview of the Nuvoton WPCM450 Controller .....	1-14
	WPCM450R DDR2 Memory Interface .....	1-15
	WPCM450R PCI System Interface .....	1-15
	Other Features Supported by the WPCM BMC Controller.....	1-15

### **Chapter 2 Installation**

2-1	Standardized Warning Statements .....	2-1
2-2	Static-Sensitive Devices.....	2-4
2-3	Processor and Heatsink Installation.....	2-5
	Installing the LGA2011 Processor .....	2-5
	Installing a Passive CPU Heatsink .....	2-9
	Removing the Heatsink.....	2-10
2-4	Installing and Removing the Memory Modules.....	2-11
	Installing & Removing DIMMs.....	2-11
	Removing Memory Modules .....	2-11
2-5	Motherboard Installation.....	2-16
	Tools Needed .....	2-16
	Location of Mounting Holes .....	2-16
	Installing the Motherboard .....	2-17
2-6	Control Panel Connectors and I/O Ports .....	2-18
	Back Panel Connectors and I/O Ports.....	2-18
	Back Panel I/O Port Locations and Definitions .....	2-18
	Video Connection .....	2-19
	Ethernet Ports .....	2-19
	Universal Serial Bus (USB).....	2-20
	(Back_Panel) High Definition Audio (HD Audio) .....	2-21

---

Front Accessible Audio Header .....	2-21
SPDIF_In/SPDIF_Out Headers .....	2-22
COM Port Header .....	2-22
Front Control Panel .....	2-23
Front Control Panel Pin Definitions .....	2-24
NMI Button .....	2-24
Power LED .....	2-24
HDD LED .....	2-25
NIC1/NIC2 LED Indicators .....	2-25
Overheat (OH)/Fan Fail LED .....	2-26
Power Fail LED .....	2-26
Reset Button .....	2-27
Power Button .....	2-27
2-7 Connecting Cables .....	2-28
Power Connectors .....	2-28
Fan Headers .....	2-29
Chassis Intrusion .....	2-29
Internal Speaker .....	2-30
Power LED/Speaker .....	2-30
TPM Header/Port 80 Header .....	2-31
Standby Power Header .....	2-31
Power SMB (I <sup>2</sup> C) Connector .....	2-32
IPMB .....	2-32
T-SGPIO 1/2 & SCU-SGPIO 1 Headers .....	2-33
IEEE 1394a_1/ 1394a_2 Connectors .....	2-33
Overheat/Fan Fail LED .....	2-34
DOM Power Connector .....	2-34
BBU Header (Optional for the X9DAX-7F/7TF Only) .....	2-35
2-8 Jumper Settings .....	2-36
Explanation of Jumpers .....	2-36
GLAN Enable/Disable .....	2-36
Clear CMOS .....	2-37
BIOS Chip Select Jumper (J19) .....	2-37
IEEE 1394a Enable .....	2-38
SMB Enable .....	2-38
VGA Enable .....	2-39
BMC Enable .....	2-39
I <sup>2</sup> C Bus to PCI-Exp. Slots .....	2-40
SAS Enable (X9DAX-7F/7TF Only) .....	2-40

---

Management Engine (ME) Recovery .....	2-41
Manufacture Mode Select .....	2-41
Watch Dog Enable/Disable .....	2-42
2-9 Onboard LED Indicators .....	2-43
GLAN LEDs.....	2-43
Onboard Power LED .....	2-43
BMC Heartbeat LED .....	2-44
2-10 SATA/SAS Connections .....	2-45
Serial ATA Ports.....	2-45
SAS2 Ports (X9DAX-7F/-7TF Only).....	2-45

***Chapter 3 Troubleshooting***

3-1 Troubleshooting Procedures .....	3-1
3-2 Technical Support Procedures.....	3-5
3-3 Battery Removal and Installation.....	3-6
3-4 Frequently Asked Questions .....	3-7
3-5 Returning Merchandise for Service.....	3-8

***Chapter 4 BIOS***

4-1 Introduction.....	4-1
4-2 Main Setup .....	4-2
4-3 Advanced Setup Configurations.....	4-4
4-4 Overclocking.....	4-24
4-5 Event Logs .....	4-26
4-6 IPMI .....	4-28
4-7 Boot .....	4-30
4-8 Security .....	4-31
4-9 Save & Exit .....	4-32

***Appendix A BIOS Error Beep Codes***

A-1 BIOS Error Beep Codes .....	A-1
---------------------------------	-----

***Appendix B Software Installation Instructions***

B-1 Installing Software Programs .....	B-1
B-2 Configuring SuperDoctor III .....	B-2

# Chapter 1

## Overview

### 1-1 Overview

#### Checklist

Congratulations on purchasing your computer motherboard from an acknowledged leader in the industry. Supermicro boards are designed with the utmost attention to detail to provide you with the highest standards in quality and performance.

Please check that the following items have all been included with your motherboard. If anything listed here is damaged or missing, contact your retailer.

The following items are included in the retail box:

- One (1) Supermicro Mainboard
- Six (6) Serial ATA cables (CBL-0044Lx6) (X9DAX-iF/iTF Only)
- Four (4) Serial ATA cables (CBL-0044Lx4) (X9DAX-7F/7TF Only)
- Two (2) I-Pass to Serial ATA cable (CBL-097L-03 x2) (X9DAX-7F/7TF Only)
- One (1) IEEE 1394a Internal to External cable (CBL-0173L)
- One (1) I/O Shield (MCP-260-00049-0N)



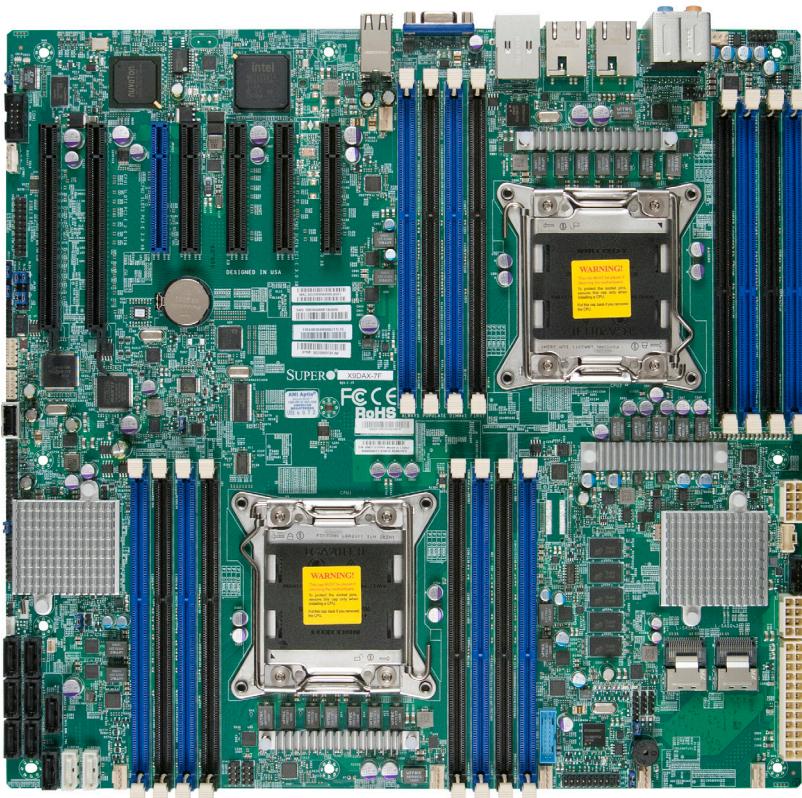
**Note:** For your system to work properly, please follow the links below to download all necessary drivers/utilities and the user's manual for your motherboard.

Supermicro product manuals: <http://www.supermicro.com/support/manuals/>

Product Drivers and utilities: [ftp://ftp.supermicro.com/](http://ftp.supermicro.com/)

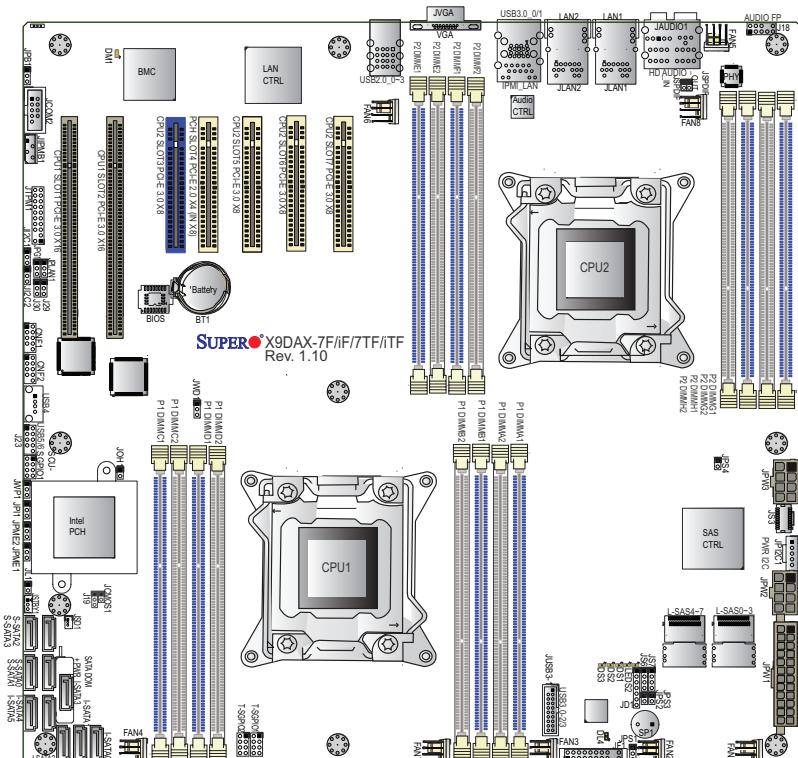
If you have any questions, please contact our support team at [support@supermicro.com](mailto:support@supermicro.com).

**SUPER<sup>®</sup> Motherboard Image**



**Note:** All graphics shown in this manual were based upon the latest PCB Revision available at the time of publishing of the manual. The motherboard you've received may or may not look exactly the same as the graphics shown in this manual.

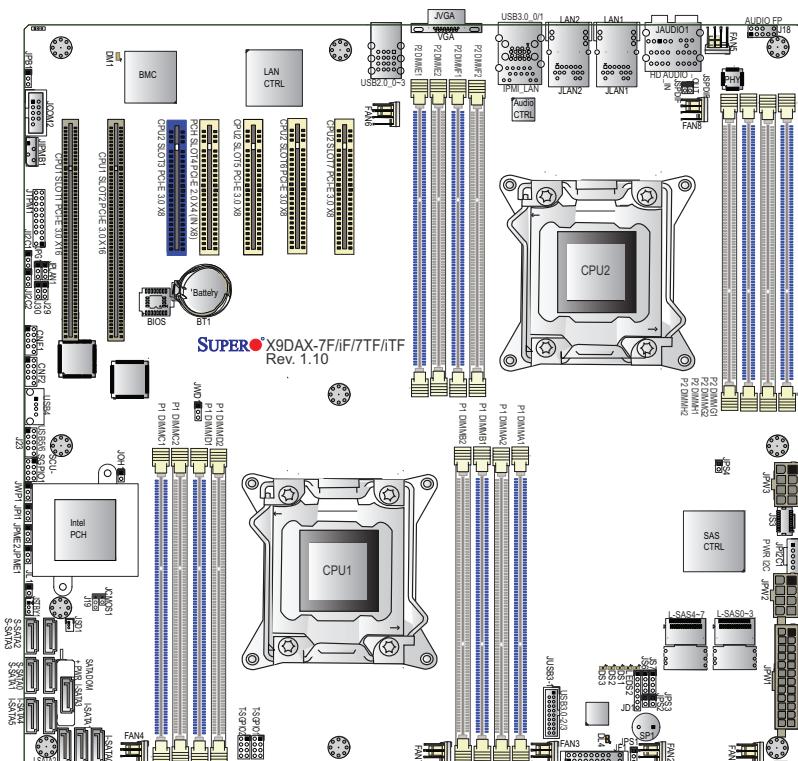
## Motherboard Layout



**Note 1:** For the latest CPU/Memory updates, please refer to our website at <http://www.supermicro.com/products/motherboard/> for details.

**Note 2:** Changing BMC log-in information is recommended during initial system power-on. The default username is ADMIN and password is ADMIN. For BMC best practices, please refer to: [http://www.supermicro.com/products/nfo/files/IPMI/Best\\_Practices\\_BMC\\_Security.pdf](http://www.supermicro.com/products/nfo/files/IPMI/Best_Practices_BMC_Security.pdf)

## X9DAX-iF/-7F/-iTF/-7TF Quick Reference



**Notes:**

- See Chapter 2 for detailed information on jumpers, I/O ports and JF1 front panel connections.
- "■" indicates the location of "Pin 1".
- Jumpers/LED Indicators not indicated are for testing only. Also, components that are not documented in this manual are reserved for internal use only.
- Use only the correct type of onboard CMOS battery as specified by the manufacturer. Do not install the onboard battery upside down to avoid possible explosion.

**X9DAX-iF-7F-/iTF-/7TF Jumpers**

<b>Jumper</b>	<b>Description</b>	<b>Default Setting</b>
J19	BIOS Chip Select	Pins 1-2 (Primary BIOS Chip Select)
J29/J30	SMB Enable	Pins 1-2 (Enabled)
JCMOS1	Clear CMOS	Open (Normal)
JI <sup>2</sup> C1/JI <sup>2</sup> C2	SMBus to PCI-E Slots	Pins 2-3 (Normal)
JPB1	BMC Enable	Pins 1-2 (Enabled)
JPG1	VGA Enable	Pins 1-2 (Enabled)
JPI1	IEEE1394a Enable	Pins 1-2 (Enabled)
JPLAN1 (X9DAX-iF/7F)	Gigabit Ethernet LAN Enable	Pins 1-2 (Enabled)
JPLAN1 (X9DAX-iTF/7TF)	10G Ethernet LAN Enable	Pins 1-2 (Enabled)
JPME1	Management Engine (ME) Recovery Mode Engine	Pins 1-2 (Normal)
JPME2	Manufacture Mode Select	Pins 1-2 (Normal)
JPS1 (X9DAX-7F-/7TF)	SAS Enable	Pins 1-2 (Enabled)
JWD1	Watch Dog	Pins 1-2 (Reset)

**X9DAX-iF-7F-/iTF-/7TF Connectors**

<b>Connectors</b>	<b>Description</b>
(J) Audio	8 Channel (7.1) High-Definition Backplane Audio Connector
(FP) Audio	Front Panel Audio Header (J18)
Battery	Onboard CMOS Battery (See Chpt. 3 for Used Battery Disposal) (BT1)
CNF1/CNF2	IEEE 1394a 1/2 Connectors
(J) COM2	Front_Panel Accessible COM Port Header
FAN1~8	CPU/System Fan Headers (Fan7/Fan8: CPU 1/2 Fans)
JD1	Speaker/Power LED Indicator
JF1	Front Panel Control Header
JL1	Chassis Intrusion
JOH1	Overheat/Fan Fail LED Indicator
JPI <sup>2</sup> C1	Power Supply SMBus I <sup>2</sup> C Header
JPW1	24-Pin 24-Pin Power Connectors (See Warning on Pg. 1-6.)
JPW2/JPW3	12V 8-Pin Power Connectors (See Warning on Pg. 1-6.)
JS3	Battery Backup Unit (BBU) Connector (For X9DAX-7F/7TF)
JSD1	SATA DOM (Device_On_Module) Power Connector
JSPDIF_In	SPDIF_(Sony/Philips Digital Interface)_In Header
JSPDIF_Out	SPDIF_(Sony/Philips Digital Interface)_Out Header

JSTBY1	Standby Power Header
JTPM1	TPM (Trusted Platform Module)/Port 80 Header
JVGA	VGA Port
LAN1/2	Gigabit Ethernet LAN Ports 1/2 (X9DAX-iF/7F Only), 10G Ethernet LAN Ports 1/2 (X9DAX-iTF/7TF Only)
(L-)SAS 0~3, 4~7	Serial_Attached SCSI 2.0 Connections 0~3, 4~7 from the LSI SAS 2208 Controller (X9DAX-7F/7TF Only)
(I-)SATA0~5	Intel SB SATA Connectors 0~5 (SATA 0/1: SATA 3.0 Ports, SATA2~5: SATA 2.0 ports)
(S-)SATA0~3	SATA 2.0 Connectors 0~3 from SCU (Storage Control Unit)
(CPU1) Slot1/Slot2	PCI-E 3.0 x16 Slots (Available when CPU1 is installed.)
(CPU2) Slots 3/5/6/7	PCI-E 3.0 x8 Slot (Available when CPU2 is installed.)
(PCH) Slot4	PCI-E 2.0 x4 in x8 Slot
SP1	Onboard Buzzer (Internal Speaker)
SCU-SGPIO 1	Serial-Link General_Purpose IO -Header for S-SATA Connections 0~3
T-SGPIO 1/2	Serial-Link General_Purpose IO Headers for I-SATA Connections 0~3, 4/5
USB 2.0 #0~3	Back Panel USB 2.0 Ports 0/1/2/3
USB 2.0 #4	Front-Panel Accessible Type A USB 2.0 Header (USB 4: J25)
USB 2.0 #5/6	Front-Panel Accessible USB 2.0 Connections 5/6 (J23)
USB 3.0 #0/1	Back Panel USB 3.0 Ports 0/1
USB 3.0 #2/3	Front-Panel Accessible USB 3.0 Connections 2/3 (JUSB3)

#### X9DAX-iF/-7F/-iTF/-7TF LED Indicators

LED	Description	State	Status
DL4	Standby PWR LED	Green: On	Standby Power On
DM1	BMC LED	Green	BMC Active
LEDS2	SAS LED	On	SAS Active

**Warning:** To prevent damage to the power supply or motherboard, please use a power supply that contains a 24-pin and two 8-pin power connectors. Be sure to connect these power supply connectors to the 24-pin power connector (JPW1) and two 8-pin power connectors (JPW2, JPW3) on the motherboard. Failure in doing so will void the manufacturer warranty on your power supply and motherboard.

## Motherboard Features

<b>CPU</b>	<ul style="list-style-type: none"> <li>Dual Intel® E5-2600(v2) Series (Socket R LGA 2011) Processors; each supports two full-width Intel Quick-Path Interconnect (QPI) links of up to 8.0 GT/s per QPI link.</li> </ul> <p> <b>Note:</b> For Intel E5-2600(v2) processor support, BIOS version 3.0 or above is required.</p>
<b>Memory</b>	<ul style="list-style-type: none"> <li>Integrated memory controller supports up to 1 TB of Load Reduced (LRDIMM), 512 GB of Registered (RDIMM) or 128 GB of Unbuffered (UDIMM) ECC/ Non-ECC DDR3 800/1066/1333/1600/1866 MHz 240-pin 4-channel memory modules in 16 DIMM slots.</li> </ul> <p> <b>Note 1:</b> 1866 MHz memory speed is dependent on Intel E5-2600v2 CPUs.</p> <p><b>Note 2:</b> For the latest memory updates, please refer to the Tested Memory List posted on our website (<a href="http://www.supermicro.com/products/motherboard">http://www.supermicro.com/products/motherboard</a>).</p>
<b>Chipset</b>	<ul style="list-style-type: none"> <li>Intel® C602 Chipset (PCH A)</li> </ul>
<b>Expansion Slots</b>	<ul style="list-style-type: none"> <li>Two (2) PCI-Express 3.0 x16 slots (CPU1 Slots1/2),</li> <li>Four (4) PCI-E3.0 x8 slot (CPU2 Slots3/5/6/7),</li> <li>One (1) PCI Express 2.0 x 4 in x8 slot (PCH Slot4)</li> </ul>
<b>Network</b>	<ul style="list-style-type: none"> <li>One I350 Dual-Channel Gigabit (100/1000 Mb/s) Ethernet Controller for LAN 1/LAN 2 ports (X9DAX-iF/7F),</li> <li>One X540 Dual 10GbE Ethernet Controller for LAN 1/LAN 2 ports (X9DAX-iTF/7TF),</li> <li>LSI 2208 SAS Controller (X9DAX-7F/-7TF Only)</li> </ul>
	<b>SATA Connections</b>
	<ul style="list-style-type: none"> <li>SATA Ports</li> </ul> <ul style="list-style-type: none"> <li>Two (2) SATA 3.0 Ports: I-SATA 0/1</li> <li>Four (4) SATA 2.0 Ports (I-SATA 2~5) from Intel PCH</li> <li>Four (4) SATA 2.0 Ports (S-SATA 0~3) from SCU</li> </ul>
	<ul style="list-style-type: none"> <li>RAID</li> </ul> <p>RAID 0, 1, 5, 10 (Windows/Linux latest kernel)</p>

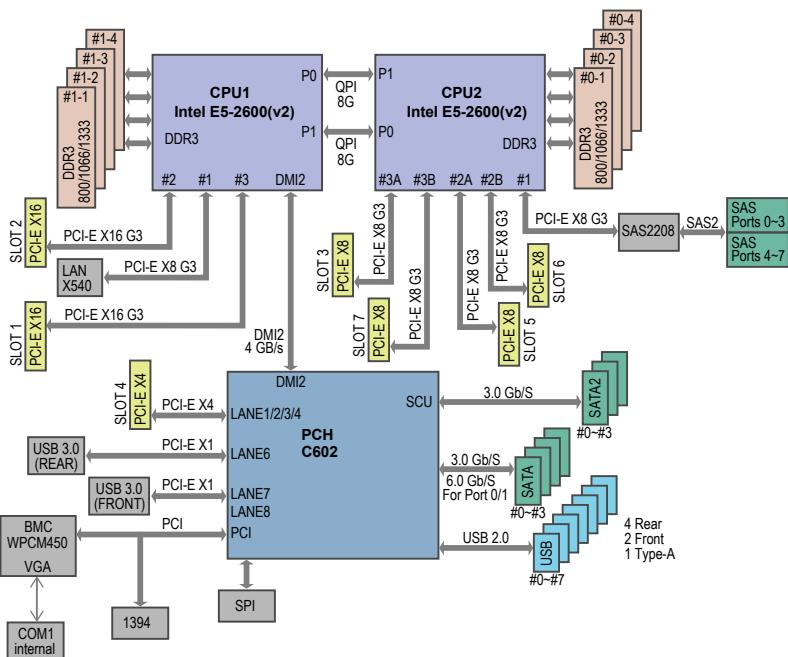
SAS Connections (For X9DAX-7F/-7TF Only)	
• SAS Ports	• Eight (8) SAS 2.0 Ports: L-SAS 0~3, 4~7
• RAID	RAID 0, 1, 5, 10

<b>Peripheral Devices</b>	<b>USB Devices (11 USB Connections)</b>
	<ul style="list-style-type: none"> <li>• Four (4) USB 3.0 ports: USB 3.0 Ports 0/1 (on the I/O Backplane), USB 3.0 USB 2/3 (close to SAS ports for front access),</li> <li>• Seven (7) USB 2.0 Connections: <ul style="list-style-type: none"> <li>• Four (4) Backplane USB 2.0 (Ports 0~3)</li> <li>• Two (2) Front-Accessible USB (USB 5/6 at J23)</li> <li>• One (1) Type A USB Header (USB 4 at J25) for front USB access</li> </ul> </li> </ul>
<b>I/O Devices</b>	<b>COM port header</b>
	<ul style="list-style-type: none"> <li>• One (1) COM port header for front access</li> </ul>
	<b>Audio</b>
	<ul style="list-style-type: none"> <li>• ALC 889 Audio Controller</li> <li>• Backpanel 7.1 High Definition Audio</li> <li>• Front Panel Audio Header (J18)</li> </ul>
<b>Power Connectors</b>	<b>ATX Power Connectors &amp; DOM Power Connector</b>
	<ul style="list-style-type: none"> <li>• One (1) 24-pin Main Power (JPW1)</li> <li>• Two (2) 8-pin CPU PWR Connectors (JPW2/JPW3)</li> <li>• DOM (Disk_On_Module) Power Connector for Serial_link devices (JSD1)</li> </ul>
<b>Super I/O</b>	<b>Super I/O</b>
	<ul style="list-style-type: none"> <li>• Winbond W83627</li> </ul>
<b>BIOS</b>	<ul style="list-style-type: none"> <li>• 16MB AMI BIOS<sup>®</sup> Flash EEPROM</li> <li>• APM 1.2, DMI 2.3, PCI 2.3, ACPI 1.0/2.0/3.0, USB Keyboard, Plug &amp; Play (PnP) and SMBIOS 2.3</li> </ul>
<b>Power Config.</b>	<ul style="list-style-type: none"> <li>• ACPI/ACPM Power Management</li> <li>• Main switch override mechanism</li> <li>• Keyboard Wake-up from Soft-Off</li> <li>• Power-on mode for AC power recovery</li> </ul>

<b>PC Health Monitoring</b>	<b>CPU Monitoring</b>
	<ul style="list-style-type: none"> <li>Onboard voltage monitors for 1.5V, 1.8V, +3.3V, 3.3V Standby, +5V, +5V Standby and Battery Voltage.</li> <li>CPU 6-Phase switching voltage regulator</li> <li>CPU/System overheat LED and control</li> <li>CPU Thermal Trip support</li> <li>Thermal Monitor 2 (TM2) support</li> <li>Intel® Intelligent Power Node Management (IPNM)</li> <li>Intel® Management Engine (ME)</li> </ul>
	<b>Fan Control</b>
	<ul style="list-style-type: none"> <li>Fan status monitoring with firmware thermal management via IPMI interface</li> <li>Low noise fan speed control</li> </ul>
	<b>LED Indicators</b>
	<ul style="list-style-type: none"> <li>System/CPU Overheat LED</li> <li>Suspend-state LED</li> <li>UID/Remote UID LED</li> </ul>
<b>System Management</b>	<ul style="list-style-type: none"> <li>PECI (Platform Environment Configuration Interface) 2.0 support</li> <li>System resource alert via SuperDoctor® III</li> <li>Dual Cooling Zones</li> <li>Thermal Monitor 2 (TM2) support</li> <li>PECI (Platform-Environment Configuration Interface) support</li> <li>SuperDoctor® III, Watch Dog, NMI</li> <li>Chassis Intrusion Header and Detection</li> </ul>
	<b>Dimensions</b>
	<ul style="list-style-type: none"> <li>13.68" (L) x 13.00" (W) (347.47 mm x 330.20 mm)</li> </ul>

 **Note 1:** For IPMI Configuration Instructions, please refer to the IPMI Configuration User's Guide available @ <http://www.supermicro.com/support/manuals/>.

**Note 2:** Changing BMC log-in information is recommended during initial system power-on. The default username is ADMIN and password is ADMIN. For BMC best practices, please refer to: [http://www.supermicro.com/products/info/files/IPMI/Best\\_Practices\\_BMC\\_Security.pdf](http://www.supermicro.com/products/info/files/IPMI/Best_Practices_BMC_Security.pdf)



### System Block Diagram

 **Notes:** 1. This is a general block diagram and may not exactly represent the features on your motherboard. See the Motherboard Features pages for the actual specifications of each motherboard. 2. This block diagram is intended for your reference only.

## 1-2 Processor and Chipset Overview

Built upon the functionality and the capability of the Intel E5-2600(v2) Series Processors (Socket R LGA 2011) and the C602 chipset, the X9DAX-iF/-7F/-iTF/-7TF motherboard provides the performance and feature sets required for dual\_processor-based high-end workstation platforms.

With support of Intel QuickPath interconnect (QPI) Technology, the X9DAX-iF/-7F/-iTF/-7TF offers point-to-point serial interconnect interface with a transfer speed of up to 8.0 GT/s, providing superb system performance.

The C602 chipset provides extensive IO support, including the following functions and capabilities:

- PCI-Express Rev. 2.0 support
- PCI-Express Gen. 3 uplink supported by some SKUs
- ACPI Power Management Logic Support Rev. 3.0b or Rev. 4.0
- USB host interface backplane and front access support
- Intel Rapid Storage Technology supported
- Intel Virtualization Technology for Directed I/O (Intel VT-d) supported
- Intel Trusted Execution Technology supported
- Serial Peripheral Interface (SPI) Supported
- Digital Media Interface (DMI) supported
- Advanced Host Controller Interface (AHCI) supported



**Note:** For Intel E5-2600(v2) processor support, BIOS version 3.0 or above is required.

## 1-3 Special Features

### Recovery from AC Power Loss

The Basic I/O System (BIOS) provides a setting that determines how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off (in which case you must press the power switch to turn it back on), or for it to automatically return to the power-on state. See the Advanced BIOS Setup section for this setting. The default setting is **Last State**.

## 1-4 PC Health Monitoring

This motherboard has an onboard System\_Hardware\_Monitor chip that supports PC health monitoring. An onboard voltage monitor will scan the following onboard voltages continuously: CPU1 Vcore, CPU2 Vcore, +1.8V, +12V, 1.5V, 3.3V, 3.3VSB, 5V, 5VSB, battery voltage, memory voltages and chipset voltage. Once a voltage becomes unstable, a warning is given, or an error message is sent to the screen. The user can adjust the voltage thresholds to define the sensitivity of the voltage monitor.

### Fan Status Monitor with Firmware Control

The PC health monitor chip can check the RPM status of a cooling fan. The onboard CPU and chassis fans are controlled by firmware thermal management via IPMI.

### Environmental Temperature Control

A thermal control sensor monitors the CPU temperature in real time and will turn on the thermal control fan whenever the CPU temperature exceeds a user-defined threshold. The overheat circuitry runs independently from the CPU. Once it detects that the CPU temperature is too high, it will automatically turn on the thermal fan control to prevent the CPU from overheating. The onboard chassis thermal circuitry can monitor the overall system temperature and alert the user when the chassis temperature is too high.

 **Note:** To avoid possible system overheating, please be sure to provide adequate airflow to your system.

### System Resource Alert

This feature is available when used with SuperDoctor® III in the Windows OS environment or used with SuperDoctor II in Linux. SuperDoctor is used to notify the user of certain system events. For example, you can configure SuperDoctor®

to provide you with warnings when the system/CPU temperatures, CPU voltages and fan speeds go beyond a predefined range.

## 1-5 ACPI Features

ACPI stands for Advanced Configuration and Power Interface. The ACPI specification defines a flexible and abstract hardware interface that provides a standard way to integrate power management features throughout a PC system, including its hardware, operating system and application software. This enables the system to automatically turn on and off peripheral devices such as CD-ROMs, network cards, hard disk drives and printers.

In addition to enabling operating\_system-directed power management, ACPI also provides a generic system event mechanism for Plug and Play, and an operating system-independent interface for configuration control. ACPI leverages the Plug and Play BIOS data structures, while providing a processor architecture-independent implementation that is compatible with Windows 2008, Windows Vista and Windows 7 Operating Systems.

### Slow Blinking LED for Suspend-State Indicator

When the CPU goes into a suspend state, the chassis power LED will start blinking to indicate that the CPU is in suspend mode. When the user presses any key, the CPU will "wake up" and the LED will automatically stop blinking and remain on.

## 1-6 Power Supply

As with all computer products, a stable power source is necessary for proper and reliable operation. It is even more important for processors that have high CPU clock rates.

The X9DAX-iF/-7F/-iTF/-7TF motherboard accommodates 24-pin ATX power supplies. Although most power supplies generally meet the specifications required by the CPU, some are inadequate. In addition, two 12V 8-pin power connections are also required to ensure adequate power supply to the system. Your power supply must also supply 1.5A for the Ethernet ports.

**Warning:** To prevent damage to the power supply or motherboard, please use a power supply that contains a 24-pin and two 8-pin power connectors. Be sure to connect these power supply connectors to the 24-pin (JPW1) and two 8-pin power connectors (JPW2, JPW3) on the motherboard. Failure to do so will void the manufacturer warranty on your power supply and motherboard.

It is strongly recommended that you use a high quality power supply that meets ATX power supply Specification 2.02 or above. It must also be SSI compliant. (For more

information, please refer to the website at <http://www.ssiforum.org/>). Additionally, in areas where noisy power transmission is present, you may choose to install a line filter to shield the computer from noise. It is recommended that you also install a power surge protector to help avoid problems caused by power surges.

## **1-7 Super I/O**

The Super I/O supports one high-speed, 16550 compatible serial communication ports (UARTs). Each UART includes a 16-byte send/receive FIFO, a programmable baud rate generator, complete modem control capability and a processor interrupt system. Each UART provides legacy speed with baud rate of up to 115.2 Kbps as well as an advanced speed with baud rates of 250 K, 500 K, or 1 Mb/s, which support higher speed modems.

The Super I/O provides functions that comply with ACPI (Advanced Configuration and Power Interface), which includes support of legacy and ACPI power management through an SMI or SCI function pin. It also features auto power management to reduce power consumption.

### **Intel<sup>®</sup> Intelligent Power Node Manager (IPNM)**

The Intel<sup>®</sup> Intelligent Power Node Manager (IPNM) provides your system with real-time thermal control and power management for maximum energy efficiency. Although IPNM is supported by the BMC (Baseboard Management Controller), your system must also have IPNM-compatible Management Engine (ME) firmware installed in your system for IPNM support.



**Note:** Support for IPNM Specification Version 1.5 or Version 2.0 depends on the power supply used in the system.

### **Management Engine (ME)**

The Management Engine, which is an ARC controller embedded in the PCH, provides Server Platform Services (SPS) to your system. The services provided by SPS are different from those provided by the ME on client platforms.

## **1-8 Overview of the Nuvoton WPCM450 Controller**

The Nuvoton WPCM450R Controller, a Baseboard Management Controller (BMC), supports 2D/VGA-compatible Graphic Cores with PCI interface, creating multi-media virtualization via Keyboard/Video/Mouse Redirection (KVMR). The WPCM450R Controller is ideal for remote system management.

The WPCM450R Controller interfaces with the host system via PCI connections to communicate with the graphics cores. It supports USB 2.0 and 1.1 for remote keyboard/mouse/virtual media emulation. It also provides LPC interface support to control Super IO functions. The WPCM450R Controller is connected to the network via an external Ethernet PHY module or shared NCSI connections.

The WPCM450R communicates with onboard components via six SMBus interfaces, PECI (Platform Environment Control Interface) buses, and General Purpose I/O ports.

## **WPCM450R DDR2 Memory Interface**

The WPCM450R supports a 16-bit DDR2 memory module with a speed of up to 220 MHz. For best signal integrity, the WPCM450R provides point-to-point connection.

## **WPCM450R PCI System Interface**

The WPCM450R provides 32-bit, 33 MHz 3.3V PCI interface, which is compliant with the PCI Local Bus Specification Rev. 2.3. The PCI system interface connects to the onboard PCI Bridge used by the graphics controller.

## **Other Features Supported by the WPCM BMC Controller**

The WPCM450R supports the following features:

- IPMI 2.0
- Serial over LAN
- KVM over LAN
- LAN Alerting-SNMP Trap
- Event Log
- X-Bus parallel interface for I/O expansion
- Multiple ADC inputs, Analog and Digital Video outputs
- SPI Flash Host BIOS and firmware bootstrap program supported
- Reduced Media Independent Interface (RMII)
- OS (Operating System) Independency

- Provides remote Hardware Health Monitoring via IPMI. Key features
- Provides Network Management Security via remote access/console redirection.
- Supports the following Management tools: IPMIView, CLI (Command Line Interface)
- RMCP+ protocol supported



**Note 1:** For more information on IPMI configuration, please refer to the IPMI User's Guide posted on our website at <http://www.supermicro.com/support/manuals/>.

**Note 2:** The term "IPMI controller" and the term "BMC controller" can be used interchangeably in this section.

**Note 3:** Changing BMC log-in information is recommended during initial system power-on. The default username is ADMIN and password is ADMIN. For BMC best practices, please refer to: [http://www.supermicro.com/products/info/files/IPMI/Best\\_Practices\\_BMC\\_Security.pdf](http://www.supermicro.com/products/info/files/IPMI/Best_Practices_BMC_Security.pdf)

# Chapter 2

## Installation

### 2-1 Standardized Warning Statements

The following statements are industry-standard warnings, provided to warn the user of situations which have the potential for bodily injury. Should you have questions or experience difficulty, contact Supermicro's Technical Support department for assistance. Only certified technicians should attempt to install or configure components.

Read this section in its entirety before installing or configuring components in the Supermicro chassis.

### Battery Handling



#### Warning!

There is a danger of explosion if the battery is replaced incorrectly. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions

電池の取り扱い

電池交換が正しく行われなかった場合、破裂の危険性があります。交換する電池はメーカーが推奨する型、または同等のものを使用下さい。使用済電池は製造元の指示に従って処分して下さい。

警告

电池更换不当会有爆炸危险。请只使用同类电池或制造商推荐的功能相当的电池更换原有电池。请按制造商的说明处理废旧电池。

警告

電池更換不當會有爆炸危險。請使用製造商建議之相同或功能相當的電池更換原有電池。請按照製造商的說明指示處理廢棄舊電池。

Warnung

Bei Einsetzen einer falschen Batterie besteht Explosionsgefahr. Ersetzen Sie die Batterie nur durch den gleichen oder vom Hersteller empfohlenen Batterietyp. Entsorgen Sie die benutzten Batterien nach den Anweisungen des Herstellers.

### Attention

Danger d'explosion si la pile n'est pas remplacée correctement. Ne la remplacer que par une pile de type semblable ou équivalent, recommandée par le fabricant. Jeter les piles usagées conformément aux instructions du fabricant.

### ¡Advertencia!

Existe peligro de explosión si la batería se reemplaza de manera incorrecta. Reemplazar la batería exclusivamente con el mismo tipo o el equivalente recomendado por el fabricante. Desechar las baterías gastadas según las instrucciones del fabricante.

**אזהרה !**

קיימת סכנת פיצוץ של הסוללה במידה והוחלפה בדרך לא תקינה. יש להחליף את הסוללה בסוג התואם מהחברה יצרן מומלצת.

סילוק הסוללות המשומשות יש לבצע לפי הוראות היצרן.

هناك خطر من انفجار في حالة استبدال البطارية بطريقة غير صحيحة فعليك استبدال البطارية

فقط بنفس النوع أو ما يعادلها كما أوصت به الشركة المصنعة  
تخلص من البطاريات المستعملة وفقاً لتعليمات الشركة الصانعة

### 경고!

배터리가 올바르게 교체되지 않으면 폭발의 위험이 있습니다. 기존 배터리와 동일하거나 제조사에서 권장하는 동등한 종류의 배터리로만 교체해야 합니다. 제조사의 안내에 따라 사용된 배터리를 처리하여 주십시오.

### Waarschuwing

Er is ontploffingsgevaar indien de batterij verkeerd vervangen wordt. Vervang de batterij slechts met hetzelfde of een equivalent type die door de fabrikant aanbevolen wordt. Gebruikte batterijen dienen overeenkomstig fabrieksvoorschriften afgevoerd te worden.

## Product Disposal



### Warning!

Ultimate disposal of this product should be handled according to all national laws and regulations.

#### 製品の廃棄

この製品を廃棄処分する場合、国の関係する全ての法律・条例に従い処理する必要があります。

#### 警告

本产品的废弃处理应根据所有国家的法律和规章进行。

#### 警告

本產品的廢棄處理應根據所有國家的法律和規章進行。

#### Warnung

Die Entsorgung dieses Produkts sollte gemäß allen Bestimmungen und Gesetzen des Landes erfolgen.

#### ¡Advertencia!

Al deshacerse por completo de este producto debe seguir todas las leyes y reglamentos nacionales.

#### Attention

La mise au rebut ou le recyclage de ce produit sont généralement soumis à des lois et/ou directives de respect de l'environnement. Renseignez-vous auprès de l'organisme compétent.

## סילוק המוצר

אזהרה !

סילוק סופי של מוצר זה חייב להיות בהתאם להנחיות וחוקי המדינה.

عند التخلص النهائي من هذا المنتج ينبغي التعامل معه وفقاً لجميع القوانين واللوائح الوطنية

#### Waarschuwing

De uiteindelijke verwijdering van dit product dient te geschieden in overeenstemming met alle nationale wetten en reglementen.

## 2-2 Static-Sensitive Devices

Electrostatic Discharge (ESD) can damage electronic components. To avoid damaging your system board, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

### Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing the board from the antistatic bag.
- Handle the board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure that your system chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.

### Unpacking

The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the board, make sure that the person handling it is static protected.

## 2-3 Processor and Heatsink Installation

**Warning:** When handling the processor package, avoid placing direct pressure on the label area.

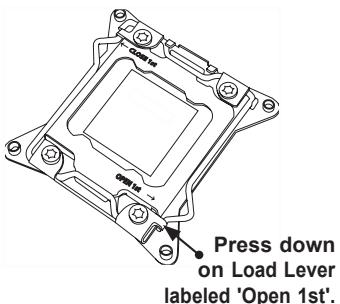
### Notes:

- Always connect the power cord last, and always remove it before adding, removing or changing any hardware components. Make sure that you install the processor into the CPU socket before you install the CPU heatsink.
- If you buy a CPU separately, make sure that you use an Intel-certified multi-directional heatsink only.
- Make sure to install the system board into the chassis before you install the CPU heatsink.
- When receiving a server board without a processor pre-installed, make sure that the plastic CPU socket cap is in place and none of the socket pins are bent; otherwise, contact your retailer immediately.
- Refer to the Supermicro website for updates on CPU support.

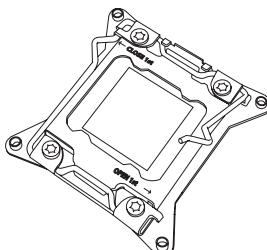
### Installing the LGA2011 Processor

1. There are two load levers on the LGA2011 socket. To open the socket cover, first press and release the load lever labeled 'Open 1st'.

**1**



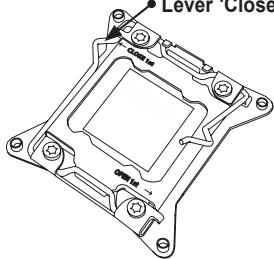
**2**



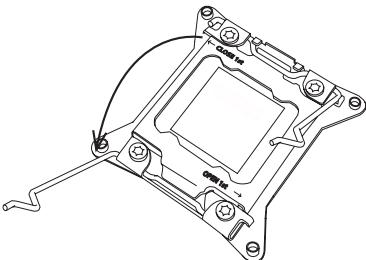
2. Press the second load lever labeled 'Close 1st' to release the load plate that covers the CPU socket from its locking position.

**1**

Press down on Load  
Lever 'Close 1st'



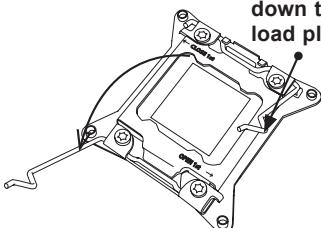
**2** Pull lever away from  
the socket



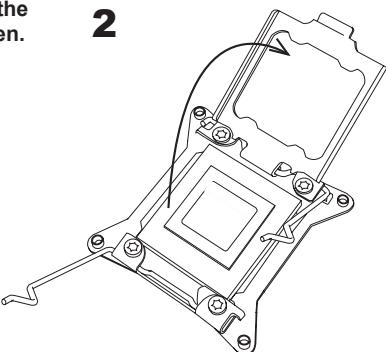
3. With the 'Close 1st' lever fully retracted, gently push down on the 'Open 1st' lever to open the load plate. Lift the load plate to open it completely.

**1**

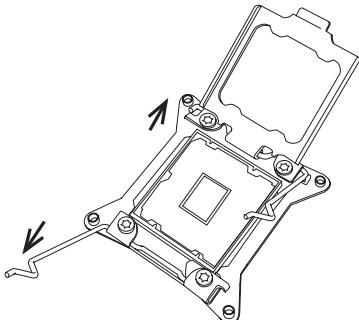
Gently push  
down to pop the  
load plate open.



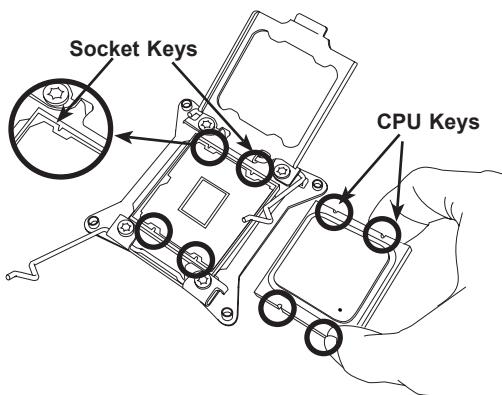
**2**



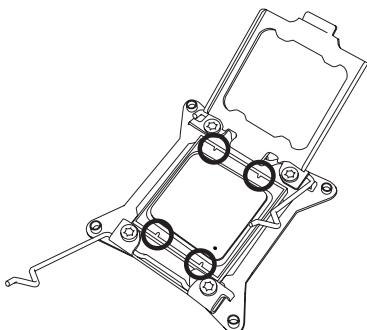
1. Using your thumb and the index finger, loosen the CPU lever and open the load plate.



2. Use your thumb and index finger to hold the CPU on its edges. Align the CPU keys, which are semi-circle cutouts, against the socket keys.



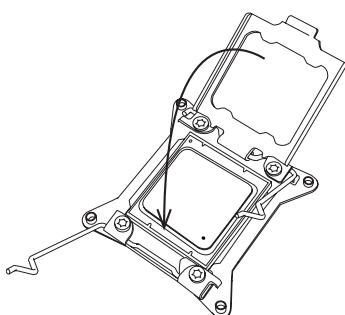
3. Once they are aligned, carefully lower the CPU straight down into the socket. (Do not drop the CPU on the socket. Do not move the CPU horizontally or vertically. Do not rub the CPU against the surface or against any pins of the socket to avoid damaging the CPU or the socket.)



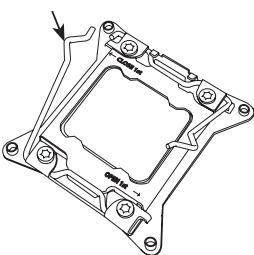
**Warning:** You can only install the CPU inside the socket in one direction. Make sure that it is properly inserted into the CPU socket before closing the load plate. If it doesn't close properly, do not force it as it may damage your CPU. Instead, open the load plate again and double-check that the CPU is aligned properly.

4. With the CPU inside the socket, inspect the four corners of the CPU to make sure that the CPU is properly installed.
5. Close the load plate with the CPU inside the socket. Lock the 'Close 1st' lever first, then lock the 'Open 1st' lever second. Use your thumb to gently push the load levers down to the lever locks.

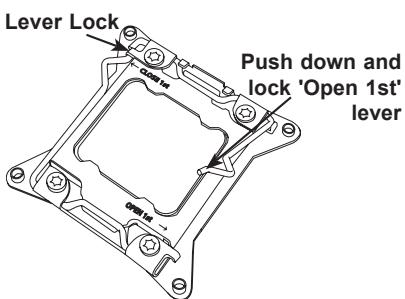
**1** Gently close the load plate.



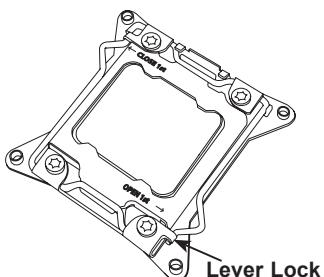
**2** Push down and lock 'Close 1st' lever.



**3**

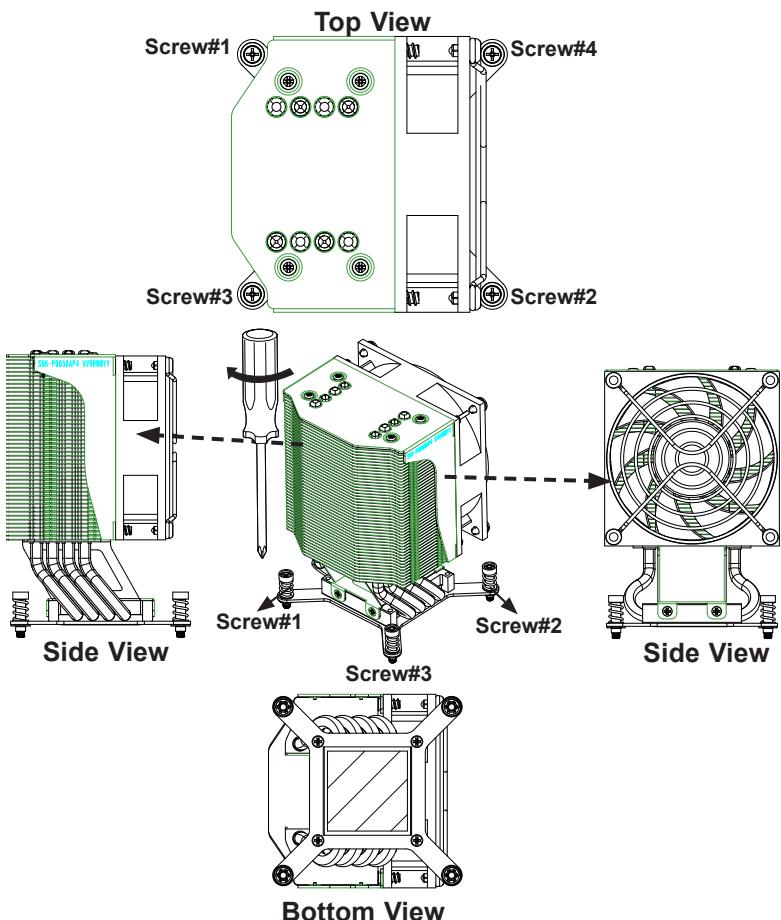


**4**



## Installing a Passive CPU Heatsink

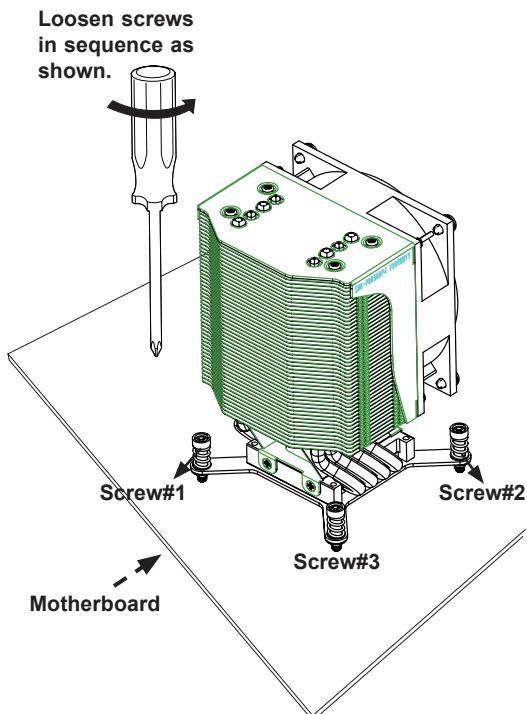
1. Do not apply any thermal grease to the heatsink or the CPU die -- the required amount has already been applied.
2. Place the heatsink on top of the CPU so that the four mounting holes are aligned with those on the Motherboard's and the Heatsink Bracket underneath.
3. Screw in two diagonal screws (i.e., the #1 and the #2 screws) until just snug (-do not over-tighten the screws to avoid possible damage to the CPU.)
4. Finish the installation by fully tightening all four screws.



## Removing the Heatsink

**Warning:** We do not recommend that the CPU or the heatsink be removed. However, if you do need to uninstall the heatsink, please follow the instructions below to uninstall the heatsink to prevent damage done to the CPU or the CPU socket.

1. Unscrew the heatsink screws from the motherboard in the sequence as shown in the illustration below.
2. Gently wriggle the heatsink to loosen it from the CPU. (Do not use excessive force when wriggling the heatsink!)
3. Once the CPU is loosened, remove the CPU from the CPU socket.
4. Remove the used thermal grease and clean the surface of the CPU and the heatsink. Reapply the proper amount of thermal grease on the surface before reinstalling the CPU and the heatsink.



**Notes:** 1. For optimized airflow, please follow your chassis airflow direction to install the correct CPU heatsink direction. 2. Graphic drawings included in this manual are for reference only. They might look different from the components installed in your system

## 2-4 Installing and Removing the Memory Modules

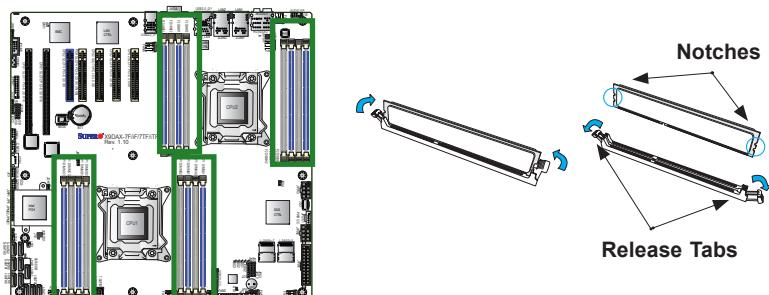
 **Note:** Check Supermicro's website for recommended memory modules.

### CAUTION

Exercise extreme care when installing or removing DIMM modules to prevent any possible damage.

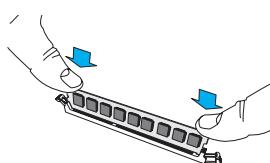
### Installing & Removing DIMMs

1. Insert the desired number of DIMMs into the memory slots, starting with P1-DIMMA1. (For best performance, please use the memory modules of the same type and the same speed.)
2. Push the release tabs outwards on both ends of the DIMM slot to unlock it.



3. Align the key of the DIMM module with the receptive point on the memory slot.
4. Align the notches on both ends of the module against the receptive points on the ends of the slot.
5. Use two thumbs together to press the notches on both ends of the module straight down into the slot until the module snaps into place.
6. Press the release tabs to the locking positions to secure the DIMM module into the slot.

Press both notches straight down into the memory slot at the same time.



### Removing Memory Modules

Press both notches on the ends of the DIMM module to unlock it. Once the DIMM module is loosened, remove it from the memory slot.

## Memory Support for the X9DAX-iF/-7F/-iTF/-7TF Motherboard

The X9DAX-iF/-7F/-iTF/-7TF motherboard supports up to 1 TB of Load Reduced (LRDIMM), 512 GB of Registered (RDIMM) or 128 GB of Unbuffered (UDIMM) ECC/Non-ECC DDR3 800/1066/1333/1600/1866 MHz 240-pin 4-channel memory modules in 16 DIMM slots.



**Note:** For the latest memory updates, please refer to the Tested Memory List posted on our website (<http://www.supermicro.com/products/motherboard>).

### Processor & Memory Module Population Configuration

For memory to work properly, follow the tables below for memory population.

Processors and their Corresponding Memory Modules								
CPU#	Corresponding DIMM Modules							
CPU 1	P1-DIMMA1	P1-DIMMB1	P1-DIMMC1	P1-DIMMD1	P1-DIMMA2	P1-DIMMB2	P1-DIMMC2	P1-DIMMD2
CPU2	P2-DIMME1	P2-DIMMF1	P2-DIMMG1	P2-DIMMH1	P2-DIMME2	P2-DIMMF2	P2-DIMMG2	P2-DIMMH2

### Processor and Memory Module Population for Optimal Performance

Number of CPUs+DIMMs	CPU and Memory Population Configuration Table (*For memory to work properly, follow the instructions below to install DIMMs.)
1 CPU & 2 DIMMs	CPU1 P1-DIMMA1/P1-DIMMB1
1 CPU & 4 DIMMs	CPU1 P1-DIMMA1/P1-DIMMB1, P1-DIMMC1/P1-DIMMD1
1 CPU & 5~8 DIMMs	CPU1 P1-DIMMA1/P1-DIMMB1, P1-DIMMC1/P1-DIMMD1 + Any memory pairs in P1-DIMMA2/P1-DIMMB2/P1-DIMMC2/P1-DIMMD2 slots
2 CPUs & 4 DIMMs	CPU1 + CPU2 P1-DIMMA1/P1-DIMMB1, P2-DIMME1/P2-DIMMF1
2 CPUs & 6 DIMMs	CPU1 + CPU2 P1-DIMMA1/P1-DIMMB1/P1-DIMMC1/P1-DIMMD1, P2-DIMME1/P2-DIMMF1
2 CPUs & 8 DIMMs	CPU1 + CPU2 P1-DIMMA1/P1-DIMMB1/P1-DIMMC1/P1-DIMMD1, P2-DIMME1/P2-DIMMF1/P2-DIMMG1/P2-DIMMH1
2 CPUs & 10~16 DIMMs	CPU1/CPU2 P1-DIMMA1/P1-DIMMB1/P1-DIMMC1/P1-DIMMD1, P2-DIMME1/P2-DIMMF1/P2-DIMMG1/P2-DIMMH1 + Any memory pairs in P1, P2 DIMM slots
2 CPUs & 16 DIMMs	CPU1/CPU2 P1-DIMMA1/P1-DIMMB1/P1-DIMMC1/P1-DIMMD1, P2-DIMME1/P2-DIMMF1/P2-DIMMG1/P2-DIMMH1, P1-DIMMA2/P1-DIMMB2/P1-DIMMC2/P1-DIMMD2, P2-DIMME2/P2-DIMMF2/P2-DIMMG2/P2-DIMMH2



**Notes:** 1866 MHz memory speed is dependent on Intel E5-2600v2 CPUs. For Intel E5-2600(v2) processor support, BIOS version 3.0 or above is required.

## Populating UDIMM (ECC/Non-ECC) Memory Modules

Intel E5-2600(v2) Series Processor UDIMM Memory Support											
Ranks Per DIMM & Data Width	Memory Capacity Per DIMM (See the Note below)			Speed (MT/s) and Voltage Validated by Slot per Channel (SPC) and DIMM Per Channel (DPC)							
				2 Slots Per Channel				3 Slots Per Channel			
				1DPC		2DPC		1DPC		2DPC	
				1.35V	1.5V	1.35V	1.5V	1.35V	1.5V	1.35V	1.5V
SRx8 Non-ECC	1GB	2GB	4GB	NA	1066, 1333, 1600, 1866	NA	1066, 1333, 1600	N/A	1066, 1333, 1600, 1866	N/A	1066, 1333, 1600
DRx8 Non-ECC	2GB	4GB	8GB	NA	1066, 1333, 1600, 1866	NA	1066, 1333, 1600	N/A	1066, 1333, 1600, 1866	N/A	1066, 1333, 1600
SRx16 Non-ECC	512MB	1GB	2GB	NA	1066, 1333, 1600, 1866	NA	1066, 1333, 1600	N/A	1066, 1333, 1600, 1866	N/A	1066, 1333, 1600
SRx8 ECC	1GB	2GB	4GB	1066, 1333	1066, 1333, 1600, 1866	1066, 1333	1066, 1333, 1600	1066, 1333	1066, 1333, 1600, 1866	1066, 1333	1066, 1333, 1600
DRx8 ECC	2GB	4GB	8GB	1066, 1333	1066, 1333, 1600, 1866	1066, 1333	1066, 1333, 1600	1066, 1333	1066, 1333, 1600, 1866	1066, 1333	1066, 1333, 1600

**Note:** For detailed information on memory support and updates, please refer to the SMC Recommended Memory List posted on our website at <http://www.supermicro.com/support/resources/mem.cfm>.

## Populating RDIMM (ECC) Memory Modules

Intel E5-2600(v2) Series Processor RDIMM Memory Support											
Ranks Per DIMM & Data Width	Memory Capacity Per DIMM (See the Note Below)			Speed (MT/s) and Voltage Validated by Slot per Channel (SPC) and DIMM Per Channel (DPC)							
				2 Slots Per Channel				3 Slots Per Channel			
				1DPC		2DPC		1 DPC		2DPC	
				1.35V	1.5V	1.35V	1.5V	1.35V	1.5V	1.35V	1.5V
SRx8	1GB	2GB	4GB	1066, 1333	1066, 1333, 1600, 1866	1066, 1333	1066, 1333, 1600	1066, 1333	1066, 1333, 1600, 1866	1066, 1333	800, 1066
DRx8	2GB	4GB	8GB	1066, 1333	1066, 1333, 1600, 1866	1066, 1333	1066, 1333, 1600	1066, 1333	1066, 1333, 1600, 1866	1066, 1333	800, 1066
SRx4	2GB	4GB	8GB	1066, 1333	1066, 1333, 1600, 1866	1066, 1333	1066, 1333, 1600	1066, 1333	1066, 1333, 1600, 1866	1066, 1333	800, 1066
DRx4	4GB	8GB	16GB	1066, 1333	1066, 1333, 1600, 1866	1066, 1333	1066, 1333, 1600	1066, 1333	1066, 1333, 1600, 1866	1066, 1333	800, 1066
QRx4	8GB	16GB	32GB	800	800, 1066	800	800	800	800, 1066	800	N/A
QRx8	4GB	8GB	16GB	800	800, 1066	800	800	800	800, 1066	800	N/A

**Note:** For detailed information on memory support and updates, please refer to the SMC Recommended Memory List posted on our website at <http://www.supermicro.com/support/resources/mem.cfm>.

## Populating UDIMM (ECC/Non-ECC) Memory Modules

Intel E5-2600 Series Processor UDIMM Memory Support											
Ranks Per DIMM & Data Width	(See the Note below)			Speed (MT/s) and Voltage Validated by Slot per Channel (SPC) and DIMM Per Channel (DPC)							
				2 Slots Per Channel				3 Slots Per Channel			
				1DPC		2DPC		1DPC		2DPC	
				1.35V	1.5V	1.35V	1.5V	1.35V	1.5V	1.35V	1.5V
SRx8 Non-ECC	1GB	2GB	4GB	NA	1066, 1333	NA	1066, 1333	N/A	1066, 1333	N/A	1066, 1333
DRx8 Non-ECC	2GB	4GB	8GB	NA	1066, 1333	NA	1066, 1333	N/A	1066, 1333	N/A	1066, 1333
SRx16 Non-ECC	512MB	1GB	2GB	NA	1066, 1333	NA	1066, 1333	N/A	1066, 1333	N/A	1066, 1333
SRx8 ECC	1GB	2GB	4GB	1066, 1333	1066, 1333	1066	1066, 1333	1066	1066, 1333	1066, 1333	1066, 1333
DRx8 ECC	2GB	4GB	8GB	1066, 1333	1066, 1333	1066	1066, 1333	1066	1066, 1333	1066, 1333	1066, 1333
Note: For detailed information on memory support and updates, please refer to the SMC Recommended Memory List posted on our website at <a href="http://www.supermicro.com/support/resources/mem.cfm">http://www.supermicro.com/support/resources/mem.cfm</a> .											

## Populating RDIMM (ECC) Memory Modules

Intel E5-2600 Series Processor RDIMM Memory Support											
Ranks Per DIMM & Data Width	(See the Note Below)			Speed (MT/s) and Voltage Validated by Slot per Channel (SPC) and DIMM Per Channel (DPC)							
				2 Slots Per Channel				3 Slots Per Channel			
				1DPC		2DPC		1DPC		2DPC	
				1.35V	1.5V	1.35V	1.5V	1.35V	1.5V	1.35V	1.5V
SRx8	1GB	2GB	4GB	1066, 1333	1066, 1333, 1600	1066, 1333	1066, 1333, 1600	1066, 1333	1066, 1333, 1600	1066, 1333, 1600	N/A
DRx8	2GB	4GB	8GB	1066, 1333	1066, 1333, 1600	1066, 1333	1066, 1333, 1600	1066, 1333	1066, 1333, 1600	1066, 1333, 1600	N/A
SRx4	2GB	4GB	8GB	1066, 1333	1066, 1333, 1600	1066, 1333	1066, 1333, 1600	1066, 1333	1066, 1333, 1600	1066, 1333, 1600	N/A
DRx4	4GB	8GB	16GB	1066, 1333	1066, 1333, 1600	1066, 1333	1066, 1333, 1600	1066, 1333	1066, 1333, 1600	1066, 1333, 1600	N/A
QRx4	8GB	16GB	32GB	800	1066	800	800	800	1066	800	800
QRx8	4GB	8GB	16GB	800	1066	800	800	800	1066	800	N/A
Note: For detailed information on memory support and updates, please refer to the SMC Recommended Memory List posted on our website at <a href="http://www.supermicro.com/support/resources/mem.cfm">http://www.supermicro.com/support/resources/mem.cfm</a> .											

### Populating LRDIMM (ECC) Memory Modules

Intel E5-2600(v2) Series Processor LRDIMM Memory Support												
Ranks Per DIMM & Data Width	Memory Capacity Per DIMM (See the Note Below)	Speed (MT/s) and Voltage Validated by Slot per Channel (SPC) and DIMM Per Channel (DPC)										
		2 Slots Per Channel				3 Slots Per Channel						
		1DPC		2DPC		1DPC		2DPC		3DPC		
		1.35V	1.5V	1.35V	1.5V	1.35V	1.5V	1.35V	1.5V	1.35V	1.5V	
QRx4 (DDP)	16GB	32GB	1066, 1333, 1600	1066, 1333, 1600	1066, 1333, 1600	1066, 1333, 1600	1066, 1333, 1600	1066, 1333, 1600	1066, 1333, 1600	1066	1066	
8Rx4 (QDP)	32GB	64GB	1066	1066	1066	1066	1066	1066	1066	1066	1066	

**Note:** For detailed information on memory support and updates, please refer to the SMC Recommended Memory List posted on our website at <http://www.supermicro.com/support/resources/mem.cfm>.

Intel E5-2600 Series Processor LRDIMM Memory Support												
Ranks Per DIMM & Data Width	Memory Capacity Per DIMM (See the Note Below)	Speed (MT/s) and Voltage Validated by Slot per Channel (SPC) and DIMM Per Channel (DPC)										
		2 Slots Per Channel				3 Slots Per Channel						
		1DPC		2DPC		1DPC		2DPC		3DPC		
		1.35V	1.5V	1.35V	1.5V	1.35V	1.5V	1.35V	1.5V	1.35V	1.5V	
QRx4 (DDP)	16GB	32GB	1066	1066, 1333	1066	1066, 1333	1066	1066, 1333	1066	1066	1066	
QRx8 (QDP)	8GB	16GB	1066	1066, 1333	1066	1066, 1333	1066	1066, 1333	1066	1066	1066	

**Note:** For detailed information on memory support and updates, please refer to the SMC Recommended Memory List posted on our website at <http://www.supermicro.com/support/resources/mem.cfm>.



#### Other Important Notes and Restrictions

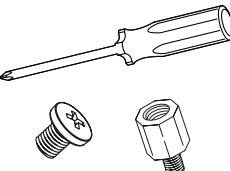
- For the memory modules to work properly, please install DIMM modules of the same type, same speed and same operating frequency on the motherboard. Mixing of RDIMMs, UDIMMs or LRDIMMs is not allowed. Do not install both ECC and Non-ECC memory modules on the same motherboard.
- Using DDR3 DIMMs with different operating frequencies is not allowed. All channels in a system will run at the lowest common frequency.

## 2-5 Motherboard Installation

All motherboards have standard mounting holes to fit different types of chassis. Make sure that the locations of all the mounting holes for both motherboard and chassis match. Although a chassis may have both plastic and metal mounting fasteners, metal ones are highly recommended because they ground the motherboard to the chassis. Make sure that the metal standoffs click in or are screwed in tightly. Then use a screwdriver to secure the motherboard onto the motherboard tray.

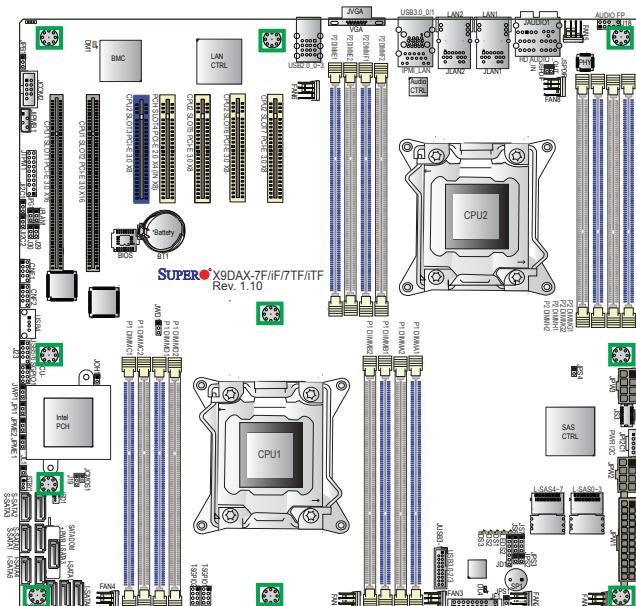
### Tools Needed

- Phillips Screwdriver
- Pan head screws (10 pieces)
- Standoffs (10 pieces, if needed)



### Location of Mounting Holes

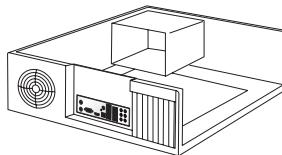
There are ten (10) mounting holes on this motherboard indicated by the arrows.



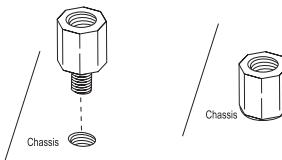
**Caution:** 1) To avoid damaging the motherboard and its components, please do not use a force greater than 8 lb/inch on each mounting screw during motherboard installation. 2) Some components are very close to the mounting holes. Please take precautionary measures to prevent damage to these components when installing the motherboard to the chassis.

## Installing the Motherboard

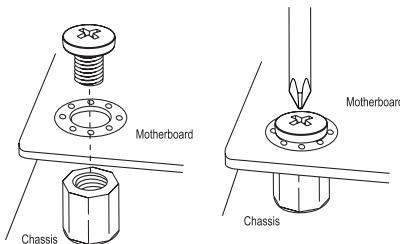
1. Install the I/O shield into the chassis.



2. Locate the mounting holes on the motherboard.
3. Locate the matching mounting holes on the chassis. Align the mounting holes on the motherboard against the mounting holes on the chassis.
4. Install standoffs in the chassis as needed.



5. Install the motherboard into the chassis carefully to avoid damaging motherboard components.
6. Using the Phillips screwdriver, insert a Pan head #6 screw into a mounting hole on the motherboard and its matching mounting hole on the chassis.



7. Repeat Step 5 to insert #6 screws into all mounting holes.
8. Make sure that the motherboard is securely placed in the chassis.

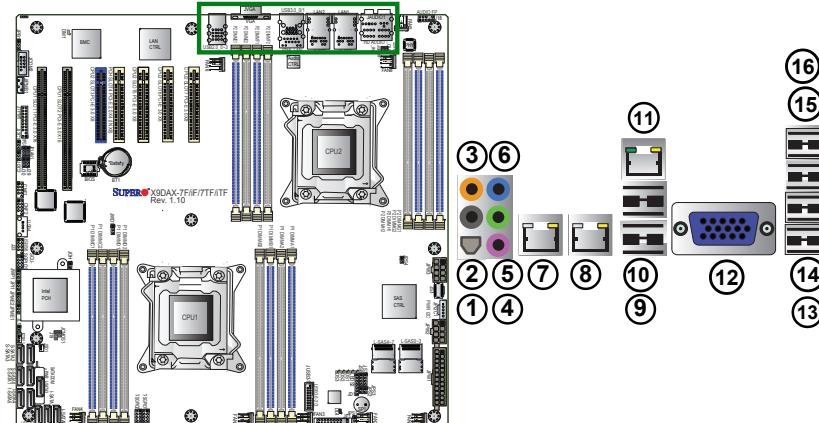


**Note:** Images displayed are for illustration only. Your chassis or components might look different from those shown in this manual.

## 2-6 Control Panel Connectors and I/O Ports

The I/O ports are color coded in conformance with the PC 99 specification. See the picture below for the colors and locations of the various I/O ports.

### Back Panel Connectors and I/O Ports



### Back Panel I/O Port Locations and Definitions

1. SPDIF_Out
2. Surround_Out
3. CEN/LFE_Out
4. Mic_In
5. Line_Out
6. Line_In
7. Gigabit LAN 1 (X9DAX-iF/7F), 10G LAN 1 (X9DAX-iTF/7TF)
8. Gigabit LAN 2 (X9DAX-iF/7F), 10G LAN 2 (X9DAX-iTF/7TF)
9. Back Panel USB 3.0 Port 0
10. Back Panel USB 3.0 Port 1
11. IPMI LAN
12. VGA
13. Back Panel USB 2.0 Port 0
14. Back Panel USB 2.0 Port 1
15. Back Panel USB 2.0 Port 2
16. Back Panel USB 2.0 Port 3

## Video Connection

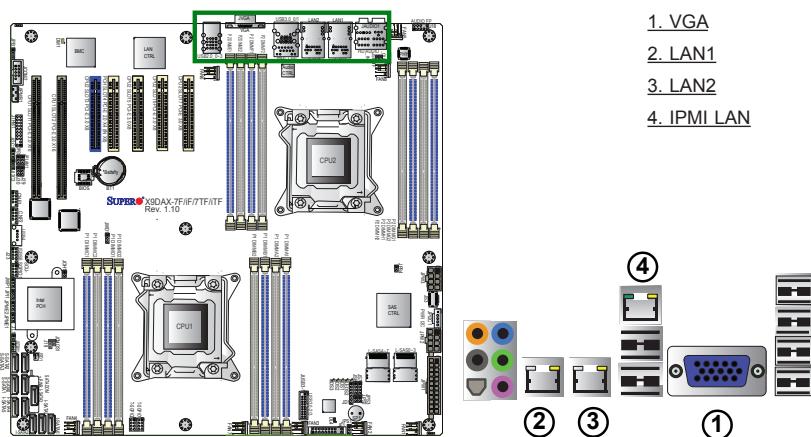
A Video (VGA) port is located next to USB 3.0 ports 0/1 on the I/O backplane. Refer to the board layout below for the location.

## Ethernet Ports

Two Ethernet ports (LAN1/2) are located on the I/O backplane on the motherboard. These ports support Gigabit LAN connections on the X9DAX-iF/7F, and support 10 GLAN connections on the X9DAX-iTF/7TF. All these ports accept RJ45 type cables. In addition, an IPMI LAN is located on USB Ports 0/1 to provide KVM support for IPMI interface. (**Note:** Please refer to the LED Indicator Section for LAN LED information.)

LAN Ports Pin Definition			
Pin#	Definition	Pin#	Definition
1	P2V5SB	10	SGND
2	TD0+	11	Act LED
3	TD0-	12	P3V3SB
4	TD1+	13	Link 100 LED (Yellow, +3V3SB)
5	TD1-	14	Link 1000 LED (Yellow, +3V3SB)
6	TD2+	15	Ground
7	TD2-	16	Ground
8	TD3+	17	Ground
9	TD3-	18	Ground

(NC: No Connection)



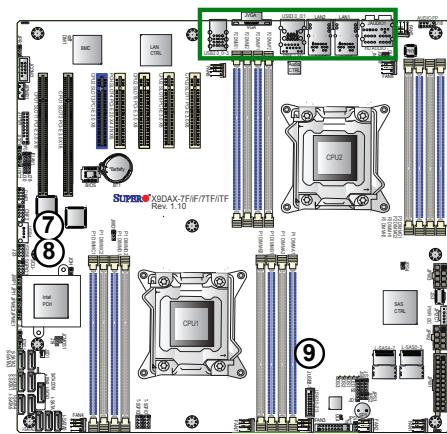
## Universal Serial Bus (USB)

Four Universal Serial Bus 2.0 ports (USB 2.0 Ports 0~3), and two USB 3.0 Ports (USB 3.0 Ports 0/1) are located on the I/O backplane. In addition, two USB 2.0 ports (USB 5/6), and a Type A USB connector (USB4), located close to the Intel PCH chip, provides front USB access. Also, two USB 3.0 ports (USB 3.0 Ports 2/3) are located next to Fan 3 to provide front access. USB Cables are not included. See the tables on the right for pin definitions.

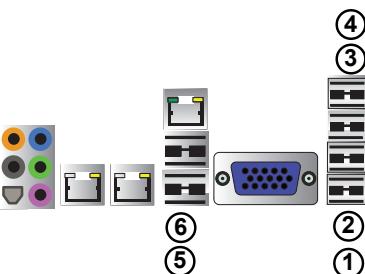
Back Panel USB (2.0) #0~3 Pin Definitions			
Pin#	Definition	Pin#	Definition
1	+5V	5	+5V
2	USB_PN1	6	USB_PN0
3	USB_PP1	7	USB_PP0
4	Ground	8	Ground

Back Panel USB (3.0) #0/1, Front Accessible USB (3.0) #2/3 Pin Definitions			
Pin#	Pin#	Signal Name	Description
1	10	VBUS	Power
2	11	D-	USB 2.0 Differential Pair
3	12	D+	
4	13	Ground	Ground of PWR Return
5	14	StdA_SSRX-	SuperSpeed Receiver
6	15	StdA_SSRX+	Differential Pair
7	16	GND_DRAIN	Ground for Signal Return
8	17	StdA_SSTX-	SuperSpeed Transmitter
9	18	StdA_SSTX+	Differential Pair

Front Panel USB (2.0) #4, 5/6 Pin Definitions			
USB 5 Pin #	Definition	USB 4/6 Pin #	Definition
1	+5V	2	+5V
3	USB_PN2	4	USB_PN3
5	USB_PP2	6	USB_PP3
7	Ground	8	Ground
9	Key	10	Ground



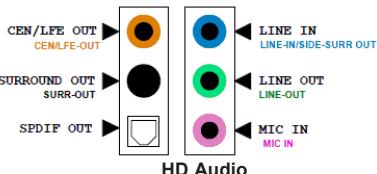
1. Backpanel USB 2.0 Port 0
2. BP USB 2.0 Port 1
3. BP USB 2.0 Port 2
4. BP USB 2.0 Port 3
5. BP USB 3.0 Port 0
6. BP USB 3.0 Port 1
7. FP USB 2.0 Port 4
8. FP USB 2.0 Port 5/6
9. FP USB 3.0 Port 2/3



### (Back\_Panel) High Definition Audio (HD Audio)

This motherboard features a 7.1 Channel High Definition Audio (HDA) codec connector that provides 8 DAC channels. The HD Audio connector supports multiple-streaming 7.1 sound playback and independent stereo output through the front-panel stereo-out connection to the front, rear, center and subwoofer speakers. Use the software program included in the CD-ROM with your motherboard to enable this function.

(BP) HD Audio	
Conn#	Signal
1	SPDIF_Out
2	Surround_Out
3	CEN/LFE_Out
4	Mic_In
5	Line_Out
6	Line_In

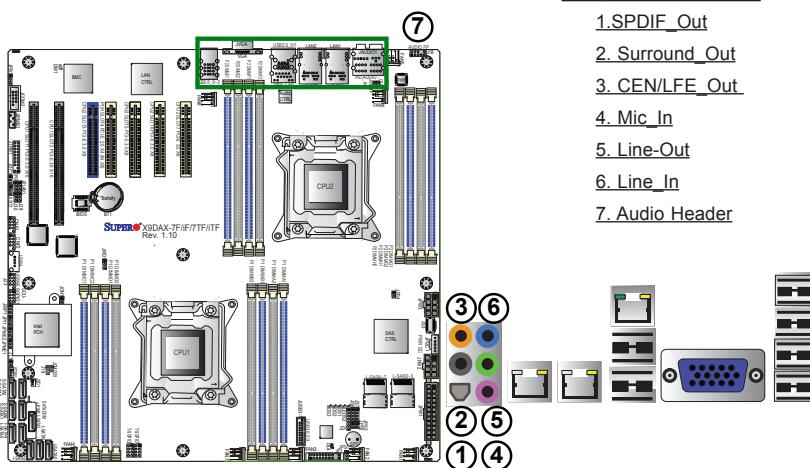


### Front Accessible Audio Header

A 10-pin Audio header (J18) is also located on the motherboard. This header allows you to use the onboard sound for audio playback. Connect an audio cable to the audio header to use this feature. See the table on the right for pin definitions for these headers.

10-Pin Audio Pin Definitions	
Pin#	Signal
1	Microphone_Left
2	Audio_Ground
3	Microphone_Right
4	Audio_Detect
5	Line_2_Right
6	Ground
7	Jack_Detect
8	Key
9	Line_2_Left
10	Ground

1. SPDIF\_Out
2. Surround\_Out
3. CEN/LFE\_Out
4. Mic\_In
5. Line\_Out
6. Line\_In
7. Audio Header



### SPDIF\_In/SPDIF\_Out Headers

The SPDIF\_In (JSPDIF\_In) and SPDIF\_Out (JSPDIF\_Out) headers are located next to Fan 8 on the motherboard. Place a cap on each header for audio support. You will also need to have a cable to use each connection.

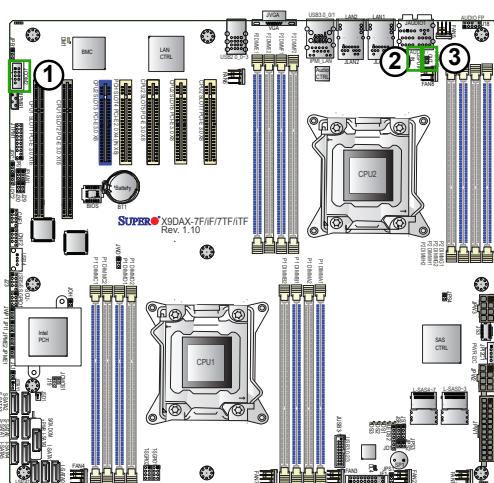
SPDIF_In Pin Definitions	
Pin#	Definition
1	S/PDIF_In
2	Ground

SPDIF_Out Pin Definitions	
Pin#	Definition
1	S/PDIF_Out
2	Ground

### COM Port Header

A COM port header (JCOM2) is located close to CPU1 Slot1 (PCI-E 3.0x16) on the motherboard. This connection provides COM connection support. See the table on the right for pin definitions.

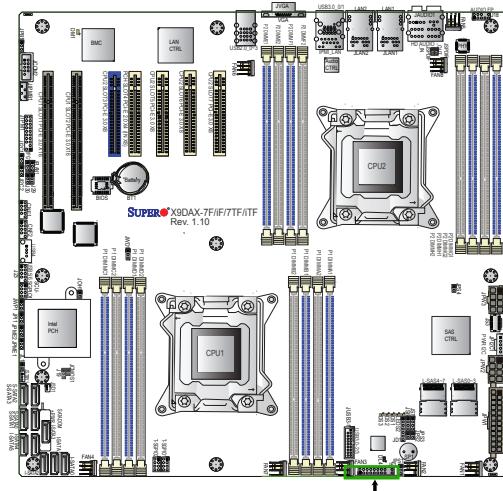
COM Port Header Pin Definitions			
Pin #	Definition	Pin #	Definition
1	DCD	6	CTS
2	DSR	7	DTR
3	RXD	8	RI
4	RTS	9	Ground
5	TXD	10	N/A



1. COM2
2. SPDIF-In Header
3. SPDIF-Out Header

## Front Control Panel

JF1 contains header pins for various buttons and indicators that are normally located on a control panel at the front of the chassis. These connectors are designed specifically for use with Supermicro's server chassis. See the figure below for the descriptions of the various control panel buttons and LED indicators. Refer to the following section for descriptions and pin definitions.



**JF1 Header Pins**

	20	19	
Ground	○	○	NMI
X	○	○	X
Power LED	○	○	Vcc
HDD LED	○	○	Vcc
NIC1 LED	○	○	Vcc
NIC2 LED	○	○	Vcc
OH/Fan Fail LED	○	○	Vcc
PWR Fail LED	○	○	Vcc
Ground	○	○	Reset Button
Ground	○	○	PWR Power Button

## Front Control Panel Pin Definitions

### NMI Button

The non-maskable interrupt button header is located on pins 19 and 20 of JF1. Refer to the table on the right for pin definitions.

NMI Button Pin Definitions (JF1)	
Pin#	Definition
19	Control
20	Ground

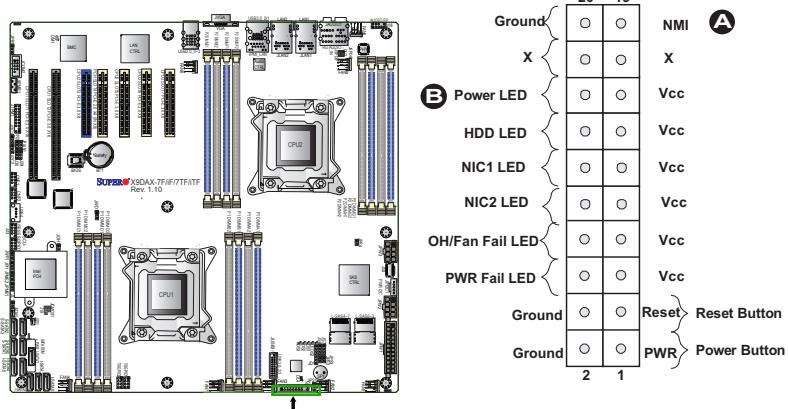
### Power LED

The Power LED connection is located on pins 15 and 16 of JF1. Refer to the table on the right for pin definitions.

Power LED Pin Definitions (JF1)	
Pin#	Definition
15	3.3V
16	PWR LED

A. NMI

B. PWR LED



## HDD LED

The HDD LED connection is located on pins 13 and 14 of JF1. Attach a cable here to indicate HDD activity. See the table on the right for pin definitions.

HDD LED Pin Definitions (JF1)	
Pin#	Definition
13	3.3V Standby
14	HD Active

## NIC1/NIC2 LED Indicators

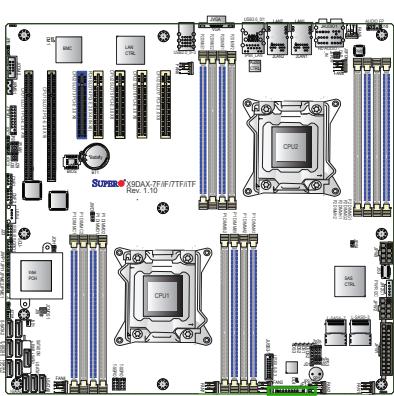
The NIC (Network Interface Controller) LED connection for GLAN port 1 is located on pins 11 and 12 of JF1, and the LED connection for GLAN Port 2 is on Pins 9 and 10. Attach the NIC LED cables here to display network activity. Refer to the table on the right for pin definitions.

GLAN1/2 LED Pin Definitions (JF1)	
Pin#	Definition
9	NIC 2 Activity LED
10	NIC 2 Link LED
11	NIC 1 Activity LED
12	NIC 1 Link LED

A. HDD LED

B. NIC1 LED

C. NIC2 LED



	20	19	
Ground	○	○	NMI
X	○	○	X
Power LED	○	○	Vcc
<b>A</b> HDD LED	○	○	Vcc
<b>B</b> NIC1 LED	○	○	Vcc
<b>C</b> NIC2 LED	○	○	Vcc
OH/Fan Fail LED	○	○	Vcc
PWR Fail LED	○	○	Vcc
Ground	○	○	Reset
Ground	○	○	Power Button
	2	1	

## Overheat (OH)/Fan Fail LED

Connect an LED cable to pins 7 and 8 of JF1 to provide advanced warnings of chassis overheating and fan failure. Refer to the table on the right for pin definitions.

OH/Fan Fail/PWR Fail LED Pin Definitions (JF1)	
Pin#	Definition
7	Vcc
8	OH/Fan Fail LED

OH/Fan Fail Indicator Status	
State	Definition
Off	Normal
On	Overheat
Flashing	Fan Fail

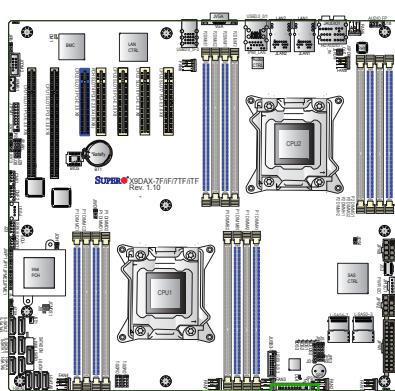
## Power Fail LED

The Power Fail LED connection is located on pins 5 and 6 of JF1. Refer to the table on the right for pin definitions.

PWR Fail LED Pin Definitions (JF1)	
Pin#	Definition
5	3.3V
6	PWR Supply Fail

A. OH/Fail Fail LED

B. PWR Supply Fail



	20	19	
Ground	○	○	NMI
X	○	○	X
Power LED	○	○	Vcc
HDD LED	○	○	Vcc
NIC1 LED	○	○	Vcc
NIC2 LED	○	○	Vcc
<b>A</b> OH/Fan Fail LED	○	○	Vcc
<b>B</b> PWR Fail LED	○	○	Vcc
Ground	○	○	Reset
Ground	○	○	Power
	2	1	

## Reset Button

The Reset Button connection is located on pins 3 and 4 of JF1. Attach it to a hardware reset switch on the chassis to use this feature. Refer to the table on the right for pin definitions.

Reset Button Pin Definitions (JF1)	
Pin#	Definition
3	Reset
4	Ground

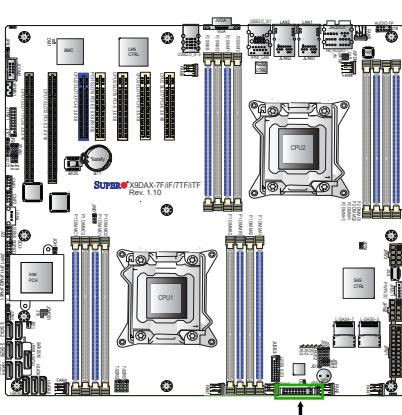
## Power Button

The Power Button connection is located on pins 1 and 2 of JF1. Momentarily contacting both pins will power on or off the system. This button can also be configured to function as a suspend button (with a setting in the BIOS - See Chapter 4). To turn off the power when the system is in suspend mode, press the button for 4 seconds or longer. Refer to the table on the right for pin definitions.

Power Button Pin Definitions (JF1)	
Pin#	Definition
1	Signal
2	Ground

[A. Reset Button](#)

[B. PWR Button](#)



	20	19	
Ground	○	○	NMI
X	○	○	X
Power LED	○	○	Vcc
HDD LED	○	○	Vcc
NIC1 LED	○	○	Vcc
NIC2 LED	○	○	Vcc
OH/Fan Fail LED	○	○	Vcc
PWR Fail LED	○	○	Vcc
Ground	○	○	Reset
Ground	○	○	Power Button
	2	1	

**A** Reset Button      **B** Power Button

## 2-7 Connecting Cables

### Power Connectors

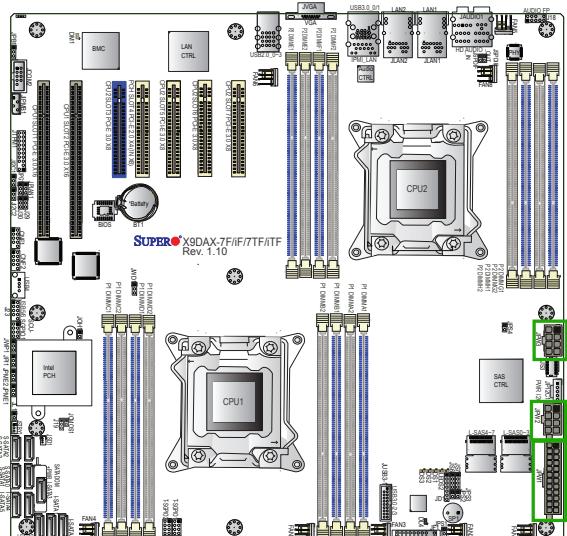
A24-pin main power supply connector(JPW1) and two 8-pin CPU PWR connectors (JPW2/2) are located on the motherboard. These power connectors meet the SSI EPS 12V specification. These power connectors must also be connected to your power supply. See the table on the right for pin definitions.

**Warning:** To provide adequate power supply to the motherboard, be sure to connect the 24-pin ATX PWR (JPW1) and two 8-pin PWR connectors (JPW2, JPW3) to the power supply. Failure to do so will void the manufacturer warranty on your power supply and motherboard.

ATX Power 24-pin Connector Pin Definitions			
Pin#	Definition	Pin #	Definition
13	+3.3V	1	+3.3V
14	-12V	2	+3.3V
15	COM	3	COM
16	PS_ON	4	+5V
17	COM	5	COM
18	COM	6	+5V
19	COM	7	COM
20	Res (NC)	8	PWR_OK
21	+5V	9	5VSB
22	+5V	10	+12V
23	+5V	11	+12V
24	COM	12	+3.3V

12V 8-pin PWR Connector Pin Definitions	
Pins	Definition
1 through 4	Ground
5 through 8	+12V

(Required)



A. JPW1: 24-pin ATX

PWR (Req'd)

B. JPW2: 8-pin Processor

PWR (Req'd)

C. JPW3: 8-pin Processor

PWR (Req'd)

## Fan Headers

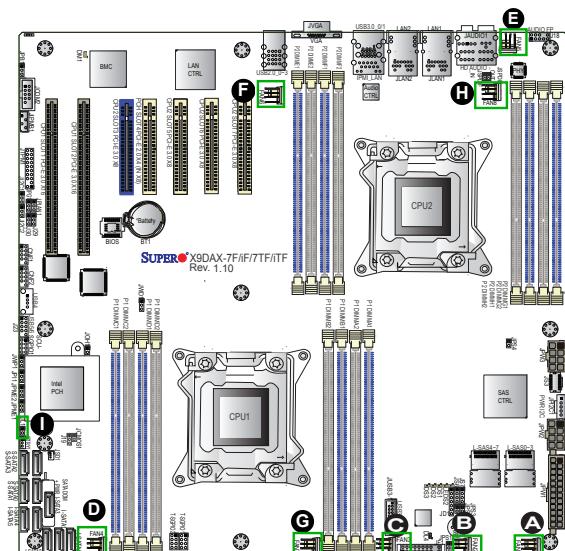
This motherboard has eight system/CPU fan headers (Fan 1~Fan 8) on the motherboard. All these 4-pin fans headers are backward compatible with the traditional 3-pin fans. The fan speeds are controlled by firmware thermal management via IPMI. (See Chapter 4 for more details.) See the table on the right for pin definitions.

Fan Header Pin Definitions	
Pin#	Definition
1	Ground
2	+12V
3	Tachometer
4	PWR Modulation

## Chassis Intrusion

A Chassis Intrusion header is located at JL1 on the motherboard. Attach an appropriate cable from the chassis to inform you of a chassis intrusion when the chassis is opened.

Chassis Intrusion Pin Definitions	
Pin#	Definition
1	Intrusion Input
2	Ground

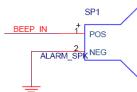


- A. Fan 1
- B. Fan 2
- C. Fan 3
- D. Fan 4
- E. Fan 5
- F. Fan 6(CPU1 Fan)
- G. Fan7(CPU2 Fan)
- H. Fan A
- I. Chassis Intrusion

## Internal Speaker

The Internal Speaker, located at SP1, can be used to provide audible indications for various beep codes. See the table on the right for pin definitions. Refer to the layout below for the locations of the Internal Buzzer (SP1).

Internal Buzzer (SP1) Pin Definition		
Pin#	Definitions	
Pin 1	Pos. (+)	Beep In
Pin 2	Neg. (-)	Alarm Speaker



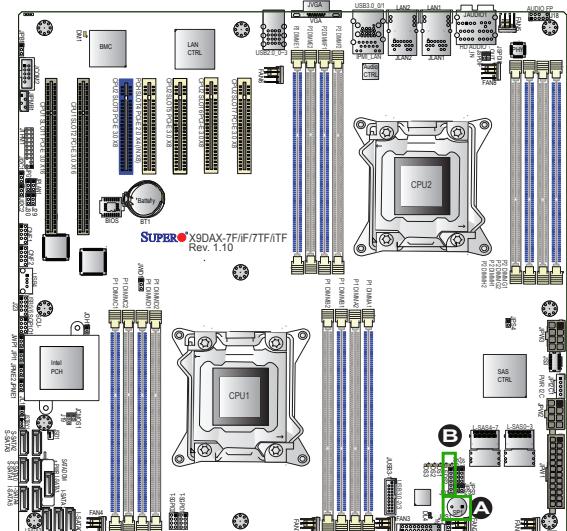
## Power LED/Speaker

On JD1 header, pins 1-3 are used for power LED indication, and pins 4-7 are for the speaker. See the tables on the right for pin definitions. Please note that the speaker connector pins (4-7) are used with an external speaker. If you wish to use the on-board speaker, you should close pins 6-7 with a jumper.

PWR LED Connector Pin Definitions	
Pin Setting	Definition
Pin 1	Anode (+)
Pin2	Cathode (-)
Pin3	NA

Speaker Connector Pin Settings	
Pin Setting	Definition
Pins 4-7	External Speaker
Pins 6-7	Internal Speaker



A. Internal Speaker

(Buzzer)

B. PWR LED/Speaker

### TPM Header/Port 80 Header

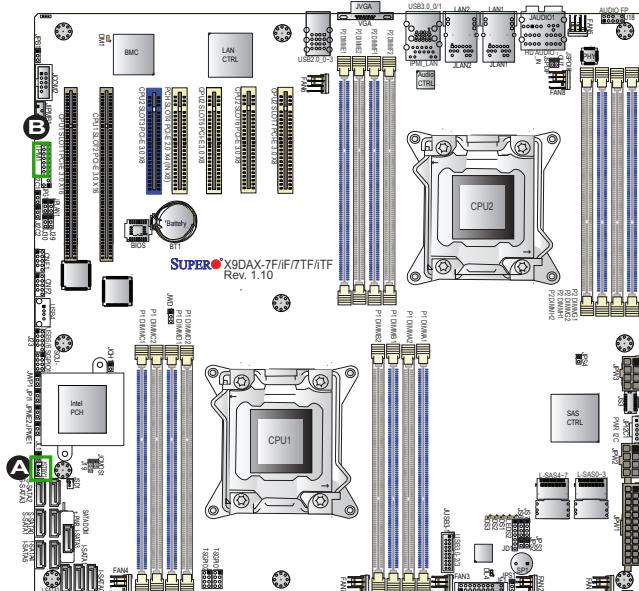
A Trusted Platform Module/Port 80 header is located at JTPM1 to provide TPM support and Port 80 connection. Use this header to enhance system performance and data security. See the table on the right for pin definitions.

TPM/Port 80 Header Pin Definitions			
Pin #	Definition	Pin #	Definition
1	LCLK	2	GND
3	LFRAME#	4	<(KEY)>
5	LRESET#	6	+5V (X)
7	LAD 3	8	LAD 2
9	+3.3V	10	LAD1
11	LAD0	12	GND
13	SMB_CLK4	14	SMB_DAT4
15	+3V_DUAL	16	SERIRQ
17	GND	18	CLKRUN# (X)
19	LPCPD#	20	LDRQ# (X)

### Standby Power Header

The Standby Power header is located at JSTBY1 on the motherboard. See the table on the right for pin definitions.

Standby Power Pin Definitions	
Pin#	Definition
1	+5V Standby
2	Ground
3	Wake-up



A. TPM/Port 80

Header

B. Standby PWR

### Power SMB (I<sup>2</sup>C) Connector

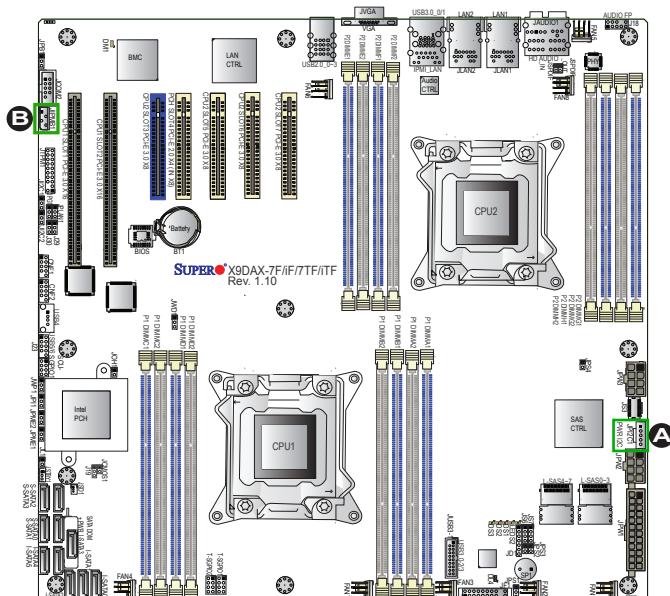
Power System Management Bus (I<sup>2</sup>C) Connector (JPI<sup>2</sup>C1) monitors power supply, fan and system temperatures. See the table on the right for pin definitions.

PWR SMB Pin Definitions	
Pin#	Definition
1	Clock
2	Data
3	PWR Fail
4	Ground
5	+3.3V

### IPMB

A System Management Bus header for IPMI 2.0 is located at JIPMB1. Connect the appropriate cable here to use the IPMB I<sup>2</sup>C connection on your system.

IPMB Header Pin Definitions	
Pin#	Definition
1	Data
2	Ground
3	Clock
4	No Connection

A. JPI<sup>2</sup>C1

B. JIPMB1

## T-SGPIO 1/2 & SCU-SGPIO 1 Headers

Three SGPIO (Serial-Link General Purpose Input/Output) headers are located on the motherboard. T-SGPIO 1/2 support SATA interface, and SCU-SGPIO 1 is for SAS interface. See the table on the right for pin definitions.

T-SGPIO 1/2 & SCU-SGPIO 1 Pin Definitions			
Pin#	Definition	Pin	Definition
1	NC	2	NC
3	Ground	4	Data
5	Load	6	Ground
7	Clock	8	NC

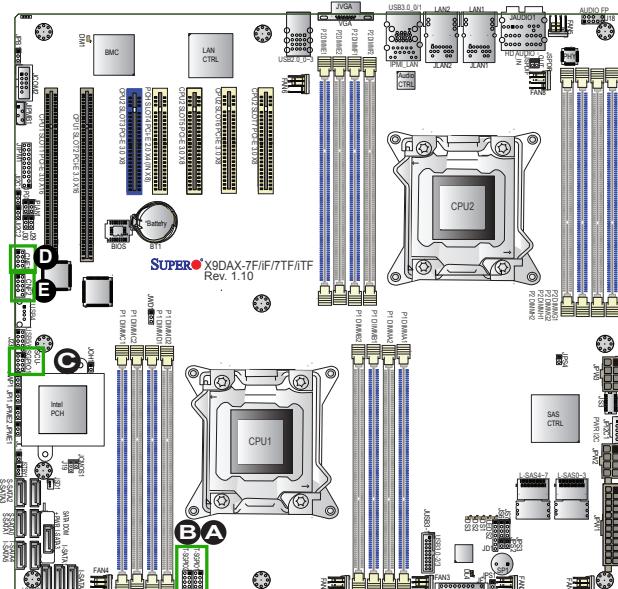
Note: NC= No Connection

1394_1 Pin Definitions			
Pin#	Definition	Pin#	Definition
1	PTPA0+	2	PTPA0-
3	GND	4	GND
5	PTPB0+	6	PTPB0-
7	PWR 1394a	8	PWR 1394a
		10	Shield GND

1394_2 Pin Definitions			
Pin#	Definition	Pin#	Definition
1	PTPA1+	2	PTPA1-
3	GND	4	GND
5	PTPB1+	6	PTPB1-
7	PWR 1394a	8	PWR 1394a
		10	Shield GND

## IEEE 1394a\_1/ 1394a\_2 Connectors

1394a\_1 (CNF1) and 1394a\_2 (CNF2) provide IEEE 1394 connections on the motherboard. Connect IEEE 1394 cables to the connectors for IEEE 1394 support. See the tables on the right for pin definitions.



A. T-SGPIO1

B. T-SGPIO 2

C. SCU-SGPIO1

D. 1394a\_1

E. 1394a\_2

### Overheat/Fan Fail LED

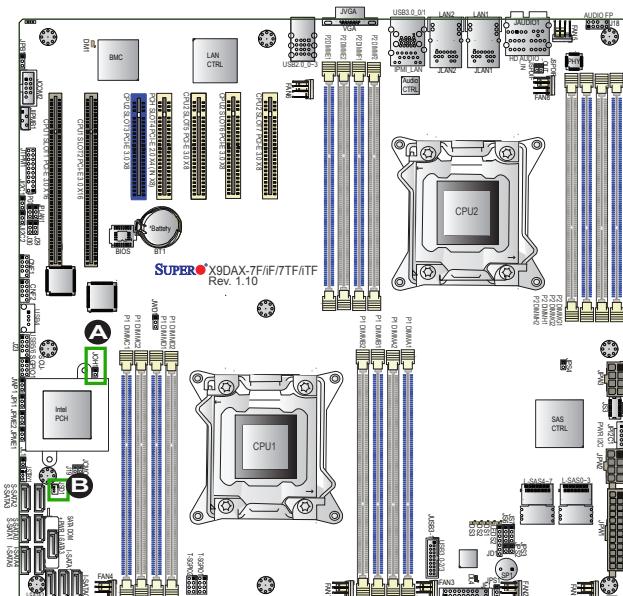
The JOH1 header is used to connect an LED indicator to provide warnings of chassis overheating and fan failure. This LED will blink when a fan failure occurs. Refer to the table on right for pin definitions.

OH/Fan Fail LED Status	
State	Message
Solid	Overheat
Blinking	Fan Fail

### DOM Power Connector

A power connector for SATA DOM (Disk\_On\_Module) devices is located at JSD1. Connect an appropriate cable here to provide backup power support for your SATA devices to retain cache data during power outage. See the table on the right for pin definitions..

DOM PWR Pin Definitions	
Pin#	Definition
1	+5V
2	Ground
3	Ground



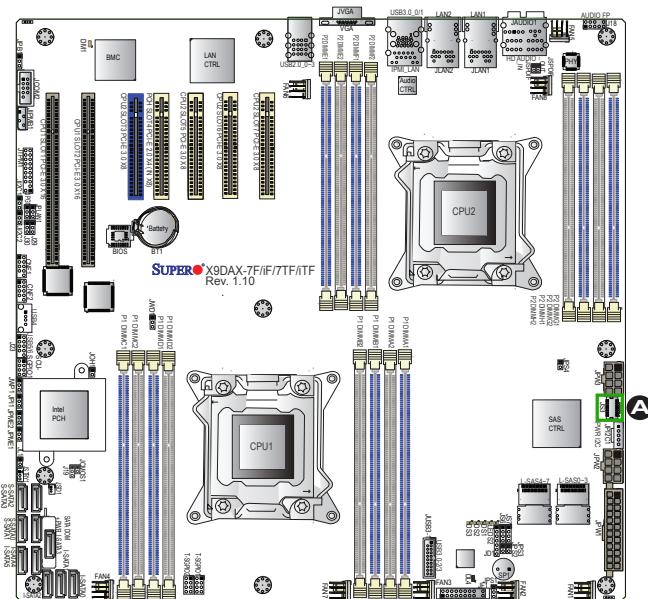
A. OH/Fan Fail

LED

B. DOM PWR

### BBU Header (Optional for the X9DAX-7F/7TF Only)

A Battery-Backup Unit (BBU) header is located at JS3 on the motherboard. When enabled, the BBU provides extended battery backup support for the onboard SAS devices to prevent data loss due to a battery power shortage. To enable extended battery support, an optional battery backup accessory kit is required. Please contact our sales at Supermicro to purchase an LSI 2208 SAS Battery Backup Accessory kit. Also, contact our tech support for installation instructions and possible component updates.

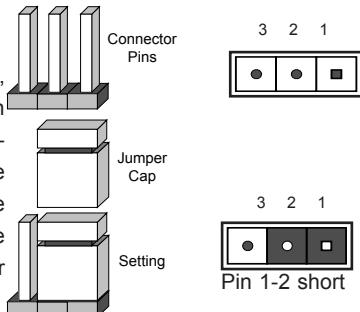


A. BBU (for X9DAX-7F/7TF)

## 2-8 Jumper Settings

### Explanation of Jumpers

To modify the operation of the motherboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. See the motherboard layout pages for jumper locations.

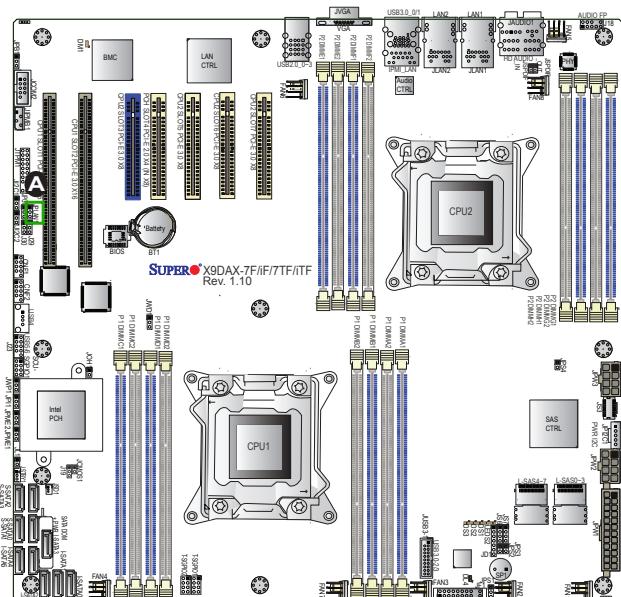


 **Note:** On two-pin jumpers, "Closed" means the jumper is on and "Open" means the jumper is off the pins.

### GLAN Enable/Disable

JPLAN1 enables and disables the GLAN ports on the X9DAX-iF/7F and the 10GLAN ports on the X9DAX-iTF/7TF. See the table on the right for jumper settings. The default setting is Enabled.

GLAN/10GLAN Enable Jumper Settings	
Jumper Setting	Definition
1-2	Enabled (default)
2-3	Disabled



#### A. GLAN Ports

Enable (X9DAX-iF/7F):

A. 10GLAN Ports  
Enable (X9DAX-iTF/7TF)

## Clear CMOS

JCMOS1 is used to clear CMOS. To clear CMOS, close pins 1 and 2 of JCMOS1 to short the connection. The default setting is "Open" for normal operation. Always remove the AC power cord from the system before clearing CMOS.

Clear CMOS Jumper Settings	
Jumper Setting	Definition
Closed	Clear CMOS
Open	Normal (Default)

 **Note 1:** For an ATX power supply, you must completely shut down the system, remove the AC power cord, and then short JBT1 to clear CMOS.

**Note 2:** Be sure to remove the onboard CMOS Battery before you short JBT1 to clear CMOS.

**Note 3:** Clearing CMOS will also clear all passwords.

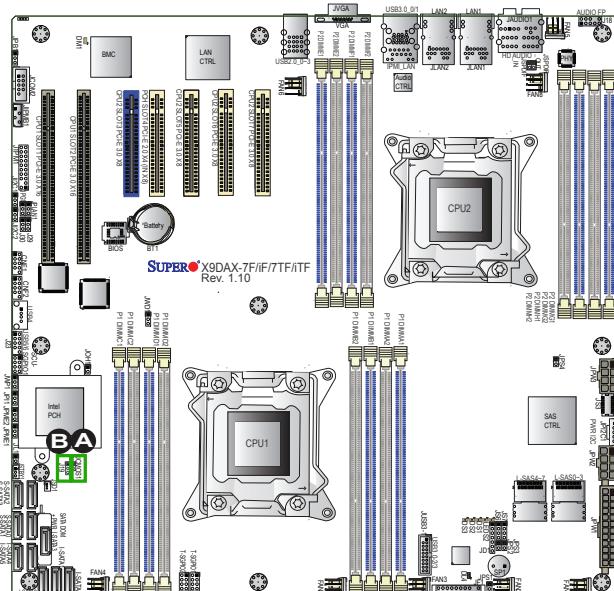
## BIOS Chip Select Jumper (J19)

This motherboard comes with two BIOS chips installed to allow two BIOS profiles. Close pins 1 & 2 of J19 to use the primary BIOS chip to support aggressive overclocking. Close pins 2 & 3 of J19 to use the secondary BIOS chip for more conservative, standard operations. In the event when the primary BIOS chip is corrupted, you can also close pins 2 and 3 to use the secondary chip as the backup, and vice versa.

BIOS Chip Select (J19) Jumper Settings	
Jumper Setting	Definition
1-2	Primary BIOS Chip (Default)
2-3	Secondary BIOS Chip

A. Clear CMOS

B. BIOS Chip Select



## IEEE 1394a Enable

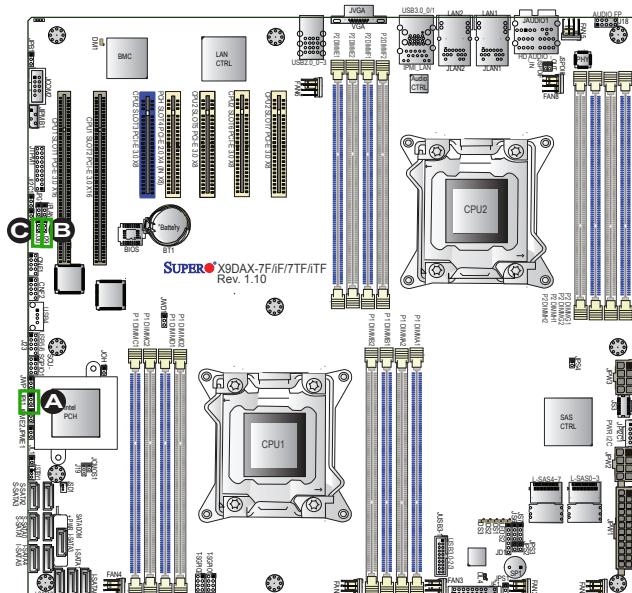
JPI1 allows you to enable or disable the onboard IEEE 1394a support. The default position is on pins 1 and 2 to use 1394\_1 and 1394\_2 connections. See the table on the right for jumper settings.

1394a Enable/Disable Jumper Settings	
Both Jumpers	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled

## SMB Enable

Jumpers J29/J30 allow the user to enable System Management Bus support. The default setting is 1-2 to enable the connection. See the table on the right for jumper settings.

SMB Enable Jumper Settings	
Jumper Setting	Definition
1-2	Enabled (Default)
2-3	Disabled



A. 1394a Enable

B. J29

C. J30

## VGA Enable

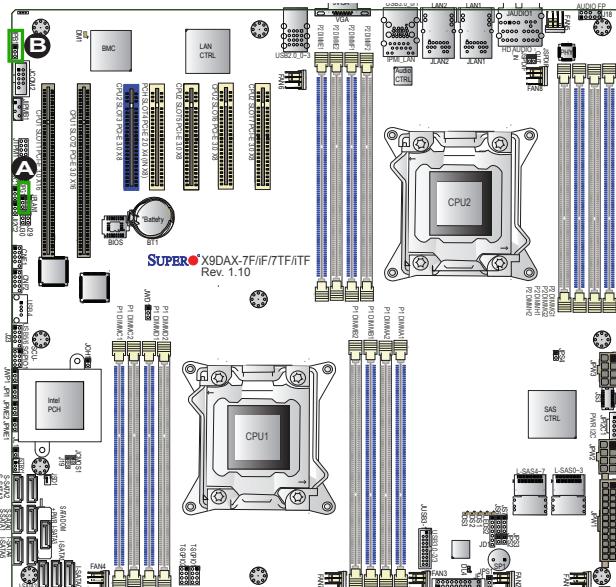
Jumper JPG1 allows the user to enable the onboard VGA connector. The default setting is 1-2 to enable the connection. See the table on the right for jumper settings.

VGA Enable Jumper Settings	
Jumper Setting	Definition
1-2	Enabled (Default)
2-3	Disabled

## BMC Enable

Jumper JPB1 allows you to enable the embedded WPCM 450 BMC (Baseboard Management) Controller to provide IPMI 2.0/KVM support on the motherboard. See the table on the right for jumper settings.

BMC Enable Jumper Settings	
Jumper Setting	Definition
Pins 1-2	BMC Enable (Default)
Pins 2-3	Normal



## I<sup>2</sup>C Bus to PCI-Exp. Slots

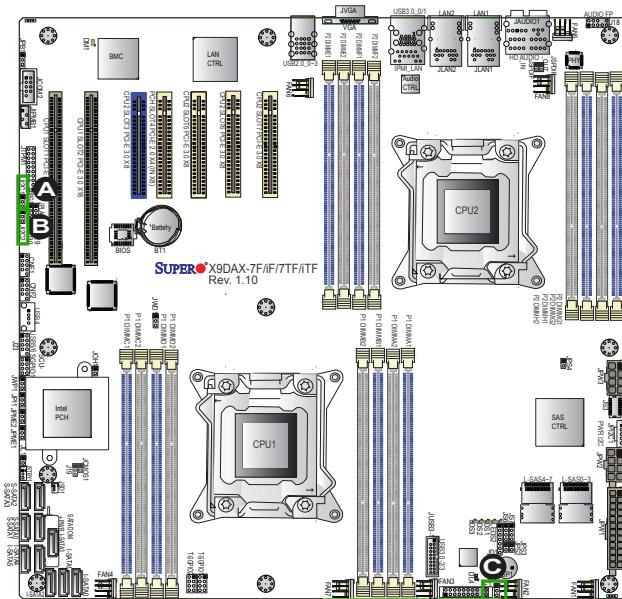
Use Jumpers JI<sup>2</sup>C1 and JI<sup>2</sup>C2 to connect the System Management Bus (I<sup>2</sup>C) to PCI-Express slots to improve PCI slot performance. These two jumpers are to be set at the same time. The default setting is "Closed" to enable the connections. See the table on the right for jumper settings.

I <sup>2</sup> C to PCI-E slots Jumper Settings	
Jumper Setting	Definition
Closed	Enabled (Default)
Open	Disabled

## SAS Enable (X9DAX-7F/7TF Only)

Jumper JPS1 allows you to enable the LSI SAS 2208 Controller to provide SAS 2.0 support on the motherboard. See the table on the right for jumper settings.

SAS Enable Jumper Settings	
Jumper Setting	Definition
Pins 1-2	BMC Enable (Default)
Pins 2-3	Normal



A. JI<sup>2</sup>C1

B. JI<sup>2</sup>C2

C. SAS Enable

## Management Engine (ME) Recovery

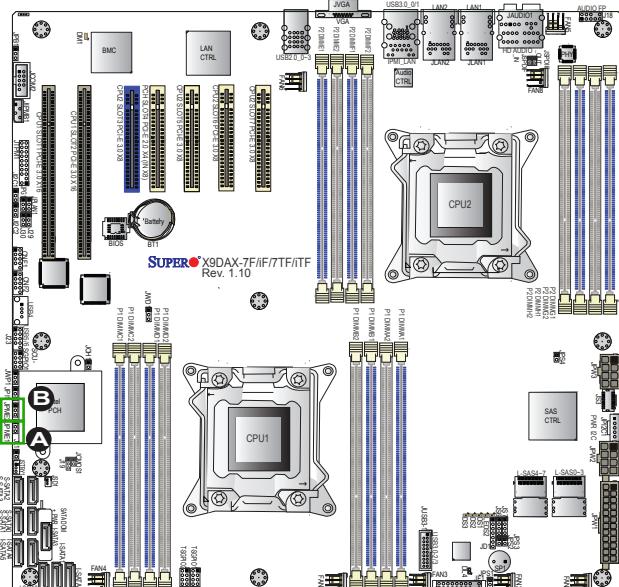
Use Jumper JPME1 to select ME Firmware Recovery mode, which will limit resource allocation for essential system operation only in order to maintain normal power operation and management. In the single operation mode, online upgrade will be available via Recovery mode. See the table on the right for jumper settings.

ME Recovery Jumper Settings	
Jumper Setting	Definition
1-2	Normal (Default)
2-3	ME Recovery

## Manufacture Mode Select

Close pins 2 and 3 of Jumper JPME2 to bypass SPI flash security and force the system to operate in the Manufacture Mode, allowing the user to flash the system firmware from a host server for system setting modifications. See the table on the right for jumper settings.

ME Mode Select Jumper Settings	
Jumper Setting	Definition
1-2	Normal (Default)
2-3	Manufacture Mode



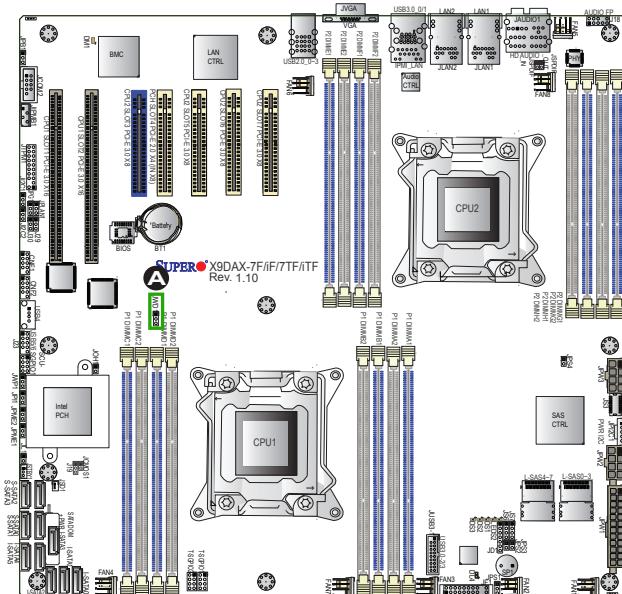
A. JPME1

B. JPME2

## Watch Dog Enable/Disable

Watch Dog (JWD1) is a system monitor that can reboot the system when a software application hangs. Close pins 1-2 to reset the system if an application hangs. Close pins 2-3 to generate a non-maskable interrupt signal for the application that hangs. See the table on the right for jumper settings. Watch Dog must also be enabled in the BIOS.

Watch Dog Jumper Settings	
Jumper Setting	Definition
Pins 1-2	Reset (default)
Pins 2-3	NMI
Open	Disabled



A. Watch Dog Enable

## 2-9 Onboard LED Indicators

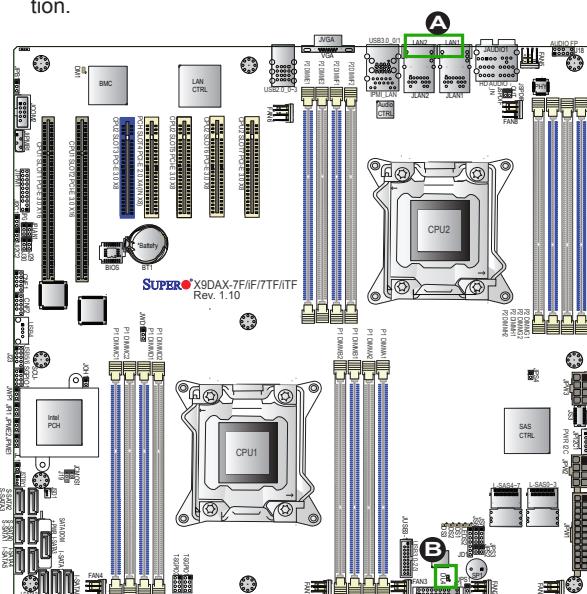
### GLAN LEDs

Two LAN ports (LAN 1/LAN 2) are located on the IO Backplane of the motherboard. Each Ethernet LAN port has two LEDs. The green LED indicates activity, while the other Link LED may be green, amber or off to indicate the speed of the connections. See the tables on the right for more information.



### Onboard Power LED

An Onboard Power LED is located at DL4 on the motherboard. When this LED is on, the system is on. Be sure to turn off the system and unplug the power cord before removing or installing components. See the tables on the right for more information.



**Rear View** (when facing the rear side of the chassis)

#### LAN 1/LAN 2 Activity LED (Right) LED State

Color	Status	Definition
Green	Flashing	Active

#### LAN 1/LAN 2 Link LED (Left) LED State

LED Color	Definition
Off	10 Mbps, 100 Mbps, or No Connection
Green	10 Gbps
Amber	1 Gbps

#### Onboard PWR LED Indicator LED Settings

LED Color	Status
Off	System Off (PWR cable not connected)
Green	System On
Green: Flashing Quickly	ACPI S1 State

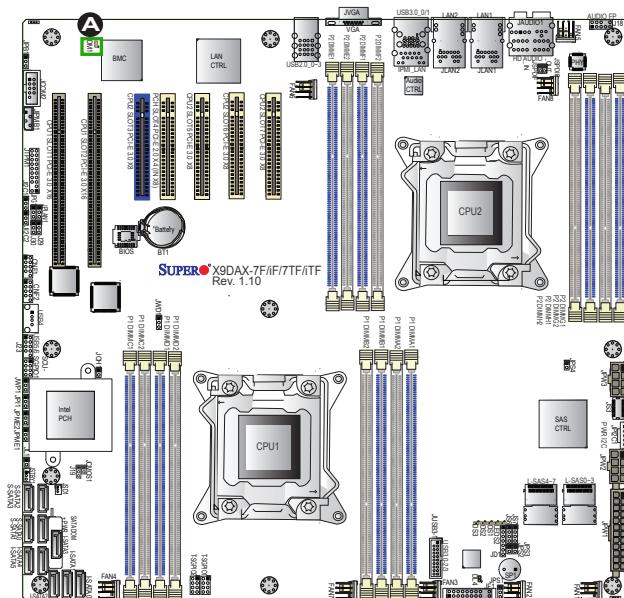
**A. LAN1/2 LEDs**

**B. Onboard PWR LED**

## BMC Heartbeat LED

A BMC Heartbeat LED is located at DM1 on the motherboard. When LEM1 is blinking, BMC functions normally. See the table on the right for more information.

BMC Heartbeat LED Status	
Color/State	Definition
Green: Blinking	BMC: Normal



A. BMC LED

## 2-10 SATA/SAS Connections

### Serial ATA Ports

Two SATA 3.0 Ports (I-SATA0/1), colored in white, and eight SATA 2 Ports (I-SATA2~5, S-SATA0~3), colored in black are located on the motherboard. I-SATA ports are supported by the Intel PCH chip; while S-SATA ports are supported by SCU (Storage Control Unit). These ports provide serial-link signal connections, which are faster than the connections of Parallel ATA. See the table on the right for pin definitions.

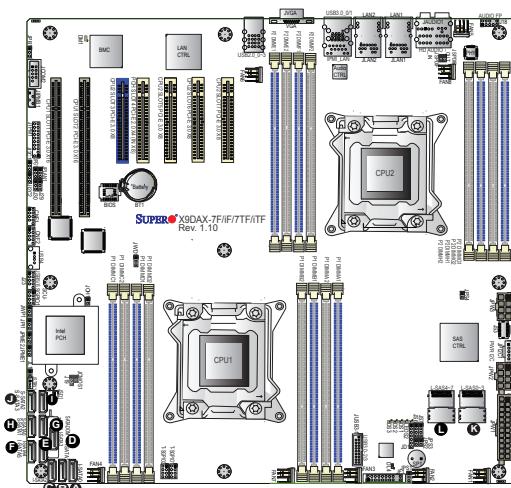
SATA/SAS Pin Definitions	
Pin#	Definition
1	Ground
2	TX_P
3	TX_N
4	Ground
5	RX_N
6	RX_P
7	Ground

### SAS2 Ports (X9DAX-7F/-7TF Only)

Eight Serial\_Attached\_SCSI Ports (L-SAS 0~3, L-SAS 4~7) provide Serial\_Attached SCSI connections on the motherboard. On the X9DAX-7F/-7TF, SAS Connections can function as SATA ports if the connectors are used with the mini-ipass cables. See the table on the right for pin definitions.



**Note:** For more information on HostRAID configuration, please refer to the HostRAID User's Guide posted on our website @ <http://www.supermicro.com>.



- A. I-SATA0 (SATA3.0)
- B. I-SATA1 (SATA3.0)
- C. I-SATA2 (SATA2.0)
- D. I-SATA3 (SATA2.0)
- E. I-SATA4 (SATA2.0)
- F. I-SATA5 (SATA2.0)
- G. S-SATA0 (SATA2.0)
- H. S-SATA1 (SATA2.0)
- I. S-SATA2 (SATA2.0)
- J. S-SATA3 (SATA2.0)
- K. L-SAS0~3 (X9DAX-7F/-7TF)
- L. L-SAS4~7 (X9DAX-7F/-7TF)

## **Notes**

## Chapter 3

# Troubleshooting

### 3-1 Troubleshooting Procedures

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the 'Technical Support Procedures' and/or 'Returning Merchandise for Service' section(s) in this chapter. Note: Always disconnect the power cord before adding, changing or installing any hardware components.

#### Before Power On

1. Make sure that there are no short circuits between the motherboard and chassis.
2. Disconnect all ribbon/wire cables from the motherboard, including those for the keyboard and mouse.
3. Remove all add-on cards.
4. Install CPU 1 first (making sure it is fully seated) and connect the front panel connectors to the motherboard.

#### No Power

1. Make sure that there are no short circuits between the motherboard and the chassis.
2. Make sure that the ATX power connectors are properly connected
3. Check that the 115V/230V switch on the power supply is properly set, if available.
4. Turn the power switch on and off to test the system, if applicable.
5. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

## **No Video**

1. If the power is on, but you have no video, remove all the add-on cards and cables.
2. Use the speaker to determine if any beep codes exist. Refer to Appendix A for details on beep codes.

## **System Boot Failure**

If the system does not display POST or does not respond after the power is turned on, check the following:

1. Check for any error beep from the motherboard speaker.
- If there is no error beep, try to turn on the system without DIMM modules installed. If there is still no error beep, try to turn on the system again with only one processor installed in CPU Socket#1. If there is still no error beep, replace the motherboard.
- If there are error beeps, clear the CMOS settings by unplugging the power cord and contracting both pads on the CMOS Clear Jumper (JBT1). (Refer to Section 2-8 in Chapter 2.)
2. Remove all components from the motherboard, especially the DIMM modules. Make sure that the system's power is on, and memory error beeps are activated.
3. Turn on the system with only one DIMM module installed. If the system boots, check for bad DIMM modules or slots by following the Memory Errors Troubleshooting procedure in this Chapter.

## **Losing the System's Setup Configuration**

1. Make sure that you are using a high quality power supply. A poor quality power supply may cause the system to lose the CMOS setup information. Refer to Section 2-7 for details on recommended power supplies.
2. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.
3. If the above steps do not fix the Setup Configuration problem, contact your vendor for repairs.

## Memory Errors

When a No-Memory Beep Code is issued by the system, check the following:

1. Make sure that the memory modules are compatible with the system and that the DIMM modules are properly and fully installed. (For memory compatibility, refer to the Memory Compatibility Chart posted on our website @ <http://www.supermicro.com>.)
2. Check if different speeds of DIMMs have been installed. It is strongly recommended that you use the same RAM speed for all DIMMs in the system.
3. Make sure that you are using the correct type of Registered/Load Reduction ECC or Unbuffered ECC/Non-ECC DDR3 DIMM modules recommended by the manufacturer.
4. Check for bad DIMM modules or slots by swapping a single module among all memory slots and check the results.
5. Make sure that all memory modules are fully seated in their slots. Follow the instructions given in Section 2-4 in Chapter 2.
6. Please follow the instructions given in the DIMM Population Tables listed in Section 2-4 to install your memory modules.

## When the System Becomes Unstable

*A. When the system becomes unstable during or after OS installation, check the following:*

1. CPU/BIOS support: Make sure that your CPU is supported, and you have the latest BIOS installed in your system.
2. Memory support: Make sure that the memory modules are supported by testing the modules using memtest86 or a similar utility.
3. HDD support: Make sure that all hard disk drives (HDDs) work properly. Replace the bad HDDs with good ones.
4. System cooling: Check system cooling to make sure that all heatsink fans, and CPU/system fans, etc., work properly. Check Hardware Monitoring settings in the BIOS to make sure that the CPU and System temperatures are



**Note:** Refer to the product page on our website <http://www.supermicro.com> for memory and CPU support and updates.

within the normal range. Also check the front panel Overheat LED, and make sure that the Overheat LED is not on.

5. Adequate power supply: Make sure that the power supply provides adequate power to the system. Make sure that all power connectors are connected. Please refer to our website for more information on minimum power requirement.
6. Proper software support: Make sure that the correct drivers are used.

***B. When the system becomes unstable before or during OS installation, check the following:***

1. Source of installation: Make sure that the devices used for installation are working properly, including boot devices such as CD/DVD disc, CD/DVD-ROM.
2. Cable connection: Check to make sure that all cables are connected and working properly.
3. Using minimum configuration for troubleshooting: Remove all unnecessary components (starting with add-on cards first), and use minimum configuration (with a CPU and a memory module installed) to identify the trouble areas. Refer to the steps listed in Section A above for proper troubleshooting procedures.
4. Identifying bad components by isolating them: If necessary, remove a component in question from the chassis, and test it in isolation to make sure that it works properly. Replace a bad component with a good one.
5. Check and change one component at a time instead of changing several items at the same time. This will help isolate and identify the problem.
6. To find out if a component is good, swap this component with a new one to see if the system will work properly. If so, then the old component is bad. You can also install the component in question in another system. If the new system works, the component is good and the old system has problems.

## 3-2 Technical Support Procedures

Before contacting Technical Support, please take the following steps. Also, please note that as a motherboard manufacturer, Supermicro also sells motherboards through its channels, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problem(s) with the specific system configuration that was sold to you.

1. Please go through the 'Troubleshooting Procedures' and 'Frequently Asked Question' (FAQ) sections in this chapter or see the FAQs on our website (<http://www.supermicro.com/>) before contacting Technical Support.
2. BIOS upgrades can be downloaded from our website (<http://www.supermicro.com>).
3. If you still cannot resolve the problem, include the following information when contacting Supermicro for technical support:
  - Motherboard model and PCB revision number
  - BIOS release date/version (This can be seen on the initial display when your system first boots up.)
  - System configuration
4. An example of a Technical Support form is on our website at (<http://www.supermicro.com>).
- Distributors: For immediate assistance, please have your account number ready when placing a call to our technical support department. We can be reached by e-mail at support@supermicro.com.

### 3-3 Battery Removal and Installation

#### Battery Removal

To remove the onboard battery, follow the steps below:

1. Power off your system and unplug your power cable.
2. Locate the onboard battery as shown below
3. Using a tool such as a pen or a small screwdriver, push the battery lock outwards to unlock it. Once unlocked, the battery will pop out from the holder.
4. Remove the battery.

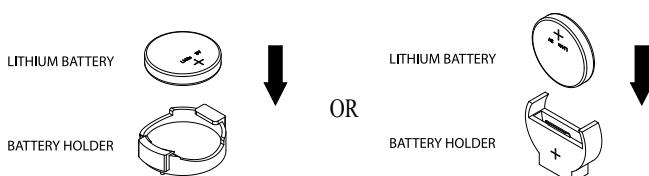
#### Proper Battery Disposal

**Warning:** Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

#### Battery Installation

1. To install an onboard battery, follow the steps 1 & 2 above and continue below:
2. Identify the battery's polarity. The positive (+) side should be facing up.
3. Insert the battery into the battery holder and push it down until you hear a click to ensure that the battery is securely locked.

**Warning:** When replacing a battery, be sure to only replace it with the same type.



## 3-4 Frequently Asked Questions

**Question:** What are the various types of memory that my motherboard can support?

**Answer:** The motherboard supports Registered/Load Reduced ECC or Unbuffered ECC/Non-ECC DDR3 DIMM modules. To enhance memory performance, do not mix memory modules of different speeds and sizes. Please follow all memory installation instructions given on Section 2-4 in Chapter 2.

**Question:** How do I update my BIOS?

It is recommended that you **do not** upgrade your BIOS if you are not experiencing any problems with your system. Updated BIOS files are located on our website at <http://www.supermicro.com>. Please check our BIOS warning message and the information on how to update your BIOS on our website. Select your motherboard model and download the BIOS file to your computer. Also, check the current BIOS revision to make sure that it is newer than your BIOS before downloading. You can choose from the zip file and the .exe file. If you choose the zip BIOS file, please unzip the BIOS file onto a bootable USB device. Run the batch file using the format AMI.bat filename.rom from your bootable USB device to flash the BIOS. Then, your system will automatically reboot.

**Warning:** Do not shut down or reset the system while updating the BIOS to prevent possible system boot failure!)



**Note:** The SPI BIOS chip used on this motherboard cannot be removed. Send your motherboard back to our RMA Department at Supermicro for repair. For BIOS Recovery instructions, please refer to the AMI BIOS Recovery Instructions posted at <http://www.supermicro.com>.

**Question:** How do I handle the used battery?

**Answer:** Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

### **3-5 Returning Merchandise for Service**

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning the motherboard to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton, and the shipping package is mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete. For faster service, you can also request a RMA authorization online (<http://www.supermicro.com/RmaForm/>).

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alternation, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.

# Chapter 4

## BIOS

### 4-1 Introduction

This chapter describes the AMI BIOS Setup utility for the X9DAX-iF/-7F/-iTF/-7TF. It also provides the instructions on how to navigate the AMI BIOS Setup utility screens. The AMI ROM BIOS is stored in a Flash EEPROM and can be easily updated.

#### Starting BIOS Setup Utility

To enter the AMI BIOS Setup utility screens, press the **<Del>** key while the system is booting up.

 **Note:** In most cases, the **<Del>** key is used to invoke the AMI BIOS setup screen. There are a few cases when other keys are used, such as **<F3>**, **<F4>**, etc.

Each main BIOS menu option is described in this manual. The Main BIOS setup menu screen has two main frames. The left frame displays all the options that can be configured. Grayed-out options cannot be configured. Options in blue can be configured by the user. The right frame displays the key legend. Above the key legend is an area reserved for informational text. When an option is selected in the left frame, it is highlighted in white. Often, informational text about the option will display on the right.

 **Note:** The AMI BIOS has default informational messages built in. The manufacturer retains the option to include, omit, or change any of these messages.

The AMI BIOS Setup utility uses a key-based navigation system called "hot keys." Most of the AMI BIOS setup utility "hot keys" can be used at any time during setup navigation. These keys include **<F3>**, **<F4>**, **<Enter>**, **<ESC>**, arrow keys, etc.

 **Note 1:** In this section, options printed in **Bold** are default settings.

**Note 2:** **<F3>** is used to load optimal default settings. **<F4>** is used to save the settings and exit the setup utility.

## How To Change the Configuration Data

The configuration data that determines the system parameters may be changed by entering the AMI BIOS Setup utility. This Setup utility can be accessed by pressing <Delete> at the appropriate time during system boot.



**Note:** For AMI UEFI BIOS Recovery, please refer to the UEFI BIOS Recovery User Guide posted @ <http://www.supermicro.com/support/manuals/>.

## Starting the Setup Utility

Normally, the only visible Power-On Self-Test (POST) routine is the memory test. As the memory is being tested, press the <Delete> key to enter the main menu of the AMI BIOS Setup utility. From the main menu, you can access the other setup screens. An AMI BIOS identification string is displayed at the left bottom corner of the screen below the copyright message.

**Warning:** Do not upgrade the BIOS unless your system has a BIOS-related issue. Flashing the wrong BIOS can cause irreparable damage to the system. In no event shall the manufacturer be liable for direct, indirect, special, incidental, or consequential damage arising from a BIOS update. If you have to update the BIOS, do not shut down or reset the system while the BIOS is being updated to avoid possible boot failure.

## 4-2 Main Setup

When you first enter the AMI BIOS Setup utility, you will enter the Main setup screen. You can always return to the Main setup screen by selecting the Main tab on the top of the screen. The Main BIOS Setup screen is shown below.

The AMI BIOS main menu displays the following information:



### **System Date**

This item displays the system date in Day MM/DD/YY format (e.g. Wed 10/12/2012).

### **System Time**

This item displays the system time in HH:MM:SS format (e.g. 15:32:52).

### **Supermicro X9DAX**

#### **SMC Version**

This item displays the SMC version of the BIOS ROM used in this system.

#### **SMC Build Date**

This item displays the date that the BIOS ROM was built.

### **Memory Information**

#### **Total Memory**

This displays the amount of memory that is available in the system.

## 4-3 Advanced Setup Configurations

Use the arrow keys to select Advanced Setup and press <Enter> to access the following submenu items.



### ► Boot Features

#### Quiet Boot

This feature allows the user to select bootup screen display between POST messages and the OEM logo. Select Disabled to display the POST messages. Select Enabled to display the OEM logo instead of the normal POST messages. The options are **Enabled** and **Disabled**.

#### AddOn ROM Display Mode

Use this item to set the display mode for the Option ROM. Select Keep Current to use the current AddOn ROM Display setting. Select Force BIOS to use the Option ROM display mode set by the system BIOS. The options are **Force BIOS** and **Keep Current**.

#### Bootup Num-Lock

Use this feature to set the Power-on state for the Numlock key. The options are **Off** and **On**.

#### Wait For 'F1' If Error

Select Enabled to force the system to wait until the 'F1' key is pressed if an error occurs. The options are **Disabled** and **Enabled**.

### Interrupt 19 Capture

Interrupt 19 is the software interrupt that handles the boot disk function. When this item is set to Enabled, the ROM BIOS of the host adaptors will "capture" Interrupt 19 at bootup and allow the drives that are attached to these host adaptors to function as bootable disks. If this item is set to Disabled, the ROM BIOS of the host adaptors will not capture Interrupt 19, and the drives attached to these adaptors will not function as bootable devices. The options are **Enabled** and **Disabled**.

## Power Configuration

### Watch Dog Function

If enabled, the Watch Dog timer will allow the system to automatically reboot when a non-recoverable error occurs that lasts for more than five minutes. The options are **Enabled** and **Disabled**.

### Power Button Function

If this feature is set to Instant Off, the system will power off immediately as soon as the user presses the power button. If this feature is set to 4 Seconds Override, the system will power off when the user presses the power button for 4 seconds or longer. The options are **Instant Off** and **4 Seconds Override**.

### Restore on AC Power Loss

Use this feature to set the power state after a power outage. Select Stay Off for the system power to remain off after a power loss. Select Power On for the system power to be turned on after a power loss. Select Last State to allow the system to resume its last state before a power loss. The options are **Power On**, **Stay Off**, and **Last State**.

## CPU Configuration

This submenu displays the information of the CPU as detected by the BIOS. It also allows the user to configure CPU settings.

### ► Socket 0 CPU Information, Socket 1 CPU Information

This submenu displays the following information regarding the CPUs installed in Socket 0 and Socket 2.

- Type of CPU
- CPU Signature
- Microcode Patch

- CPU Stepping
- Max/Min CPU Speed
- Processor Cores
- Intel HT (Hyper-Threading) Technology
- Intel VT-x Technology
- Intel SMX Technology
- L1 Data Cache
- L1 Code Cache
- L2 Cache/L3 Cache

### **CPU Speed**

This item displays the speed of the CPU installed in the Socket selected.

### **64-bit**

This item indicates if 64-bit technology is supported by the CPU installed in the Socket selected.

### **Hyper-threading**

Select Enabled to support Intel Hyper-threading Technology to enhance CPU performance. The options are **Enabled** and **Disabled**.

### **Active Processor Cores**

Set to Enabled to use a processor's second core and above. (Please refer to Intel's website for more information.) The options are **All**, **1**, **2**, **4**, and **6**.

### **Limit CPUID Maximum**

This feature allows the user to set the maximum CPU ID value. Enable this function to boot the legacy operating systems that cannot support processors with extended CPUID functions. The options are **Enabled** and **Disabled** (for the Windows OS).

### **Execute-Disable Bit (Available if supported by the OS & the CPU)**

Select Enabled to enable the Execute Disable Bit which will allow the processor to designate areas in the system memory where an application code can execute and where it cannot, thus preventing a worm or a virus from flooding illegal codes to overwhelm the processor or damage the system during an attack. The default is **Enabled**. (Refer to Intel and Microsoft websites for more information.)

**Hardware Prefetcher (Available when supported by the CPU)**

If set to Enabled, the hardware prefetcher will prefetch streams of data and instructions from the main memory to the L2 cache to improve CPU performance. The options are **Disabled** and **Enabled**.

**Adjacent Cache Line Prefetch (Available when supported by the CPU)**

The CPU prefetches the cache line for 64 bytes if this feature is set to **Disabled**. The CPU prefetches both cache lines for 128 bytes as comprised if this feature is set to **Enabled**.

**DCU Streamer Prefetcher (Available when supported by the CPU)**

Select **Enabled** to support Data Cache Unit (DCU) prefetch of L1 data to speed up data accessing and processing in the DCU to enhance CPU performance. The options are **Disabled** and **Enabled**.

**DCU IP Prefetcher**

Select **Enabled** for DCU (Data Cache Unit) IP Prefetcher support, which will prefetch IP addresses to improve network connectivity and system performance. The options are **Enabled** and **Disabled**.

**Intel® Virtualization Technology (Available when supported by the CPU)**

Select **Enabled** to support Intel Virtualization Technology, which will allow one platform to run multiple operating systems and applications in independent partitions, creating multiple "virtual" systems in one physical computer. The options are **Enabled** and **Disabled**.

 **Note:** If there is any change to this setting, you will need to power off and restart the system for the change to take effect. Please refer to Intel's website for detailed information.)

**Clock Spread Spectrum**

Select **Enable** to enable Clock Spectrum support, which will allow the BIOS to monitor and attempt to reduce the level of Electromagnetic Interference caused by the components whenever needed. The options are **Disabled** and **Enabled**.

**EuP**

Select **Enabled** to support the Low-power Compliance Mode for Energy-using Products (EuP). The options are **Enabled** and **Disabled**.

## ► CPU Power Management Configuration

This submenu allows the user to configure the following CPU Power Management settings.

---

## **Power Technology**

Select Energy Efficient to support power-saving mode. Select Custom to customize system power settings. Select Disabled to disable power-saving settings. The options are Disabled, **Energy Efficient**, and Custom. If the option is set to Custom, the following items will display:

### **EIST (Available when Power Technology is set to Custom)**

EIST (Enhanced Intel SpeedStep Technology) allows the system to automatically adjust processor voltage and core frequency to reduce power consumption and heat dissipation. The options are Disabled (GV3 Disabled), and **Enabled (GV3 Enabled)**. (Note: GV3 is Intel Speedstep support used on older platforms. Please refer to Intel's website for detailed information.)

### **Turbo Mode**

This feature allows processor cores to run faster than marked frequency in specific conditions. The options are Disabled and **Enabled**.

### **P-State Coordination**

This feature allows the user to change the P-State (Power-Performance State) coordination type. P-State is also known as "SpeedStep" for Intel processors. Select HW ALL to change the P-State coordination type for hardware components only. Select SW ALL to change the P-State coordination type for all software installed in the system. Select SW ANY to change the P-State coordination type for a software program in the system. The options are **HW All**, SW ALL, and SW ANY.

### **CPU C3 Report (Available when Power Technology is set to Custom)**

Select Enabled to allow the BIOS to report the CPU C3 State (ACPI C2) to the operating system. During the CPU C3 State, the CPU clock generator is turned off. The options are Enabled and **Disabled**.

### **CPU C6 Report (Available when Power Technology is set to Custom)**

Select Enabled to allow the BIOS to report the CPU C6 State (ACPI C3) to the operating system. During the CPU C6 State, the power to all cache is turned off. The options are **Enabled** and Disabled.

### **CPU C7 Report (Available when Power Technology is set to Custom)**

Select Enabled to allow the BIOS to report the CPU C7 State (ACPI C3) to the operating system. CPU C7 State is a processor-specific low C-State. The options are **Enabled** and Disabled.

**Package C-State limit (Available when Power Technology is set to Custom)**

This feature allows the user to set the limit on the C-State package register. The options are C0, C2, **C6**, and No Limit.

**Energy Performance Bias**

Use this feature to select an appropriate fan setting to achieve maximum system performance (with maximum cooling) or maximum energy efficiency with maximum power saving). The fan speeds are controlled by the firmware management via IPMI 2.0. The options are Performance, **Balanced Performance**, Balanced Energy, and Energy Efficient.

**Factory Long Duration Power Limit**

This item displays the power limit (in watts) set by the manufacturer during which long duration power is maintained.

**Long Duration Power Limit**

This item displays the power limit (in watts) set by the user during which long duration power is maintained. The default setting is **0**.

**Factory Long Duration Maintained**

This item displays the period of time (in seconds) set by the manufacturer during which long duration power is maintained.

**Long Duration Maintained**

This item displays the period of time (in seconds) during which long duration power is maintained. The default setting is **0**.

**Recommended Short Duration Power Limit**

This item displays the short duration power settings (in watts) recommended by the manufacturer.

**Short Duration Power Limit**

This item displays the time period during which short duration power (in watts) is maintained. The default setting is **0**.

**►Chipset Configuration****►North Bridge**

This feature allows the user to configure the settings for the Intel North Bridge.

## ►IOH Configuration

### Intel VT-d

Select Enabled to enable Intel Virtualization Technology support for Direct I/O VT-d by reporting the I/O device assignments to the VMM (Virtual Machine Monitor) through the DMAR ACPI Tables. This feature offers fully-protected I/O resource sharing across Intel platforms, providing greater reliability, security and availability in networking and data-sharing. The options are **Enabled** and **Disabled**.

### Intel® I/OAT

Select Enabled to enable Intel I/OAT (I/O Acceleration Technology), which significantly reduces CPU overhead by leveraging CPU architectural improvements and freeing the system resource for other tasks. The options are **Disabled** and **Enabled**.

### DCA Support

When set to Enabled, this feature uses Intel's DCA (Direct Cache Access) Technology to improve data transfer efficiency. The default is **Enabled** and can not be changed.

### IOH 0 PCIe Port Bifurcation Control

This submenu configures the following IO PCIe Port Bifurcation Control settings for the PCIe ports to determine how the available PCI-Express lanes will be distributed between the PCI-Exp. Root Ports.

#### IOU1 - PCIe Port

This feature allows the user to set the PCI-Exp bus speed between IOU1 and the PCI-e port. The options are **x4x4**, and **x8**.

#### Port 1A Link Speed

Select GEN1 to enable PCI-Exp Generation 1 support for Port 1A. Select GEN2 to enable PCI-Exp Generation 2 support for Port 1A. Select GEN3 to enable PCI-Exp Generation 3 support for Port 1A. The options are **GEN1**, **GEN2**, and **GEN3**.

#### IOU2 - PCIe Port

This feature allows the user to set the PCI-Exp bus speed between IOU2 and the PCI-e port. The options are **x4x4x4x4**, **x4x4x8**, **x8x4x4**, **x8x8**, and **x16**.

#### Port 2A Link Speed

Select GEN1 to enable PCI-Exp Generation 1 support for Port 2A. Select GEN2 to enable PCI-Exp Generation 2 support for Port 2A. Select GEN3 to enable

PCI-Exp Generation 3 support for Port 2A. The options are GEN1, GEN2, and **GEN3**.

#### **IOU3 - PCIe Port**

This feature allows the user to set the PCI-Exp bus speed between IOU3 and the PCI-e port. The options are x4x4x4x4, x4x4x8, x8x4x4, x8x8, and **x16**.

#### **Port 3A Link Speed**

Select GEN1 to enable PCI-Exp Generation 1 support for Port 3A. Select GEN2 to enable PCI-Exp Generation 2 support for Port 3A. Select GEN3 to enable PCI-Exp Generation 3 support for Port 3A. The options are GEN1, GEN2, and **GEN3**.

#### **IOH 1 PCIe Port Bifurcation Control**

This submenu configures the following IO PCIe Port Bifurcation Control settings for the PCIe ports to determine how the available PCI-Express lanes will be distributed between the PCI-Exp. Root Ports.

#### **IOU1 - PCIe Port**

This feature allows the user to set the PCI-Exp bus speed between IOU1 and the PCI-e port. The options are x4x4, and **x8**.

#### **Port 1A Link Speed**

Select GEN1 to enable PCI-Exp Generation 1 support for Port 1A. Select GEN2 to enable PCI-Exp Generation 2 support for Port 1A. Select GEN3 to enable PCI-Exp Generation 3 support for Port 1A. The options are GEN1, GEN2, and **GEN3**.

#### **IOU2 - PCIe Port**

This feature allows the user to set the PCI-Exp bus speed between IOU2 and the PCI-e port. The options are x4x4x4x4, x4x4x8, x8x4x4, **x8x8**, and x16.

#### **Port 2A Link Speed**

Select GEN1 to enable PCI-Exp Generation 1 support for Port 2A. Select GEN2 to enable PCI-Exp Generation 2 support for Port 2A. Select GEN3 to enable PCI-Exp Generation 3 support for Port 2A. The options are GEN1, GEN2, and **GEN3**.

#### **Port 2C Link Speed**

Select GEN1 to enable PCI-Exp Generation 1 support for Port 2C. Select GEN2 to enable PCI-Exp Generation 2 support for Port 2C. Select GEN3 to enable PCI-Exp Generation 3 support for Port 2C. The options are GEN1, GEN2, and **GEN3**.

### **IOU3 - PCIe Port**

This feature allows the user to set the PCI-Exp bus speed between IOU3 and the PCI-e port. The options are x4x4x4x4, x4x4x8, x8x4x4, **x8x8**, and x16.

#### **Port 3A Link Speed**

Select GEN1 to enable PCI-Exp Generation 1 support for Port 3A. Select GEN2 to enable PCI-Exp Generation 2 support for Port 3A. Select GEN3 to enable PCI-Exp Generation 3 support for Port 3A. The options are GEN1, GEN2, and **GEN3**.

#### **Port 3C Link Speed**

Select GEN1 to enable PCI-Exp Generation 1 support for Port 3C. Select GEN2 to enable PCI-Exp Generation 2 support for Port 3C. Select GEN3 to enable PCI-Exp Generation 3 support for Port 3C. The options are GEN1, GEN2, and **GEN3**.

## **►QPI Configuration**

#### **Current QPI Link Speed**

This item displays the current status of the QPI Link.

#### **Current QPI Link Frequency**

This item displays the frequency of the QPI Link.

#### **QPI (Quick Path Interconnect) Link Speed Mode**

Use this feature to select data transfer speed for QPI Link connections. The options are **Fast** and Slow.

#### **QPI Link Frequency Select**

Use this feature to select the desired QPI frequency. The options are **Auto**, 6.4 GT/s, 7.2 GT/s, and 8.0 GT/s.

## **►DIMM Configuration**

This section displays the following DIMM information.

#### **Current Memory Mode**

This item displays the current memory mode.

#### **Current Memory Speed**

This item displays the current memory speed.

### **Mirroring**

This item displays if memory mirroring is supported by the motherboard. Memory mirroring creates a duplicate copy of the data stored in the memory to enhance data security.

### **Sparing**

This item displays if memory sparing is supported by the motherboard. Memory sparing enhances system performance.

### **►DIMM Information**

This section displays the status of the memory modules as detected by the BIOS.

### **Memory Mode**

When Independent is selected, all DIMMs are available to the operating system. When Mirroring is selected, the motherboard maintains two identical copies of all data in memory for data backup. When Lockstep is selected, the motherboard uses two areas of memory to run the same set of operations in parallel. The options are **Independent**, Mirroring, Lock Step, and Sparing.

### **DRAM RAPL BWLIMIT**

This item sets the limits on the average power consumption and the bandwidth of a DRAM module in operation so that the OS can manage power consumption and energy budget of hardware more effectively within a certain window of time. The options are 0, 1, 8, and 16.

### **Perfmon and DFX Devices**

A PerfMon device monitors the activities of a remote system such as disk usage, memory consumption, and CPU load which will allow an IT administrator to maximize the performance of each computer within the network. A DFX device, usually in the form of a USB adaptor, can be used to enhance audio performance. Select Unhide to display the Perfmon and DFX devices installed in the system. The options are **HIDE** and **UNHIDE**.

### **DRAM RAPL Mode**

RAPL (Running Average Power Limit) provides mechanisms to enforce power consumption limits on supported processors. The options are DRAM RAPL MODE0 , **DRAM RAPL MODE1**, and Disabled.

### **MPST Support**

Select Enabled to enable the Message Processing Subscriber Terminal which is used to process short messages. The options are **Disabled** and **Enabled**.

### **Channel Interleaving**

This feature selects from the different channel interleaving methods. The options are **Auto**, 1 Way, 2 Way, 3, Way, and 4 Way.

### **Rank Interleaving**

This feature allows the user to select a rank memory interleaving method. The options are **Auto**, 1 Way, 2 Way, 4, Way, and 8 Way.

### **Patrol Scrub**

Patrol Scrubbing is a process that allows the CPU to correct correctable memory errors detected on a memory module and send the correction to the requestor (the original source). When this item is set to Enabled, the IO hub will read and write back one cache line every 16K cycles, if there is no delay caused by internal processing. By using this method, roughly 64 GB of memory behind the IO hub will be scrubbed every day. The options are **Enabled** and **Disabled**.

### **Demand Scrub**

Demand Scrubbing is a process that allows the CPU to correct correctable memory errors found on a memory module. When the CPU or I/O issues a demand-read command, and the read data from memory turns out to be a correctable error, the error is corrected and sent to the requestor (the original source). Memory is updated as well. Select Enabled to use Demand Scrubbing for ECC memory correction. The options are **Enabled** and **Disabled**.

### **Data Scrambling**

Select Enabled to enable data scrambling to ensure data security and integrity. The options are **Disabled** and **Enabled**.

### **DRAM RAPL (Running Average Power Limit)**

This item allows the user to select the average power limit setting when a DRAM module is in operation. The options are **Disabled**, **Mode 0**, and **Mode 1**.

### **Device Tagging**

Select Enabled to support device tagging. The options are **Disabled** and **Enabled**.

### **Thermal Throttling**

Throttling improves reliability and reduces power consumption in the processor via automatic voltage control during processor idle states. The options are **Disabled** and **CLTT** (Closed Loop Thermal Throttling).

**OLTT (Open Loop Thermal Throttling) Peak BW (Bandwidth) %**

This item sets a percentage of the peak bandwidth allowed for Open Loop Thermal Throttling. The range is between 25% and 100%. The default settling is **50 (%)**.

## ►South Bridge Configuration

This feature allows the user to configure the settings for the Intel PCH chip.

### PCH Information

This feature displays the following PCH information.

**Name:** This item displays the name of the PCH chip.

**Stepping:** This item displays the PCH stepping.

**USB Devices:** This item displays the USB devices detected by the BIOS.

### All USB Devices

This feature enables all USB ports/devices. The options are **Disabled** and **Enabled**.

#### **EHCI Controller 1, EHCI Controller 2 (Available when All USB Devices is set to Enabled)**

Select Enabled to enable EHCI (Enhanced Host Controller Interface) Controller 1 or Controller 2. The options are **Disabled** and **Enabled**.

#### **Legacy USB Support (Available when USB Functions is not Disabled)**

Select Enabled to support legacy USB devices. Select Auto to disable legacy support if USB devices are not present. Select Disabled to have USB devices available for EFI (Extensive Firmware Interface) applications only. The settings are **Disabled**, **Enabled** and **Auto**.

### Port 60/64 Emulation

Select Enabled to enable I/O port 60h/64h emulation support for the legacy USB keyboard so that it can be fully supported by the operating systems that does not recognize a USB device. The options are **Disabled** and **Enabled**.

### EHCI Hand-Off

This item is for operating systems that do not support Enhanced Host Controller Interface (EHCI) hand-off. When enabled, EHCI ownership change will be claimed by the EHCI driver. The options are **Disabled** and **Enabled**.

## Audio Configuration

### Azalia HD (High Definition) Audio

Select Enabled to enable support for Azalia High Definition Audio. The options are **Enabled** and **Disabled**.

### Azalia Internal HDMI Decode

Select Enabled to enable support for Azalia High Definition Multi-Media Interface Decode. The options are Enabled and **Disabled**.

## ►SATA Configuration

When this submenu is selected, the AMI BIOS automatically detects the presence of IDE or SATA devices and displays the following items.

**SATA Port0~SATA Port5:** The AMI BIOS displays the status of each SATA port as detected by the BIOS.

### SATA Mode

Use this feature to configure SATA mode for a selected SATA port. The options are **Disabled**, **IDE Mode**, **AHCI Mode** and **RAID Mode**. The following are displayed depending on your selection:

#### IDE Mode

The following items are displayed when IDE Mode is selected:

##### Serial-ATA (SATA) Controller 0~1

Use this feature to activate or deactivate the SATA controller, and set the compatibility mode. The options are **Disabled**, **Enhanced**, and **Compatible**. The default for SATA Controller 0 is **Compatible**. The default of SATA Controller 1 is **Enhanced**.

#### AHCI Mode

The following items are displayed when the AHCI Mode is selected.

##### Aggressive Link Power Management

When Enabled, the SATA AHCI controller manages the power usage of the SATA link. The controller will put the link in a low power mode during extended periods of I/O inactivity, and will return the link to an active state when I/O activity resumes. The options are **Enabled** and **Disabled**.

### **Port 0~5 Hot Plug**

Select Enabled to enable hot-plug support for a particular port, which will allow the user to change a hardware component or device without shutting down the system. The options are Enabled and **Disabled**.

### **Staggered Spin Up**

Select Enabled to enable Staggered Spin-up support to prevent excessive power consumption caused by multiple HDDs spinning-up simultaneously. The options are Enabled and **Disabled**.

## **RAID Mode**

The following items are displayed when RAID Mode is selected:

### **Port 0~5 Hot Plug**

Select Enabled to enable hot-plug support for the particular port. The options are **Enabled** and **Disabled**.

## **►SCU (Storage Control Unit) Configuration**

### **SCU Devices**

Select Enabled to enable PCH SCU storage devices. The options are **Disabled** and **Enabled**.

### **OnChip SAS Oeprom**

Select Enabled to support the onboard SAS Option ROM to boot up the system via a storage device. The options are **Disabled** and **Enabled**.

## **►PCIe/PCI/PnP Configuration**

### **PCI ROM Priority**

Use this feature to select the Option ROM to boot the system when there are multiple Option ROMs available in the system. The options are **EFI Compatible ROM** and **Legacy ROM**.

### **PCI Latency Timer**

Use this feature to set the latency Timer of each PCI device installed on a PCI bus. Select 64 to set the PCI latency to 64 PCI clock cycles. The options are 32, **64**, 96, 128, 160, 192, 224 and 248.

**Above 4G Decoding (Available if the system supports 64-bit PCI decoding)**

Select Enabled to decode a PCI device that supports 64-bit in the space above 4G Address. The options are Enabled and **Disabled**.

**PERR# Generation**

Select Enabled to allow a PCI device to generate a PERR number for a PCI Bus Signal Error Event. The options are Enabled and **Disabled**.

**SERR# Generation**

Select Enabled to allow a PCI device to generate an SERR number for a PCI Bus Signal Error Event. The options are Enabled and **Disabled**.

**Maximum Payload**

Select Auto to allow the system BIOS to automatically set the maximum payload value for a PCI-E device to enhance system performance. The options are **Auto**, 128 Bytes and 256 Bytes, 512 Bytes, 1024 Bytes, 2048 Bytes, and 4096 Bytes.

**Maximum Read Request**

Select Auto to allow the system BIOS to automatically set the maximum Read Request size for a PCI-E device to enhance system performance. The options are **Auto**, 128 Bytes, 256 Bytes, 512 Bytes, 1024 Bytes, 2048 Bytes, and 4096 Bytes.

**ASPM Support**

This feature allows the user to set the Active State Power Management (ASPM) level for a PCI-E device. Select Force L0s to force all PCI-E links to operate at L0s state. Select Auto to allow the system BIOS to automatically set the ASPM level for the system. Select Disabled to disable ASPM support. The options are **Disabled**, Force L0s, and Auto.

**Warning:** Enabling ASPM support may cause some PCI-E devices to fail!

**Slot 1, 2, 3, 4, 5, 6, 7 OPROM**

Select Enabled to enable Option ROM support to boot the computer using a device installed on the slots specified above. The options are **Enabled** and **Disabled**.

**Onboard LAN Option ROM Select**

Select iSCSI to use the iSCSI Option ROM to boot the computer using a network device. Select PXE (Preboot Execution Environment) to use an PXE Option ROM to boot the computer using a network device. The options are **PXE** and **iSCSI**.

### Load Onboard LAN1 OPROM, Load Onboard LAN2 OPROM

Select Enabled to enable the onboard LAN1/LAN2 Option ROM. This is to boot the computer using a network device. The default setting for LAN1 Option ROM is **Enabled**, and the default setting for LAN2 Option ROM is **Disabled**.

### Load Onboard SAS OPROM

Select Enabled to enable the onboard SAS Option ROM. This is to boot the computer using a SAS device. The default setting for SAS Option ROM is **Enabled**.

### VGA Priority

This feature allows the user to select the graphics adapter to be used as the primary boot device. The options are **Onboard**, and **Offboard**.

### Network Stack

Select Enabled enable PXE (Preboot Execution Environment) or UEFI (Unified Extensible Firmware Interface) for network stack support. The options are **Enabled** and **Disabled**.

## ►Serial Port Console Redirection

### COM 1

These two submenus allow the user to configure the following Console Redirection settings for a COM Port specified by the user.

#### Console Redirection

Select Enabled to use a COM Port selected by the user for Console Redirection. The options are **Enabled** and **Disabled**.

## ►Console Redirection Settings

This feature allows the user to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

#### Terminal Type

This feature allows the user to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are **ANSI**, **VT100**, **VT100+**, and **VT-UTF8**.

#### Bits Per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the

client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and **115200** (bits per second).

### **Data Bits**

Use this feature to set the data transmission size for Console Redirection. The options are 7 Bits and **8 Bits**.

### **Parity**

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark and Space.

### **Stop Bits**

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are 1 and 2.

### **Flow Control**

This feature allows the user to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

### **VT-UTF8 Combo Key Support**

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are **Enabled** and Disabled.

### **Recorder Mode**

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

### **Resolution 100x31**

Select Enabled for extended-terminal resolution support. The options are **Disabled** and **Enabled**.

### **Legacy OS Redirection Resolution**

Use this feature to select the number of rows and columns used in Console Redirection for legacy OS support. The options are **80x24** and **80x25**.

### **Putty KeyPad**

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, **LINUX**, **XTERMR6**, **SCO**, **ESCN**, and **VT400**.

### **Serial Port for Out-of-Band Management/Windows Emergency Management Services (EMS)**

The submenu allows the user to configure Console Redirection settings to support Out-of-Band Serial Port management.

#### **Console Redirection (for EMS)**

Select **Enabled** to use a COM Port selected by the user for Console Redirection. The options are **Enabled** and **Disabled**.

### **►Console Redirection Settings (for EMS)**

This feature allows the user to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

#### **Out-of-Band Management Port**

The feature selects a serial port used by the Microsoft Windows Emergency Management Services (EMS) to communicate with a remote server. The default setting is **COM1**.

#### **Terminal Type**

This feature allows the user to select the target terminal emulation type for Console Redirection. Select **VT100** to use the ASCII character set. Select **VT100+** to add color and function key support. Select **ANSI** to use the extended ASCII character set. Select **VT-UTF8** to use UTF8 encoding to map Unicode characters into one or more bytes. The options are **ANSI**, **VT100**, **VT100+**, and **VT-UTF8**.

#### **Bits Per Second**

This item sets the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 57600, and **115200** (bits per second).

#### **Flow Control**

This feature allows the user to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None**, **Hardware RTS/CTS**, and **Software Xon/Xoff**.

### **Data Bits, Parity, Stop Bits**

The setting for each of these features is displayed.

## **►ACPI Settings**

Use this feature to configure Advanced Configuration and Power Interface (ACPI) power management settings for your system.

### **ACPI Sleep State**

Use this feature to select the ACPI State when the system is in sleep mode. Select S1 (CPU Stop Clock) to erase all CPU caches and stop executing instructions. Power to the CPU(s) and RAM is maintained, but RAM is refreshed. Select Suspend Disabled to use power-reduced mode. Power will only be supplied to limited components (such as RAMs) to maintain the most critical functions of the system. The options are **S1 (CPU Stop Clock)**, and Suspend Disabled.

### **NUMA (NON-Uniform Memory Access)**

This feature enables the Non-Uniform Memory Access ACPI support. The options are **Enabled** and Disabled.

### **High Precision Event Timer**

Select Enabled to activate the High Precision Event Timer (HPET) that produces periodic interrupts at a much higher frequency than a Real-time Clock (RTC) does in synchronizing multimedia streams, providing smooth playback, reducing the dependency on other timestamp calculation devices, such as an x86 RDTSC Instruction embedded in the CPU. The High Performance Event Timer is used to replace the 8254 Programmable Interval Timer. The options are **Enabled** and Disabled.

## **►Trusted Computing (Available when a TPM device is detected by the BIOS)**

### **Configuration**

#### **TPM Support**

Select Enabled on this item and enable the TPM jumper on the motherboard to enable TPM support to improve data integrity and network security. The options are **Enabled** and Disabled.

#### **TPM State**

Select Enabled to enable TPM security settings to improve data integrity and network security. The options are **Disabled** and **Enabled**.

### Pending Operation

Use this item to schedule an operation for the security device. The options are **None**, **Enable Take Ownership**, **Disable Take Ownership**, and **TPM Clear**.



**Note:** During restart, the computer will reboot in order to execute the pending operation and change the state of the security device.

**Current Status Information:** This item displays the information regarding the current TPM status.

### TPM Enable Status

This item displays the status of TPM Support to indicate if TPM is currently enabled or disabled.

### TPM Active Status

This item displays the status of TPM Support to indicate if TPM is currently active or deactivated.

### TPM Owner Status

This item displays the status of TPM Ownership.

## ►Intel TXT (LT-SX) Configuration

### Intel TXT (LT-SX) Hardware Support

This feature indicates if the following hardware components support Intel TXT (Trusted Execution Technology).

#### CPU: TXT Feature

#### Chipset: TXT Feature

### Intel TXT (LT-SX) Configuration

This feature displays the following TXT configuration setting.

**TXT Support:** This item indicates if the Intel TXT support is enabled or disabled. The default setting is **Disabled**.

### Intel TXT (LT-SX) Dependencies

This feature displays the features that need to be enabled for the Intel Trusted Execution Technology to work properly in the system.

**VT-d Support:** Intel Virtualization Technology with Direct I/O support

**VT Support:** Intel Virtualization Technology support

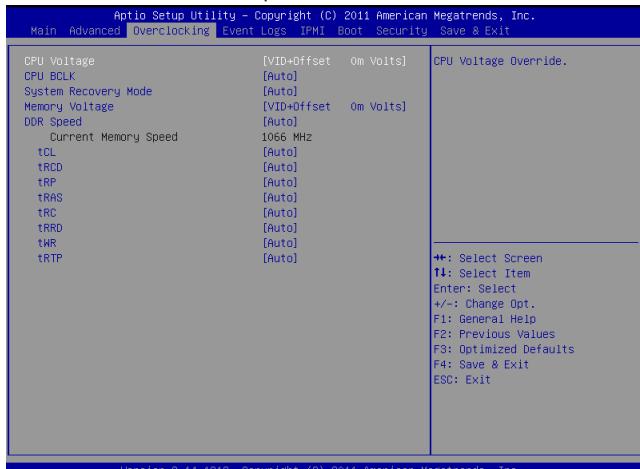
**TPM Support:** Trusted Platform support

**TPM State:** Trusted Platform state

## 4-4 Overclocking

Use this submenu to override selected CPU voltage settings.

**Warning:** Overclocking may cause system instability and is not recommended by Supermicro for standard use of the product.



### CPU Voltage

Use this feature to override the CPU Voltage settings specified by the manufacturer. The VID+Offset options range from **0m Volts** to 500m Volts.

### CPU BCLK

Use this feature to override the CPU BCLK (Base Clock) settings specified by the manufacturer. The options are **Auto**, BCLK 101, BCLK102, BCLK 104, BCLK105, and BCLK 106.

### System Recovery Mode

Use this feature to select the recovery mode setting. When Auto is selected, the BCLK and memory speed will return to default settings after a system reset by the Watchdog Timer. When Disabled is selected, BIOS settings will not change on a system reset by the Watchdog Timer. When Load Defaults is selected, all settings on the Overclocking submenu will return to default values after a system reset by the Watchdog Timer. The options are **Auto**, **Disabled**, and **Load Defaults**.

## Memory Voltage

Use this feature to override the Memory Voltage settings specified by the manufacturer. The VID+Offset options range from **0m Volts** to 310m Volts.

## DDR Speed

Use this feature to force a DDR3 memory module to run at a frequency other than what is specified by the manufacturer. The options are **Auto**, Force DDR3-800, Force DDR3-1066, Force DDR3-1333, Force DDR3-1600, Force DDR3 1866, and Force SPD.

## tCL, tRCD, tRP

Use the above items to set the tCL (Cas Latency), tRCD (Row to Col Delay), and Ras Precharge values. The options are **Auto**, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, and 18.

## tRAS

Use this item to set the Ras Active Time value. The options are Auto, 12, 13, 14, 15, 16, 17, and 18.

## tRRD

Use this item to set the minimum tRDD (Row Active to Row Active Delay) value. The options are **Auto**, 19, 23, 27, 32, 36, and 41.

## tRRD

Use this item to set the minimum tRDD (Row Active to Row Active Delay) value. The options are **Auto**, 4, 5, 6, 7, and 8.

## tWR

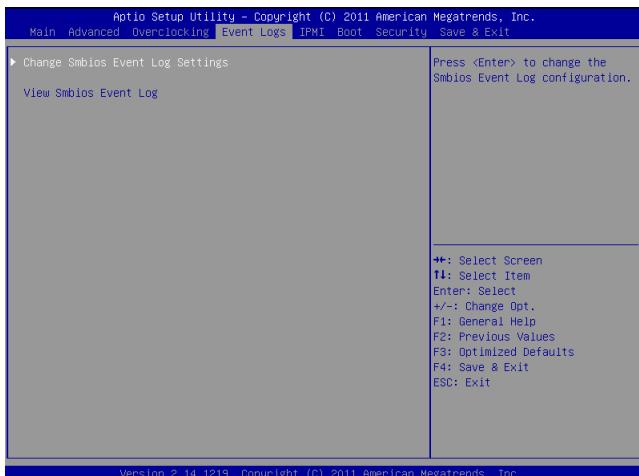
Use this item to set the minimum tWR (Write Recovery) time. The options are **Auto**, 3, 4, 5, 6, 7, and 8.

## tRTP

Use this item to set the minimum internal tRTP (Read to Precharge) command delay time. The options are **Auto**, 3, 4, 5, 6, 7 , and 8.

## 4-5 Event Logs

Use this feature to configure Event Log settings.



### ►Change SMBIOS Event Log Settings

This feature allows the user to configure SMBIOS Event settings.

#### Enabling/Disabling Options

##### SMBIOS Event Log

Select Enabled to enable SMBIOS (System Management BIOS) Event Logging during system boot. The options are **Enabled** and **Disabled**.

##### Runtime Error Logging Support

Select Enabled to support Runtime Error Logging. The options are **Enabled** and **Disabled**.

##### Memory Correctable Error Threshold

This feature allows the user to enter the threshold value for correctable memory errors. The default setting is **10**.

##### PCI Error Logging Support

Select Enabled to support error event logging for PCI slots. The options are **Enabled** and **Disabled**.

## Erasing Settings

### Erase Event Log

Select Enabled to erase the SMBIOS (System Management BIOS) Event Log, which is completed before an event logging is initialized upon system reboot. The options are **No**, Yes, next reset, and Yes, every reset.

### When Log is Full

Select Erase Immediately to immediately erase SMBIOS error event logs that exceed the limit when the SMBIOS event log is full. Select Do Nothing for the system to do nothing when the SMBIOS event log is full. The options are **Do Nothing** and Erase Immediately.

## SMBIOS Event Log Standard Settings

### Log System Boot Event

Select Enabled to log system boot events. The options are **Disabled** and Enabled.

### MECI (Multiple Event Count Increment)

Enter the increment value for the multiple event counter. Enter a number between 1 to 255. The default setting is **1**.

### METW (Multiple Event Count Time Window)

This item allows the user to decide how long (in minutes) should the multiple event counter wait before generating a new event log. Enter a number between 0 to 99. The default setting is **60**.

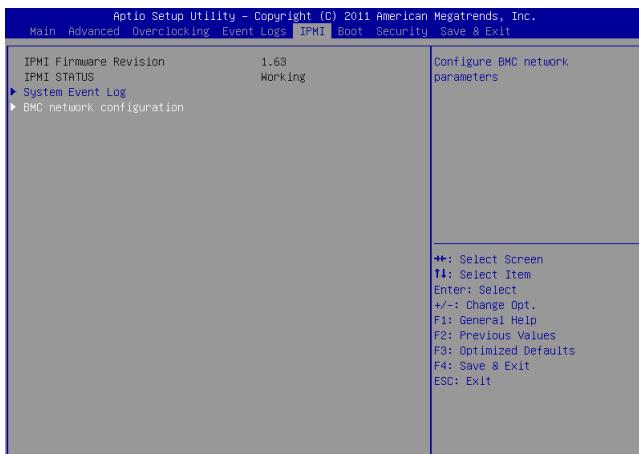
### View SMBIOS Event Log

This item allows the user to view the event in the SMBIOS event log. Select this item and press <Enter> to view the status of an event in the log.

Date/Time/Error Code/Severity

## 4-6 IPMI

Use this submenu to configure Intelligent Platform Management Interface (IPMI) settings.



### IPMI Firmware Revision

This item indicates the IPMI firmware revision used in your system.

### IPMI Status

This item indicates the status of the IPMI firmware installed in your system.

## ►System Event Log

### Enabling/Disabling Options

#### SEL Components

Select Enabled for all system event logging at bootup. The options are **Enabled** and **Disabled**.

#### Erasing Settings

#### Erase SEL

Select Yes, On next reset to erase all system event logs upon next system reboot.  
 Select Yes, On every reset to erase all system event logs upon each system reboot.  
 Select No to keep all system event logs after each system reboot. The options are **No**, Yes, On next reset, and Yes, On every reset.

## When SEL is Full

This feature allows the user to decide what the BIOS should do when the system event log is full. Select Erase Immediately to erase all events in the log when the system event log is full. The options are **Do Nothing** and **Erase Immediately**.

## Custom EFI Logging Options

### Log EFI Status Codes

Select Enabled to log EFI (Extensible Firmware Interface) Status Codes, Error Codes or Progress Codes. The options are **Enabled** and **Disabled**.

 **Note:** After making changes on a setting, be sure to reboot the system for the changes to take effect.

## ►BMC Network Configuration

### Update IPMI LAN Configuration

This feature allows the BIOS to implement any IP/MAC address changes at the next system boot. If the option is set to Yes, any changes made to the settings below will take effect when the system is rebooted. The options are **No** and **Yes**..

### Configuration Address Source

This feature allows the user to select the source of the IP address for this computer. If Static is selected, you will need to know the IP address of this computer and enter it to the system manually in the field. If DHCP is selected, the BIOS will search for a DHCP (Dynamic Host Configuration Protocol) server in the network that it is attached to and request the next available IP address for this computer. The options for LAN 1 are **DHCP** and **Static**. The options for LAN 2 are **Unspecified**, **Static**, **DHCP**, **Dynamic-Loaded by BIOS**, and **Dynamic-BMC running Other Protocol**.

The following items are assigned IP addresses automatically if DHCP is selected, or can be configured manually if Static is selected.

### Station IP Address

This item displays the Station IP address for this computer. This should be in decimal and in dotted quad form (i.e., 192.168.10.253).

### Subnet Mask

This item displays the sub-network that this computer belongs to. The value of each three-digit number separated by dots should not exceed 255.

### Station MAC Address

This item displays the Station MAC address for this computer. Mac addresses are 6 two-digit hexadecimal numbers.

## Gateway IP Address

This item displays the Gateway IP address for this computer. This should be in decimal and in dotted quad form (i.e., 192.168.10.253).

## 4-7 Boot

This submenu allows the user to configure the following boot settings for the system.



### Boot Option Priorities

#### Boot Option #1/ Boot Option #2/ Boot Option #3, etc.

Use this feature to specify the sequence of boot device priority.

### Network Devices, USB Device BBS Priorities, Hard Disk Drives

The above options appear when detected by the BIOS. Use these options to set the order of the legacy network, USB, and Hard Disk Drive devices detected by the motherboard.

#### ►Delete Boot Option

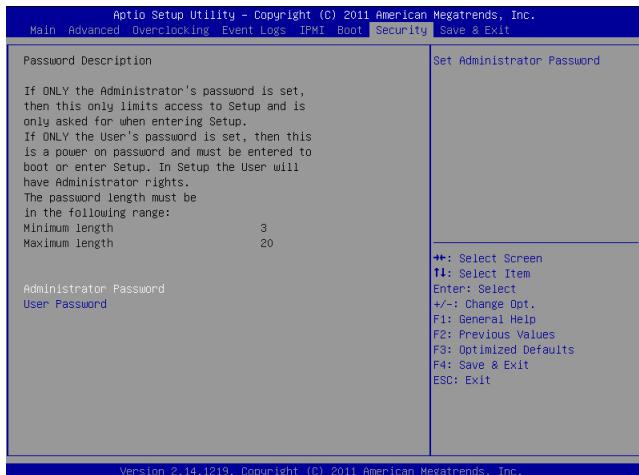
This feature allows the user to select a boot device to delete from the boot priority list.

#### Delete Boot Option

Select the desired boot device to delete.

## 4-8 Security

This menu allows the user to configure the following security settings for the system.



### Administrator Password

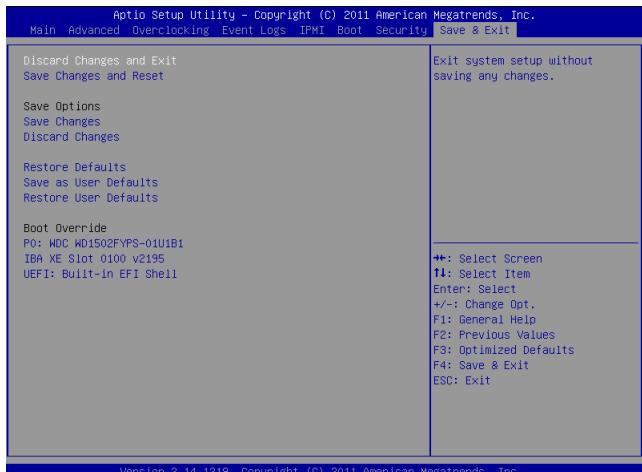
Use this feature to set the Administrator Password which is required to enter the BIOS setup utility. The length of the password should be from 3 characters to 20 characters long.

### User Password

Use this feature to set a User Password which is required to log into the system and to enter the BIOS setup utility. The length of the password should be from 3 characters to 20 characters long.

## 4-9 Save & Exit

This submenu allows the user to configure the Save and Exit settings for the system.



### Discard Changes and Exit

Select this option to quit the BIOS Setup without making any permanent changes to the system configuration, and reboot the computer. Select Discard Changes and Exit, and press <Enter>. When the dialog box appears, asking you if you want to exit the BIOS setup without saving, select **Yes** to quit BIOS without saving the changes, or select **No** to quit the BIOS and save changes.

### Save Changes and Reset

When you have completed the system configuration changes, select this option to save the changes and reboot the computer so that the new system configuration settings can take effect. Select Save Changes and Exit, and press <Enter>. When the dialog box appears, asking you if you want to exit the BIOS setup without saving, select **Yes** to quit BIOS without saving the changes, or select **No** to quit the BIOS and save changes.

### Save Options

#### Save Changes

Select this option and press <Enter> to save all changes you've done so far and return to the AMI BIOS utility Program. When the dialog box appears, asking you if you want to save configuration, select **Yes** to save the changes, or select **No** to return to the BIOS without making changes.

### **Discard Changes**

Select this feature and press <Enter> to discard all the changes and return to the BIOS setup. When the dialog box appears, asking you if you want to load previous values, select **Yes** to load the values previous saved, or select No to keep the changes you've made so far.

### **Restore Optimized Defaults**

Select this feature and press <Enter> to load the optimized default settings that help optimize system performance. When the dialog box appears, asking you if you want to load optimized defaults, select **Yes** to load the optimized default settings, or select No to abandon optimized defaults.

### **Save as User Defaults**

Select this feature and press <Enter> to save the current settings as the user's defaults. When the dialog box appears, asking you if you want to save values as user's defaults, select **Yes** to save the current values as user's default settings, or select No to keep the defaults previously saved as the user's defaults.

### **Restore User Defaults**

Select this feature and press <Enter> to load the user's defaults previously saved in the system. When the dialog box appears, asking you if you want to restore user's defaults, select **Yes** to restore the user's defaults previously saved in the system, or select No to abandon the user's defaults that were previously saved.

### **Boot Override**

This feature allows the user to override the Boot Option Priorities setting in the Boot menu, and instead immediately boot the system with one of the listed devices. This is a one-time override

## **Notes**

## Appendix A

### BIOS Error Beep Codes

During the POST (Power-On Self-Test) routines, which are performed at each system boot, errors may occur.

**Non-fatal errors** are those which, in most cases, allow the system to continue to boot. The error messages normally appear on the screen.

**Fatal errors** will not allow the system to continue with bootup procedure. If a fatal error occurs, you should consult with your system manufacturer for possible repairs.

These fatal errors are usually communicated through a series of audible beeps. The numbers on the fatal error list correspond to the number of beeps for the corresponding error.

#### A-1 BIOS Error Beep Codes

BIOS Error Beep Codes		
Beep Code/LED	Error Message	Description
1 beep	Refresh	Ready to boot
5 short beeps + 1 long beep	Memory error	No memory detected in the system
5 beeps	No Con-In or no Con-Out devices	Con-In includes USB or PS/2 keyboard, PCI or Serial Console Redirection, IPMI KVM or SOL. Con-Out includes Video Controller, PCI or Serial Console Redirection, IPMI SOL.
1 beep per device	Refresh	1 beep for each USB device

X9 IPMI Error Codes		
1 Continuous Beep	System OH	System Overheat

## **Notes**

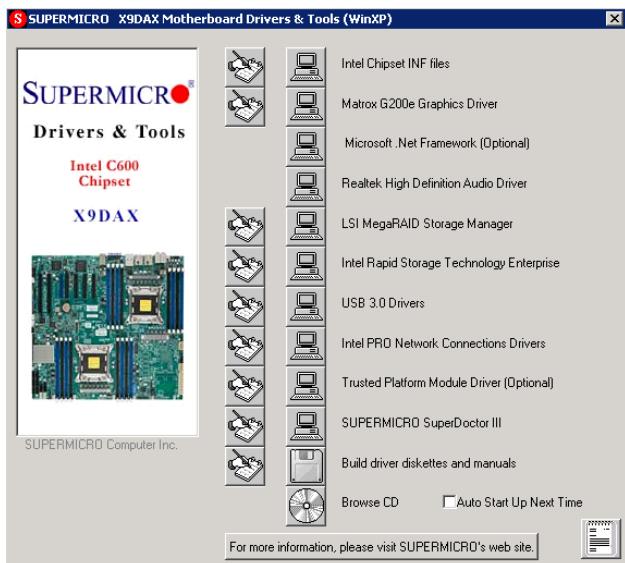
## Appendix B

### Software Installation Instructions

#### B-1 Installing Software Programs

After you've installed the operating system, a screen as shown below will appear. You are ready to install software programs and drivers that have not yet been installed. To install these programs, click the icons to the right of these items.

 **Note:** To install the Windows OS, please refer to the instructions posted on our Website at <http://www.supermicro.com/support/manuals/>.



#### Driver/Tool Installation Display Screen

 **Note 1:** Click the icons showing a hand writing on the paper to view the readme files for each item. Click a computer icon to the right of an item to install an item (from top to the bottom) one at a time. After installing each item, you must reboot the system before proceeding with the next item on the list. The bottom icon with a CD on it allows you to view the entire contents of the CD.

**Note 2:** When making a storage driver diskette by booting into a Driver CD, please set the SATA Configuration to "Compatible Mode" and configure SATA as IDE in the BIOS Setup. After making the driver diskette, be sure to change the SATA settings back to your original settings.

**Note 3:** Changing BMC log-in information is recommended during initial system power-on. The default username is ADMIN and password is ADMIN. For BMC best practices, please refer to: [http://www.supermicro.com/products/info/files/IPMI/Best\\_Practices\\_BMC\\_Security.pdf](http://www.supermicro.com/products/info/files/IPMI/Best_Practices_BMC_Security.pdf)

## B-2 Configuring SuperDoctor III

The SuperDoctor® III program is a Web-based management tool that supports remote management capability. It includes Remote and Local Management tools. The local management is called the SD III Client. The SuperDoctor III program included on the CDROM that came with your motherboard allows you to monitor the environment and operations of your system. SuperDoctor III displays crucial system information such as CPU temperature, system voltages and fan status. See the figure below for a display of the SuperDoctor III interface.

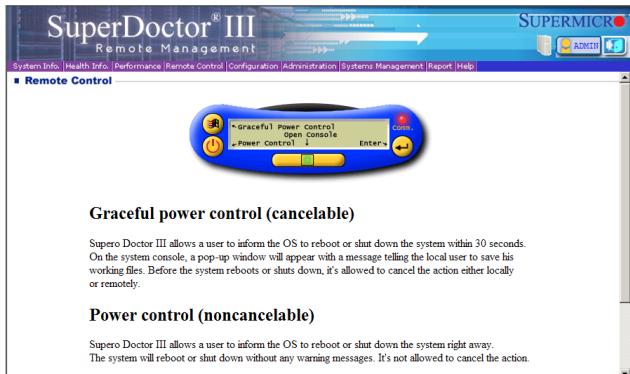
 **Note 1:** The default user name and password are ADMIN.

**Note 2:** In the Windows OS environment, the SuperDoctor III settings take precedence over the BIOS settings. When first installed, SuperDoctor III adopts the temperature threshold settings previously set in BIOS. Any subsequent changes to these thresholds must be made within SuperDoctor, since the SD III settings override the BIOS settings. For the Windows OS to adopt the BIOS temperature threshold settings, please change the SDIII settings to be the same as those set in BIOS.

SuperDoctor® III Interface Display Screen-I (Health Information)



## SuperDoctor® III Interface Display Screen-II (Remote Control)



**Note:** The SDIII utility and the user guide can be downloaded from our website at: <http://www.supermicro.com/products/accessories/software/SuperDoctorIII.cfm>. For Linux, we will still recommend that you use SuperDoctor II.

## **Notes**

(Disclaimer Continued)

The products sold by Supermicro are not intended for and will not be used in life support systems, medical equipment, nuclear facilities or systems, aircraft, aircraft devices, aircraft/emergency communication devices or other critical systems whose failure to perform be reasonably expected to result in significant injury or loss of life or catastrophic property damage. Accordingly, Supermicro disclaims any and all liability, and should buyer use or sell such products for use in such ultra-hazardous applications, it does so entirely at its own risk. Furthermore, buyer agrees to fully indemnify, defend and hold Supermicro harmless for and against any and all claims, demands, actions, litigation, and proceedings of any kind arising out of or related to such ultra-hazardous use or sale.