

SUPER  [®]

X10DGO-T
X10DGO-SXM

USER'S MANUAL

Revision 1.0a

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Manual Revision 1.0a

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Preface

This manual is written for system integrators, IT professionals, and knowledgeable end users. It provides information for the installation and use of the X10DGO(-T/-SXM) motherboard.

About This Motherboard

The Supermicro X10DGO(-T/-SXM) motherboard supports dual Intel® Xeon EP E5-2600 v3/v4 (Socket R3) processors, which balance high performance with improved power efficiency. The processor offers point-to-point connections with a transfer speed of up to 9.6GT/s and TDP (Thermal Design Power) of 160W, featuring new technologies such as Intel® Turbo Technology and vPro Technology for optimized security, flexible virtualization, and smarter resource management. Built with the PCH C612, the X10DGO(-T/-SXM) includes the Intel® Intelligent Power Node Manager, Management Engine (ME), and Digital Media Interface (DMI). The X10DGO(-T/-SXM) has 24 DIMM slots with up to 1536GB of RDIMM (Registered) and LRDIMM (Load Reduced) ECC/Non-ECC DDR4 memory at 2400MT/s, providing higher compute performance and secure encryption. Please refer to our website (<http://www.supermicro.com>) for CPU and memory support updates.

Manual Organization

Chapter 1 describes the features, specifications and performance of the motherboard. It also provides detailed information about the Intel® PCH C612 chipset.

Chapter 2 provides hardware installation instructions. Read this chapter when installing the processor, memory modules and other hardware components into the system. If you encounter any problems, see **Chapter 3**, which describes troubleshooting procedures for video, memory, and system setup stored in the CMOS.

Chapter 4 includes an introduction to BIOS, and provides detailed information on running the CMOS setup utility.

Appendix A provides BIOS error beep codes.

Appendix B lists software Installation Instructions.

Appendix C contains UEFI BIOS recovery instructions.

Conventions Used in the Manual

Pay special attention to the following symbols for proper system installation:

Warning: Important information given to ensure proper system installation or to prevent damage to the components or injury to yourself;



Note: Additional information given to differentiate between models or instructions provided for proper system setup.

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Chapter 1

Overview

1-1 Overview

Checklist

Congratulations on purchasing your computer motherboard from an acknowledged leader in the industry. Supermicro boards are designed with the utmost attention to detail to provide you with the highest standards in quality and performance.

The X10DGO(-T/-SXM) motherboard was designed to be used with a Supermicro-proprietary chassis as an integrated server platform. It is not to be used as a stand-alone product and will not be shipped independently in a retail box. No motherboard shipping package will be provided in your shipment.



Note 1: For your system to work properly, please follow the links below to download all necessary drivers/utilities and the user's manual for your motherboard.

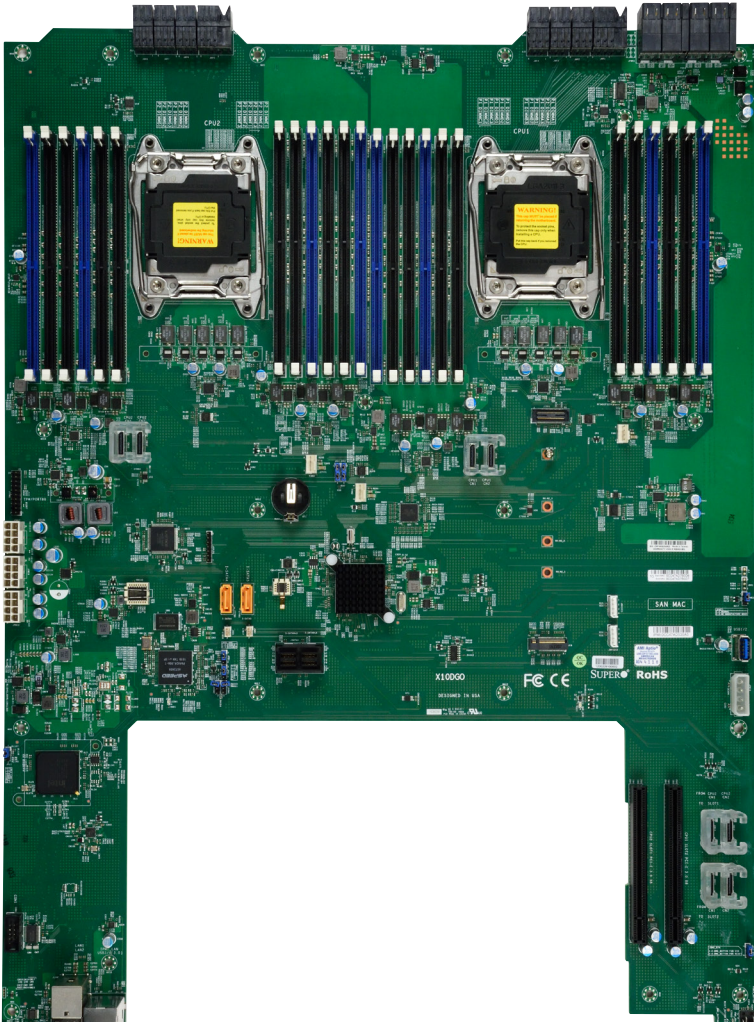
- Supermicro product manuals: <http://www.supermicro.com/support/manuals/>
- Product Drivers and utilities: <ftp://ftp.supermicro.com/>




Note 2: For safety considerations, please refer to the complete list of safety warnings posted on the Supermicro website at http://www.supermicro.com/about/policies/safety_information.cfm.

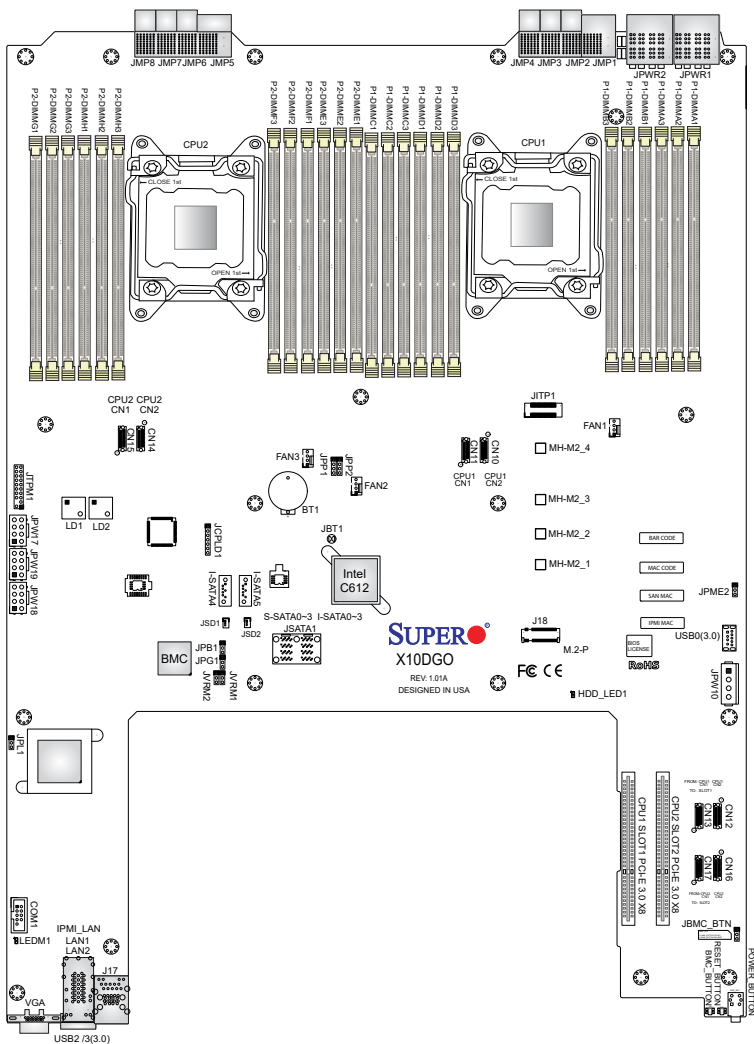
If you have any questions, please contact our support team at support@supermicro.com.


SUPER X10DGO Motherboard Image



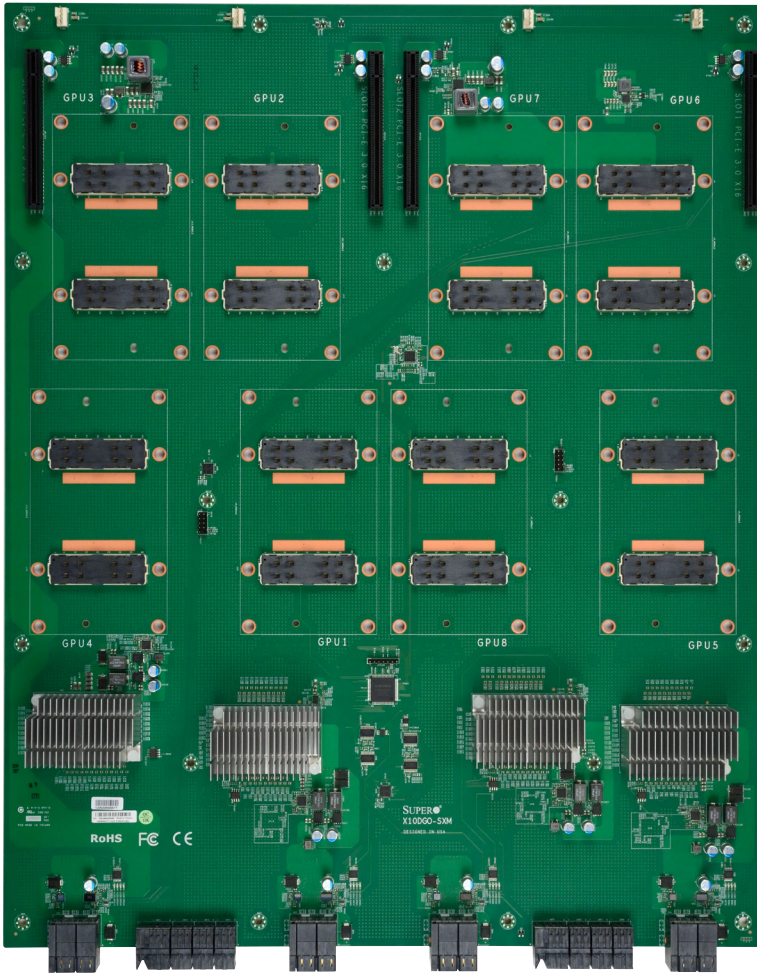
 **Note:** All graphics shown in this manual were based upon the latest PCB Revision available at the time of publishing of the manual. The motherboard you've received may or may not look exactly the same as the graphics shown in this manual.

X10DGO Motherboard Layout

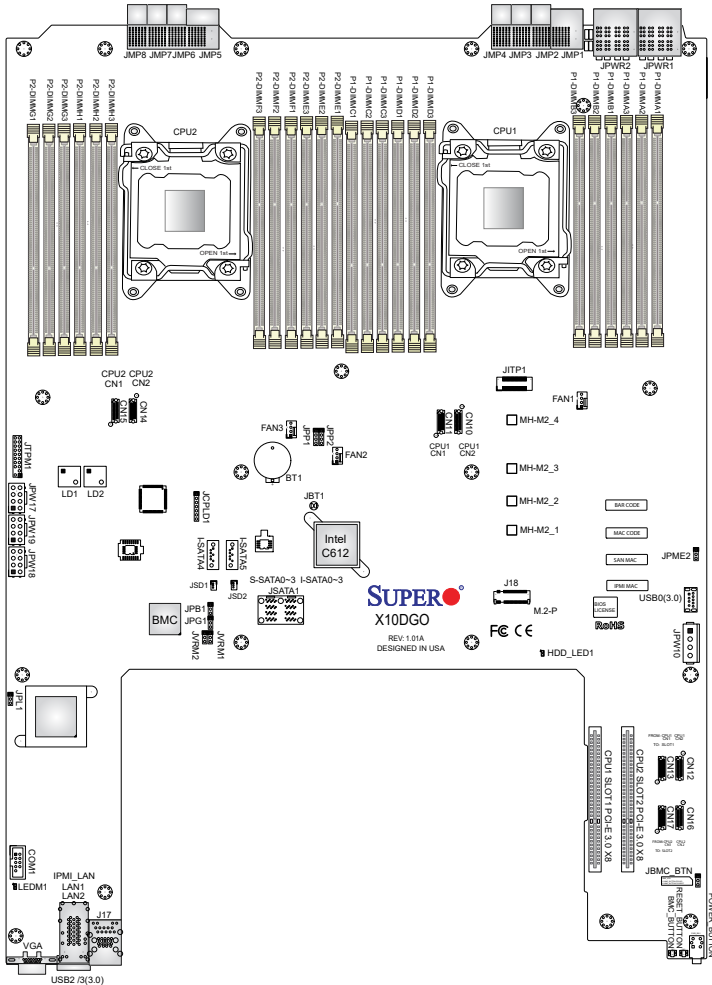


 **Note:** For the latest CPU/Memory updates, please refer to our website at <http://www.supermicro.com/products/motherboard/> for details.

SUPER X10DGO-SXM Image



X10DGO Quick Reference



 **Notes:**

- See Chapter 2 for detailed information on jumpers, I/O ports and JF1 front panel connections.
- "■" indicates the location of "Pin 1".
- Jumpers/LED Indicators not indicated are for internal testing only.
- Use only the correct type of onboard CMOS battery as specified by the manufacturer. Do not install the onboard battery upside down to avoid possible explosion.

X10DGO Jumpers

Jumper	Description	Default Setting
BMC_Button	BMC UID when JBMC_BTN Pins 1-2 are connected BMC Reset when JBMC_BTN Pins 2-3 are connected	BMC UID
JBMC_BTN	Select BMC_Button Function: Pins 1-2: BMC_Button works as BMC UID Button Pins 2-3: BMC_Button works as BMC Reset Button	Pins 1-2
JBT1	Clear CMOS	See Chapter 2
JPB1	BMC Enable	Pins 1-2 (Enabled)
JPG1	VGA Enable	Pins 1-2 (Enabled)
JPL1	GLAN1/GLAN2 Enable	Pins 1-2 (Enabled)
JPME2	ME ManufactureMode	Pins 1-2 (Normal)
JVRM1/JVRM2	I ² C Bus for VRM	Pins 1-2 (BMC: Normal)
Power_Button	Power Button	Push Button Switch
Reset_Button	Reset the System	Push Button Switch

X10DGO Connectors


Connectors	Description
Battery (BT1)	Onboard CMOS Battery (See Chpt. 3 for used battery disposal)
CN10~17	Connects PCI Bus from CPU slot using OCULINK cable (see "2-7 Connecting Cables" for CN10~17 cable connection diagram)
COM1	Serial COM Port
FAN1-3	CPU/System Cooling Fan Headers
J18	M.2 socket 3 for PCIe SSD adapter with M Key or B+M Key
JMP1~8	Connects PCIE bus and controls signals between MB, Midplane, and GPU
JPW17~19	Power for HDD Backplane
JPWR1/JPWR2	Midplane Power Connector
JSD1/JSD2	Power Connector SATA DOM
JTPM1	TPM (Trusted Platform Module)/Port 80 Header
(IPMI) LAN	Dedicated IPMI LAN Port
LAN1/LAN2	X540-based 10G Ethernet LAN Ports 1/2 (X10DGO-T); I350-based 1G LAN Ports 1/2 (X10DGO)

(I-)SATA0~3	Intel® SATA 3.0 Connectors from Intel® PCH SATA controller
(I-)SATA4/5	Intel® SATA SuperDOM Connectors from Intel® PCH
(S)-SATA0~3	SATA Connectors 3.0 from Intel® PCH sSATA controller
SLOT1	PCI-Express 3.0 x8 Slot supported by CPU2
SLOT2	PCI-Express 3.0 x8 Slot supported by CPU1
USB0	Internal USB 3.0 Type A Connector
USB2/3	External USB 3.0 Ports
VGA	VGA Port

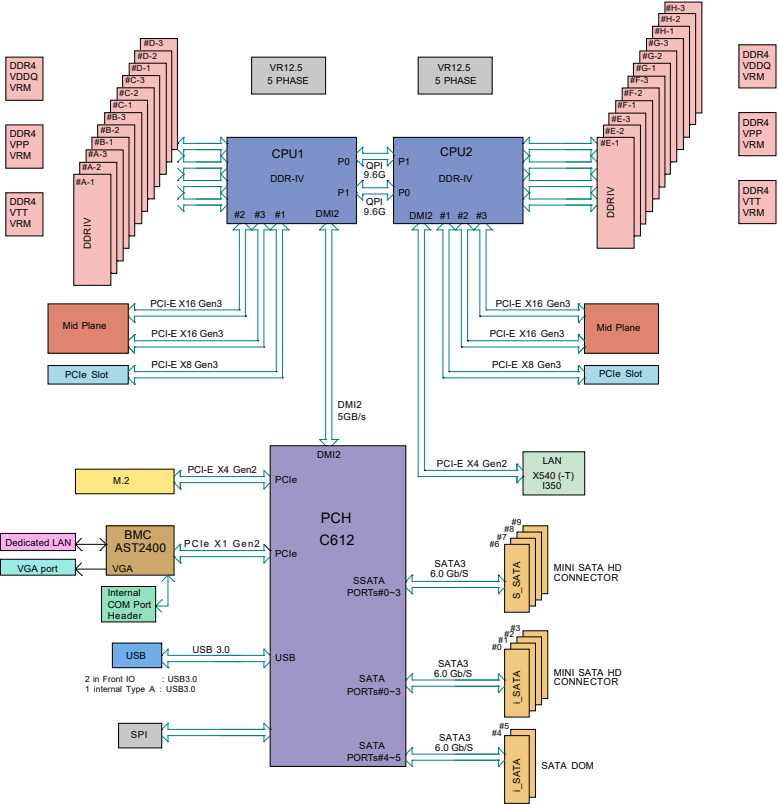
X10DGO LED Indicators

LED	Description	State	Status
LEDM1	BMC Heartbeat LED	Green	BMC Normal


Motherboard Features

CPU	<ul style="list-style-type: none"> Dual Intel® Xeon EP E5-2600 v3/v4 series processors (Socket R3); each processor supports dual full-width Intel® QuickPath Interconnect (QPI) links (with Data Transfer Rate of up to 9.6 GT/s per QPI) 	
Memory	<ul style="list-style-type: none"> Integrated memory controller supports: 1536GB of LRDIMM (Load Reduced) and RDIMM (Registered) ECC/Non-ECC DDR4 (288-pin) memory at up to 2400MT/s in 24 DIMM slots. <p> Note: For the latest CPU/memory updates, please refer to our website at http://www.supermicro.com/products/motherboard.</p>	
	DIMM sizes	
	<ul style="list-style-type: none"> DIMM 	Up to 64GB at 1.2V
Chipset	<ul style="list-style-type: none"> Intel® PCH C612 	
Expansion Slots	<ul style="list-style-type: none"> One (1) PCI-E 3.0 x8 slot supported by CPU1 (Slot1) One (1) PCI-E 3.0 x8 slot supported by CPU2 (Slot2) 	
Graphics	<ul style="list-style-type: none"> Graphics controller via the Aspeed AST2400 BMC 	
Network	<ul style="list-style-type: none"> One (1) Dedicated IPMI LAN One Intel i350 Gigabit (10/100/1000 Mb/s) Ethernet Dual-Channel Controller for LAN 1/LAN 2 ports (X10DGO) One Intel X540 Dual-Channel Controller for 10G-based-T LAN 1/LAN 2 ports (X10DGO-T) 	
I/O Devices	SATA Connections	
	<ul style="list-style-type: none"> SATA Ports 	Ten (10) SATA 3.0 ports <ul style="list-style-type: none"> I-SATA0~3 I-SATA4/5 (SuperDOM support) S-SATA0~3
	<ul style="list-style-type: none"> RAID 	RAID 0, 1, 5, 10
	IPMI 2.0	
	<ul style="list-style-type: none"> IPMI 2.0 supported by Aspeed AST2400 	
	Serial (COM) Port	
	<ul style="list-style-type: none"> One (1) COM port header 	

Peripheral Devices	USB Devices
	<ul style="list-style-type: none"> • Two (2) USB 3.0 ports on the front panel (USB2/3) • One (1) USB 3.0 Internal USB type A (USB0)
BIOS	<ul style="list-style-type: none"> • 128Mb SPI AMI BIOS® Flash ROM
	<ul style="list-style-type: none"> • ACPI 3.0, SMBIOS 2.7 or later, PCI/FW 3.0, BIOS rescue hot-key, RTC (Real Time Clock) wakeup
Power Management	<ul style="list-style-type: none"> • ACPI power management
	<ul style="list-style-type: none"> • SuperDoctor® 4, 5
	<ul style="list-style-type: none"> • Keyboard wakeup from S5
	<ul style="list-style-type: none"> • Power-on mode for AC power recovery
	<ul style="list-style-type: none"> • Management Engine (ME)
System Health Monitoring	System Health/CPU Monitoring
	<ul style="list-style-type: none"> • Onboard voltage monitoring for VCCP0, VCCP1, VDDQAB/CD/EF/GH, +1.5V_PCH, +1.05V_PCH, +1.2V_BMC, +12V_STBY, +3.3V, +5V, +3.3V standby, +5V standby, VBAT, CPU, memory, PCH temperature, system temperature, and memory temperature
	<ul style="list-style-type: none"> • CPU 5-phase switching voltage regulator
	<ul style="list-style-type: none"> • CPU thermal trip support
	Fan Control
	<ul style="list-style-type: none"> • Three (3) 4-pin fan headers
	<ul style="list-style-type: none"> • Fan speed control
System Management	<ul style="list-style-type: none"> • PECI (Platform Environment Configuration Interface) 2.0 support
	<ul style="list-style-type: none"> • UID (Unit Identification)/Remote UID
	<ul style="list-style-type: none"> • LAN activity LED
	<ul style="list-style-type: none"> • System resource alert via SuperDoctor® 5
	<ul style="list-style-type: none"> • SuperDoctor® 5, Watch Dog, NMI, RoHS
	<ul style="list-style-type: none"> • Power supply monitoring
Dimensions	<ul style="list-style-type: none"> • 22.0" (L) x 17.0" (W) (558.8 mm x 431.8 mm)



System Block Diagram

 **Note:** This is a general block diagram and may not represent the features on your motherboard. See the "Motherboard Features" pages for the actual specifications of each motherboard.

1-2 Processor and Chipset Overview

Built upon the functionality and capability of the dual Intel® Xeon EP E5-2600 v3/v4 series processors (Socket R3) and the Intel® PCH C612, the X10DGO(-T) motherboard provides the best balanced solution of performance, power-efficiency, and features to address the diverse needs of next-generation computer users.

With support of new Intel® Microarchitecture 22nm Processing Technology, the X10DGO(-T/-SXM) dramatically increases compute performance and features faster, more secure encryption for a multitude of server applications. This platform offers maximum I/O expandability, energy efficiency, and data reliability.

The PCH C612 chip provides Enterprise SMBus and MCTP support with the following features included:

- DDR4 288-pin memory support on Socket R3
- Support for MCTP protocol and ME
- Intel® Advanced Vector Extensions 2.0
- Improved I/O capabilities to high-storage-capacity configurations
- Low-power, high-reliability thermal profile processor options
- SPI Enhancements
- Intel® Node Manager 3.0
- Intel Virtualization Technology for Directed I/O (Intel VT-d)
- BMC supports remote management, virtualization, and the security package for enterprise platforms



Note: Intel® Node Manager 3.0 support is dependent on the power supply used in the system.

1-3 Special Features

Recovery from AC Power Loss

The Basic I/O System (BIOS) provides a setting that determines how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off (in which case you must press the power switch to turn it back on), or for it to automatically return to the power-on state. See the Advanced BIOS Setup section for this setting. The default setting is **Last State**.

1-4 System Health Monitoring

This section describes the features of system health monitoring of the motherboard. This motherboard has an onboard BMC chip that monitors system health. An onboard voltage monitor will scan the following onboard voltages continuously: VCCP0, VCCP1, VDDQAB/CD/EF/GH, +1.5V_PCH, +1.05V_PCH, +1.2V_BMC, +12V_STBY, +3.3V, +5V, +3.3V standby, +5V standby, VBAT, CPU, memory, PCH temperature, system temperature, and memory temperature. Once a voltage becomes unstable, a warning is given, or an error message is sent to the screen. The user can adjust the voltage thresholds to define the sensitivity of the voltage monitor.

Fan Status Monitor with Firmware Control

The system health monitoring support provided by the BMC controller can check the RPM status of a cooling fan. The onboard CPU and chassis fan speeds are controlled by IPMI Thermal Management.

Environmental Temperature Control

A thermal control sensor embedded in IPMI 2.0 monitors the CPU temperatures in real time and will turn on the onboard cooling fans whenever the CPU temperature exceeds a user-defined threshold to prevent the CPU from overheating, and it will also send warning messages to alert the user when the chassis temperature is too high.



Note: To avoid possible system overheating, please be sure to provide adequate airflow to your system.

System Resource Alert

This feature is available when used with SuperDoctor® 5. SuperDoctor® 5 is used to notify the user of certain system events. For example, you can configure SuperDoctor® 5 to provide you with warnings when the system temperature, CPU temperatures, voltages, and fan speeds go beyond a predefined range.

1-5 ACPI Features

ACPI stands for Advanced Configuration and Power Interface. The ACPI specification defines a flexible and abstract hardware interface that provides a standard way to integrate power management features throughout a PC system, including its hardware, operating system and application software. This enables the system to automatically turn on and off peripherals such as CD-ROMs, network cards, hard disk drives and printers.

In addition to operating system-directed power management, ACPI also provides a generic system event mechanism for Plug and Play, and an operating system-independent interface for configuration control. ACPI leverages the Plug and Play BIOS data structures, while providing a processor architecture-independent implementation that is compatible with Windows 7, Windows Vista, Windows 8 and Windows 2012 operating systems.

1-6 Power Supply

As with all computer products, a stable power source is necessary for proper and reliable operation. It is even more important for processors that have high CPU clock rates.

The X10DGO(-T) motherboard accommodates two power connectors to get power from the midplane. At least two power supply modules are required to power up the system.

1-7 Advanced Power Management

The following new advanced power management features are supported by this motherboard:

Intel® Intelligent Power Node Manager (IPNM) (Available when the SPM Utility is Installed)

The Intel® Intelligent Power Node Manager 3.0 (IPNM) provides your system with real-time thermal control and power management for maximum energy efficiency. Although IPNM Specification Version 2.0/3.0 is supported by the BMC (Baseboard Management Controller), your system must also have IPNM-compatible Management Engine (ME) firmware installed to use this feature.



Note: IPNM support is dependent on the power supply used in the system.

Management Engine (ME)

Management Engine, an ARC controller embedded in the PCH, provides Server Platform Services (SPS) support to your system. The services provided by SPS are different from those provided by the ME on client platforms.

Notes

Chapter 2

Installation

2-1 Standardized Warning Statements

The following statements are industry-standard warnings, provided to warn the user of situations which have the potential for bodily injury. Should you have questions or experience difficulty, contact Supermicro's Technical Support department for assistance. Only certified technicians should attempt to install or configure components.

Read this section in its entirety before installing or configuring components in the Supermicro chassis.

Battery Handling



Warning!

There is a danger of explosion if the battery is replaced incorrectly. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions

電池の取り扱い

電池交換が正しく行われなかった場合、破裂の危険性があります。交換する電池はメーカーが推奨する型、または同等のものを使用下さい。使用済電池は製造元の指示に従って処分して下さい。

警告

電池更換不當會有爆炸危險。請只使用同類電池或製造商推薦的功能相當的電池更換原有電池。請按製造商的說明處理廢舊電池。

警告

電池更換不當會有爆炸危險。請使用製造商建議之相同或功能相當的電池更換原有電池。請按照製造商的說明指示處理廢棄舊電池。

Warnung

Bei Einsetzen einer falschen Batterie besteht Explosionsgefahr. Ersetzen Sie die Batterie nur durch den gleichen oder vom Hersteller empfohlenen Batterietyp. Entsorgen Sie die benutzten Batterien nach den Anweisungen des Herstellers.

Attention

Danger d'explosion si la pile n'est pas remplacée correctement. Ne la remplacer que par une pile de type semblable ou équivalent, recommandée par le fabricant. Jeter les piles usagées conformément aux instructions du fabricant.

¡Advertencia!

Existe peligro de explosión si la batería se reemplaza de manera incorrecta. Reemplazar la batería exclusivamente con el mismo tipo o el equivalente recomendado por el fabricante. Desechar las baterías gastadas según las instrucciones del fabricante.

אזהרה!

קיימת סכנת פיצוץ של הסוללה במידה והוחלפה בדרך לא תקינה. יש להחליף את הסוללה בסוג התואם מחברת יצרן מומלצת.

סילוק הסוללות המשומשות יש לבצע לפי הוראות היצרן.

هناك خطر من انفجار في حالة استبدال البطارية بطريقة غير صحيحة فعليك استبدال البطارية فقط بنفس النوع أو ما يعادلها كما أوصت به الشركة المصنعة تخلص من البطاريات المستعملة وفقا لتعليمات الشركة الصانعة

경고!

배터리가 올바르게 교체되지 않으면 폭발의 위험이 있습니다. 기존 배터리와 동일하거나 제조사에서 권장하는 동등한 종류의 배터리로만 교체해야 합니다. 제조사의 안내에 따라 사용된 배터리를 처리하여 주십시오.

Waarschuwing

Er is ontploffingsgevaar indien de batterij verkeerd vervangen wordt. Vervang de batterij slechts met hetzelfde of een equivalent type die door de fabrikant aanbevolen wordt. Gebruikte batterijen dienen overeenkomstig fabrieksvoorschriften afgevoerd te worden.

Product Disposal



Warning!

Ultimate disposal of this product should be handled according to all national laws and regulations.

製品の廃棄

この製品を廃棄処分する場合、国の関係する全ての法律・条例に従い処理する必要があります。

警告

本产品的废弃处理应根据所有国家的法律和规章进行。

警告

本產品的廢棄處理應根據所有國家的法律和規章進行。

Warnung

Die Entsorgung dieses Produkts sollte gemäß allen Bestimmungen und Gesetzen des Landes erfolgen.

¡Advertencia!

Al deshacerse por completo de este producto debe seguir todas las leyes y reglamentos nacionales.

Attention

La mise au rebut ou le recyclage de ce produit sont généralement soumis à des lois et/ou directives de respect de l'environnement. Renseignez-vous auprès de l'organisme compétent.

סילוק המוצר

אזהרה!
סילוק סופי של מוצר זה חייב להיות בהתאם להנחיות וחוקי המדינה.

عند التخلص النهائي من هذا المنتج ينبغي التعامل معه وفقا لجميع القوانين واللوائح الوطنية

경고!

이 제품은 해당 국가의 관련 법규 및 규정에 따라 폐기되어야 합니다.

Waarschuwing

De uiteindelijke verwijdering van dit product dient te geschieden in overeenstemming met alle nationale wetten en reglementen.

2-2 Static-Sensitive Devices

Electrostatic Discharge (ESD) can damage electronic components. To avoid damaging your system board, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing the board from the antistatic bag.
- Handle the motherboard by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure that your system chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.

Unpacking

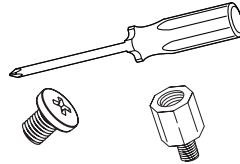
The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the motherboard, make sure that the person handling it is static protected.

2-3 Motherboard Installation

All motherboards have standard mounting holes to fit different types of chassis. Make sure that the locations of all the mounting holes for both motherboard and chassis match. Although a chassis may have both plastic and metal mounting fasteners, metal ones are highly recommended because they ground the motherboard to the chassis. Make sure that the metal standoffs click in or are screwed in tightly. Then use a screwdriver to secure the motherboard onto the motherboard tray.

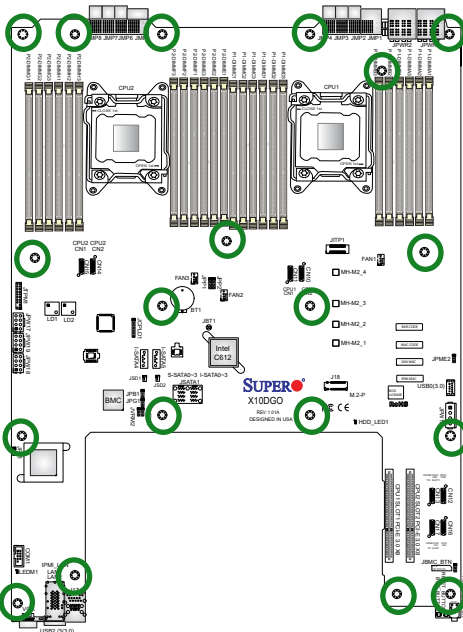
Tools Needed

- Phillips Screwdriver
- Pan head screws (19 pieces)
- Standoffs (19 pieces, if needed)



Location of Mounting Holes

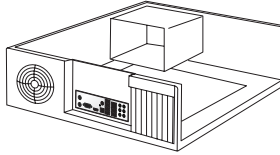
There are nineteen (19) mounting holes on this motherboard as shown below.



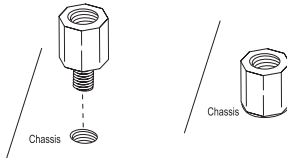
Caution: 1) To avoid damaging the motherboard and its components, please do not use a force greater than 8 lb/inch on each mounting screw during motherboard installation. 2) Some components are very close to the mounting holes. Please take precautionary measures to prevent damage to these components when installing the motherboard to the chassis.

Installing the Motherboard

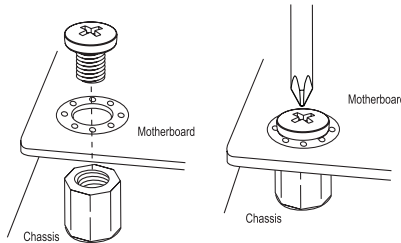
1. Install the I/O shield into the chassis.




2. Locate the mounting holes on the motherboard.
3. Locate the matching mounting holes on the chassis. Align the mounting holes on the motherboard against the mounting holes on the chassis.
4. Install standoffs in the chassis as needed.



5. Install the motherboard into the chassis carefully to avoid damaging motherboard components.
6. Using the Phillips screwdriver, insert a Pan head #6 screw into a mounting hole on the motherboard and its matching mounting hole on the chassis.



7. Repeat Step 5 to insert #6 screws into all mounting holes.
8. Make sure that the motherboard is securely placed in the chassis.

 **Note:** Images displayed are for illustration only. Your chassis or components might look different from those shown in this manual.

2-4 Processor and Heatsink Installation

Warning: When handling the processor package, avoid placing direct pressure on the label area.

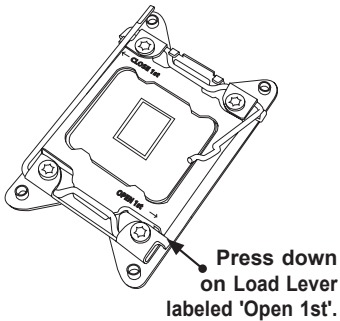
 **Notes:**

- Always connect the power cord last, and always remove it before adding, removing or changing any hardware components. Make sure that you install the processor into the CPU socket before you install the CPU heatsink.
- If you buy a CPU separately, make sure that you use an Intel-certified multi-directional heatsink only.
- Make sure to install the motherboard into the chassis before you install the CPU heatsink.
- When receiving a motherboard without a processor pre-installed, make sure that the plastic CPU socket cap is in place and none of the socket pins are bent; otherwise, contact your retailer immediately.
- Refer to the Supermicro website for updates on CPU support.

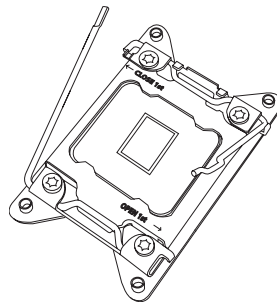
Installing the LGA2011 Processor


1. There are two load levers on the LGA2011 socket. To open the socket cover, first press and release the load lever labeled 'Open 1st'.

1



2

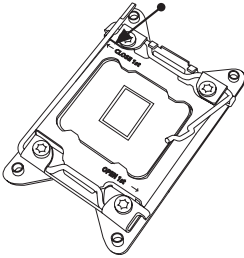


 **Note:** All graphics, drawings and pictures shown in this manual are for illustration only. The components that came with your machine may or may not look exactly the same as those shown in this manual.

2. Press the second load lever labeled 'Close 1st' to release the load plate that covers the CPU socket from its locking position.

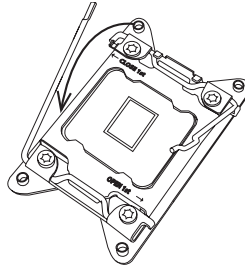
1

Press down on Load
Lever 'Close 1st'



2

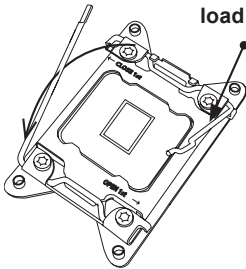
Pull lever away from
the socket



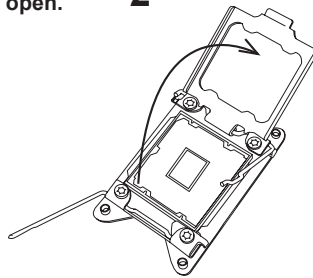
3. With the lever labelled 'Close 1st' fully retracted, gently push down on the lever labelled 'Open 1st' to open the load plate. Lift the load plate to open it completely.


1

Gently push
down to pop the
load plate open.

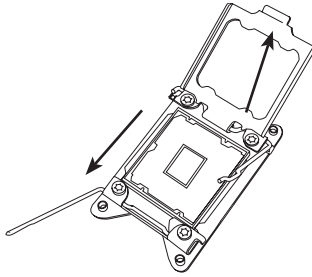


2

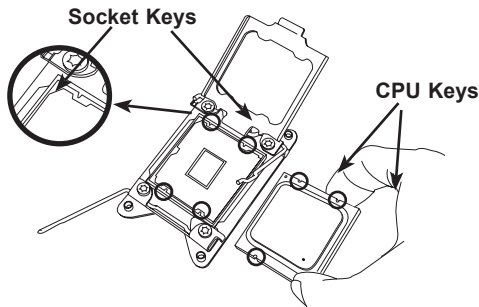


 **Note:** All graphics, drawings and pictures shown in this manual are for illustration only. The components that came with your machine may or may not look exactly the same as those shown in this manual.

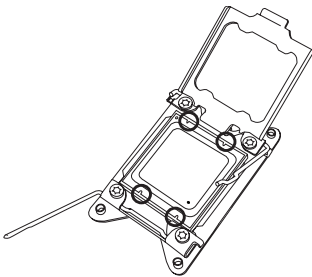
1. Use your thumb and the index finger to loosen the lever and open the load plate.



2. Using your thumb and index finger, hold the CPU on its edges. Align the CPU keys, which are semi-circle cutouts, against the socket keys.



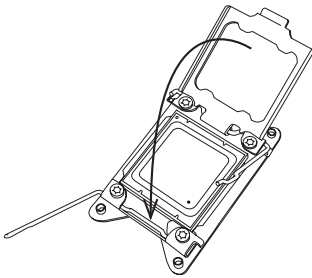
3. Once they are aligned, carefully lower the CPU straight down into the socket. (Do not drop the CPU on the socket. Do not move the CPU horizontally or vertically. Do not rub the CPU against the surface or against any pins of the socket to avoid damaging the CPU or the socket.)



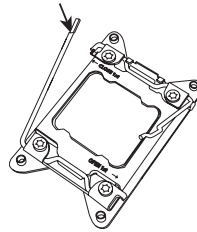
Warning: You can only install the CPU inside the socket in one direction. Make sure that it is properly inserted into the CPU socket before closing the load plate. If it doesn't close properly, do not force it as it may damage your CPU. Instead, open the load plate again to make sure that the CPU is aligned properly.

4. With the CPU inside the socket, inspect the four corners of the CPU to make sure that the CPU is properly installed.
5. Close the load plate with the CPU inside the socket. Lock the lever labelled 'Close 1st', then lock the lever labelled 'Open 1st' second. Using your thumb gently push the load levers down to the lever locks.

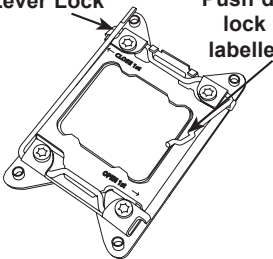
1 Gently close the load plate.



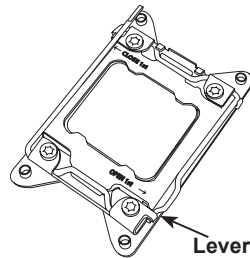
2 Push down and lock the lever labelled 'Close 1st'.



3 Lever Lock
Push down and lock the lever labelled 'Open 1st'.

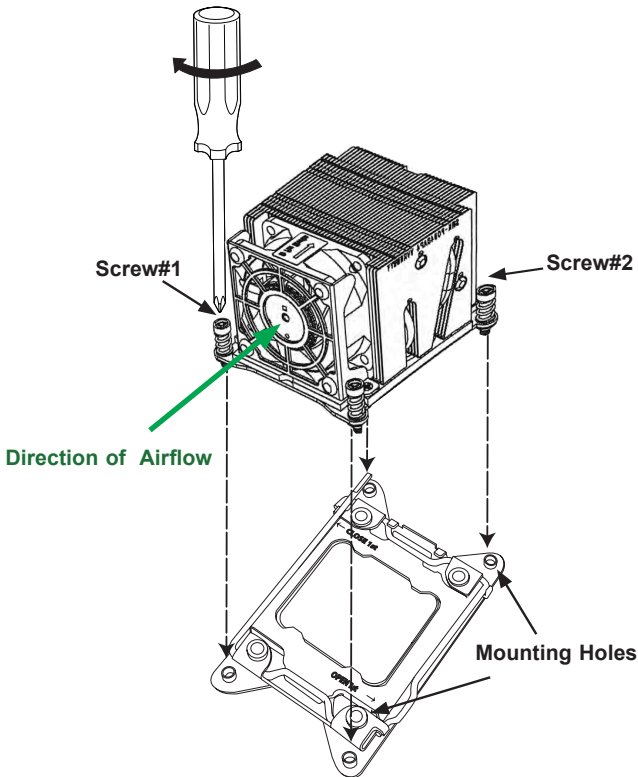



4 Lever Lock



Installing a Passive CPU Heatsink

1. Do not apply any thermal grease to the heatsink or the CPU die -- the required amount has already been applied.
2. Place the heatsink on top of the CPU so that the four mounting holes are aligned with those on the Motherboard and the Heatsink Bracket underneath.
3. Screw in two diagonal screws (i.e., the #1 and the #2 screws) until just snug (-do not over-tighten the screws to avoid possible damage to the CPU.)
4. Finish the installation by fully tightening all four screws.



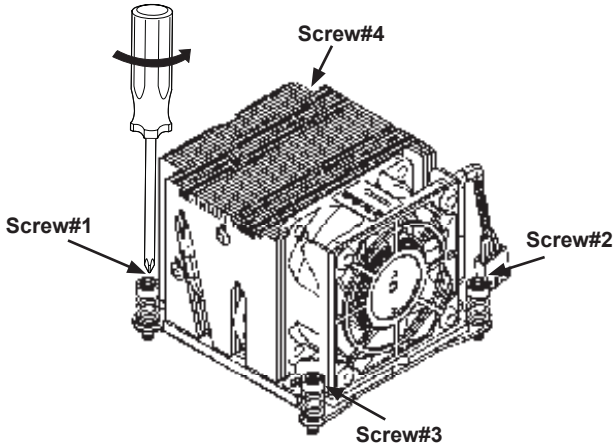
 **Note:** For optimized airflow, please follow your chassis airflow direction to properly install the heatsink. Graphics included in this manual are for reference only. They may look different from the components installed in your system.


Removing the Heatsink

Warning: We do not recommend that the CPU or the heatsink be removed. However, if you do need to uninstall the heatsink, please follow the instructions below to uninstall the heatsink to prevent damage done to the CPU or the CPU socket.

1. Unscrew the heatsink screws from the motherboard in the sequence as shown in the illustration below.
2. Gently wriggle the heatsink to loosen it from the CPU. (Do not use excessive force when wriggling the heatsink!)
3. Once the CPU is loosened from the socket, remove the CPU from the CPU socket.
4. Remove the used thermal grease and clean the surface of the CPU and the heatsink, Reapply the proper amount of thermal grease on the surface before reinstalling the CPU and the heatsink.

**Loosen screws
in sequence as
shown.**



 **Note:** All graphics, drawings and pictures shown in this manual are for illustration only. The components that came with your machine may or may not look exactly the same as those shown in this manual.

2-5 Installing and Removing the Memory Modules

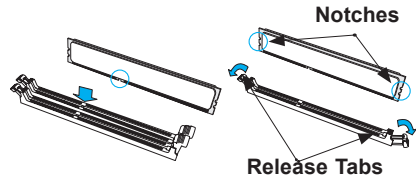
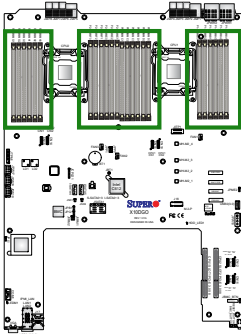
Note: Check Supermicro's website for recommended memory modules.

CAUTION

Exercise extreme care when installing or removing DIMM modules to prevent any possible damage.

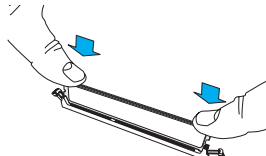
Installing Memory Modules

1. Insert the desired number of DIMMs into the memory slots, starting with P1-DIMMA1. (For best performance, please use the memory modules of the same type and speed in the same bank.)
2. Push the release tabs outwards on both ends of the DIMM slot to unlock it.



3. Align the key of the DIMM module with the receptive point on the memory slot.
4. Align the notches on both ends of the module against the receptive points on the ends of the slot.
5. Use two thumbs together to press the notches on both ends of the module straight down into the slot until the module snaps into place.
6. Press the release tabs to the locking positions to secure the DIMM module into the slot.

Press both notches straight down into the memory slot at the same time.



Removing Memory Modules

Press both notches on the ends of the DIMM module to unlock it. Once the DIMM module is loosened, remove it from the memory slot.

Memory Support for the X10DGO Motherboard

The X10DGO(-T/-SXM) motherboard supports up to 1536GB of LRDIMM (Load Reduced) and RDIMM (Registered) ECC/Non-ECC DDR4 (288-pin) memory at up to 2400MT/s in 24 DIMM slots. For the latest memory updates, please refer to our website at <http://www.supermicro.com/products/motherboard>.

Processor & Memory Module Population Configuration

For memory to work properly, follow the tables below for memory installation.

Processors and their Corresponding Memory Modules													
CPU#	Corresponding DIMM Modules												
CPU 1	P1-DIMMA1~P1DIMMD3												
P1-DIMM-	A1	B1	C1	D1	A2	B2	C2	D2	A3	B3	C3	D3	
CPU2	P2-DIMME1~P1DIMMH3												
P2-DIMM-	E1	F1	G1	H1	E2	F2	G2	H2	E3	F3	G3	H3	

Processor and Memory Module Population for Optimal Performance	
Number of CPUs+DIMMs	CPU and Memory Population Configuration Table (For memory to work properly, please follow the instructions below.)
1 CPU & 2 DIMMs	CPU1 P1-DIMMA1/P1-DIMMB1
1 CPU & 4 DIMMs	CPU1 P1-DIMMA1/P1-DIMMB1, P1-DIMMC1/P1-DIMMD1
1 CPU & 5-8 DIMMs	CPU1 P1-DIMMA1/P1-DIMMB1, P1-DIMMC1/P1-DIMMD1 + Any memory pairs in P1-DIMMA2/P1-DIMMB2/P1-DIMMC2/P1-DIMMD2 slots
2 CPUs & 4 DIMMs	CPU1 + CPU2 P1-DIMMA1/P1-DIMMB1, P2-DIMME1/P2-DIMMF1
2 CPUs & 6 DIMMs	CPU1 + CPU2 P1-DIMMA1/P1-DIMMB1/P1-DIMMC1/P1-DIMMD1, P2-DIMME1/P2-DIMMF1
2 CPUs & 8 DIMMs	CPU1 + CPU2 P1-DIMMA1/P1-DIMMB1/P1-DIMMC1/P1-DIMMD1, P2-DIMME1/P2-DIMMF1/P2-DIMMG1/P2-DIMMH1
2 CPUs & 10-14 DIMMs	CPU1/CPU2 P1-DIMMA1/P1-DIMMB1/P1-DIMMC1/P1-DIMMD1, P2-DIMME1/P2-DIMMF1/P2-DIMMG1/P2-DIMMH1 + Any memory pairs in P1, P2 DIMM slots
2 CPUs & 16 DIMMs	CPU1/CPU2 P1-DIMMA1/P1-DIMMB1/P1-DIMMC1/P1-DIMMD1, P2-DIMME1/P2-DIMMF1/P2-DIMMG1/P2-DIMMH1, P1-DIMMA2/P1-DIMMB2/P1-DIMMC2/P1-DIMMD2, P2-DIMME2/P2-DIMMF2/P2-DIMMG2/P2-DIMMH2
2 CPUs & 16-24 DIMMs	CPU1/CPU2 P1-DIMMA1/P1-DIMMB1/P1-DIMMC1/P1-DIMMD1, P2-DIMME1/P2-DIMMF1/P2-DIMMG1/P2-DIMMH1, P1-DIMMA2/P1-DIMMB2/P1-DIMMC2/P1-DIMMD2, P2-DIMME2/P2-DIMMF2/P2-DIMMG2/P2-DIMMH2, P1-DIMMA3/P1-DIMMB3/P1-DIMMC3/P1-DIMMD3, P2-DIMME3/P2-DIMMF3/P2-DIMMG3/P2-DIMMH3

Populating DDR4 DIMM Modules for the E5-2600 v3/v4-based Platform

Populating RDIMM/LRDIMM DDR4 Memory Modules									
Type	Ranks Per DIMM and Data Width	DIMM Capacity (GB)		Speed (MT/s); Voltage (V); Slots per Channel (SPC) and DIMMs per Channel (DPC)					
				3 Slots per Channel					
				1 DPC		2 DPC		3 DPC	
				E5-2600 V3	E5-2600 V4	E5-2600 V3	E5-2600 V4	E5-2600 V3	E5-2600 V4
		4 GB	8 GB	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V	1.2 V
RDIMM	SRx4	8 GB	16 GB	2133	2400	1866	2133	1600	1600
RDIMM	SRx8	4 GB	8 GB	2133	2400	1866	2133	1600	1600
RDIMM	DRx8	8 GB	16 GB	2133	2400	1866	2133	1600	1600
RDIMM	DRx4	16 GB	32 GB	2133	2400	1866	2133	1600	1600
LRDIMM	QRx4	32 GB	64 GB	2133	2400	2133	2400	1600	1866
LRDIMM 3DS	8Rx4	64 GB	128 GB	2133	2400	2133	2400	1600	1866

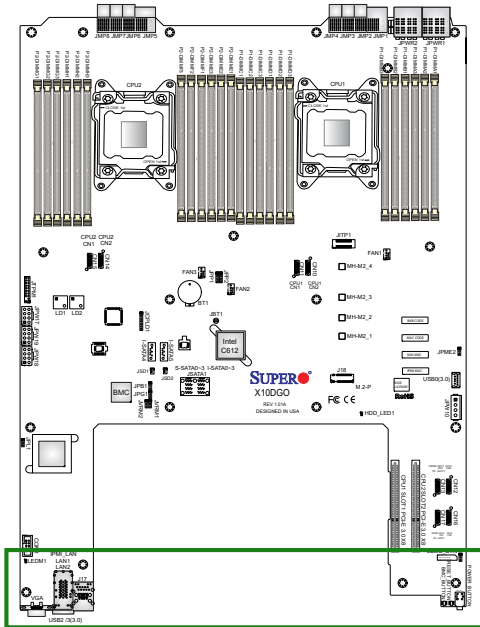
An Important Note:

- For the memory modules to work properly, please install DIMM modules of the same type and speed on the motherboard. Mixing of DIMMs of different types or different speeds is not allowed.

2-6 Control Panel Connectors and I/O Ports

The I/O ports are color coded in conformance with the industry standards. See the picture below for the colors and locations of the various I/O ports.

Front Panel Connectors and I/O Ports



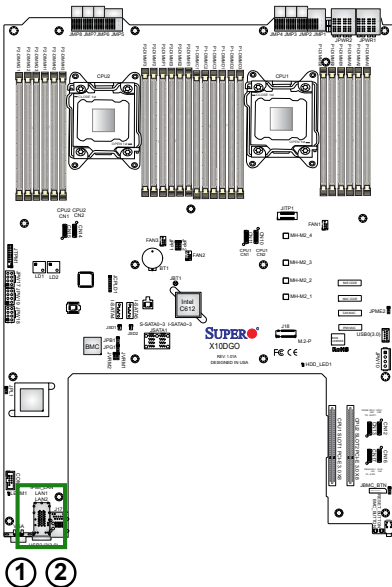
Front Panel I/O Port Locations and Definitions

1. VGA Connector	6. USB3 (3.0)
2. LAN1	7. BMC Button
3. LAN2	8. Reset Button
4. Dedicated IPMI LAN Port	9. Power Button
5. USB2 (3.0)	

Ethernet Ports

Two Ethernet ports (LAN1, LAN2) are located on the I/O front panel. These Ethernet ports support 1 GbE (X10DGO) or 10 GbE (X10DGO-T). In addition, a dedicated IPMI LAN is located above USB3/4 on the back panel. All Ethernet ports accept RJ45 type cables. Please refer to the LED Indicator Section for LAN LED information.

IPMI LAN Pin Definition			
Pin #	Definition	Pin #	Definition
19	VCT	26	TD4+
20	TX1+	27	TX4-
21	TX1-	28	GND
22	TX2+	29	Link 1000 LED (Amber)
23	TX2-	30	Link 100 LED (Green)
24	TX3+	31	P3V3_STBY
25	TX3-	32	ACT LED (Yellow)



LAN Ports Pin Definition			
10GbE -- LAN1			
Pin #	Definition	Pin #	Definition
A1	TDR1+	A10	TDR4+
A2	TDR1-	A11	TDR4-
A3	TRCT1	A12	TRCT4
A4	TDR2+	A13	IET+
A5	TDR2-	A14	IET-
A6	TRCT2	A15	Yellow-ACT-LED+
A7	TDR3+	A16	Yellow-ACT-LED-
A8	TDR3-	A17	1G-LINK-LED (Amber)
A9	TRCT3	A18	10G_LINK-LED (GREEN)
10GbE -- LAN2			
Pin #	Definition	Pin #	Definition
B1	TDR1+	B10	TDR4+
B2	TDR1-	B11	TDR4-
B3	TRCT1	B12	TRCT4
B4	TDR2+	B13	IET+
B5	TDR2-	B14	IET-
B6	TRCT2	B15	Yellow-ACT-LED+
B7	TDR3+	B16	Yellow-ACT-LED-
B8	TDR3-	B17	1G-LINK-LED (Amber)
B9	TRCT3	B18	10G_LINK-LED (GREEN)
1GbE -- LAN1			
Pin #	Definition	Pin #	Definition
A1	TDR1+	A10	TDR4+
A2	TDR1-	A11	TDR4-
A3	TRCT1	A12	TRCT4
A4	TDR2+	A13	Yellow-ACT-LED+
A5	TDR2-	A14	Yellow-ACT-LED-
A6	TRCT2	A15	1G-LINK-LED (Amber)
A7	TDR3+	A16	100M_LINK-LED (GREEN)
A8	TDR3-		
A9	TRCT3		
1GbE -- LAN2			
Pin #	Definition	Pin #	Definition
B1	TDR1+	B10	TDR4+
B2	TDR1-	B11	TDR4-
B3	TRCT1	B12	TRCT4
B4	TDR2+	B13	Yellow-ACT-LED+
B5	TDR2-	B14	Yellow-ACT-LED-
B6	TRCT2	B15	1G-LINK-LED (Amber)
B7	TDR3+	B16	100M_LINK-LED (GREEN)
B8	TDR3-		
B9	TRCT3		

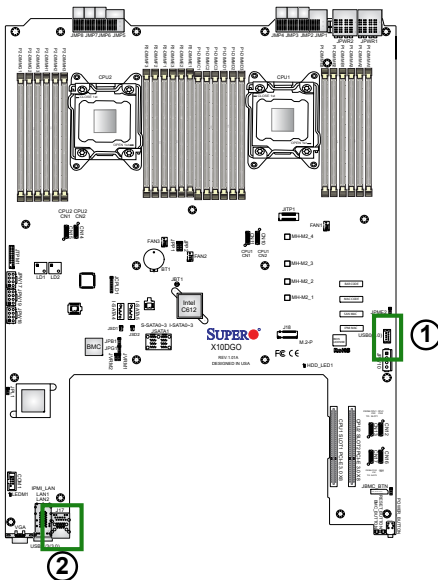
1. LAN1/2
2. IPMI LAN

Universal Serial Bus (USB)

Two USB 3.0 ports (USB 2/3) are located on the I/O front panel. In addition, there is one internal USB 3.0 Type A connector. See the tables on the right for pin definitions.

Internal USB (3.0) 0	
Pin#	Description
1	VBUS
2	D-
3	D+
4	Ground
5	SSRX-
6	SSRX+
7	GND_DRAIN
8	SSTX-
9	SSTX+

Front Panel USB (3.0) 2/3 Pin Definition			
Pin #	Definition	Pin #	Definition
1	VBUS1	10	VBUS2
2	D1-	11	D2-
3	D1+	12	D2+
4	GND	13	GND
5	SSRX1-	14	STDA_SSRX2-
6	SSRX1+	15	STDA_SSRX2+
7	GND_DRAIN	16	GND_DRAIN
8	STDA_SSTX1-	17	STDA_SSTX2-
9	STDA_SSTX1+	18	STDA_SSTX2+



- 1. USB0
- 2. USB2/3

Video Connector

A Video (VGA) connector is located on the I/O front panel. This connector is used to provide video and CRT display. Refer to the motherboard layout below for the location.

Power Button

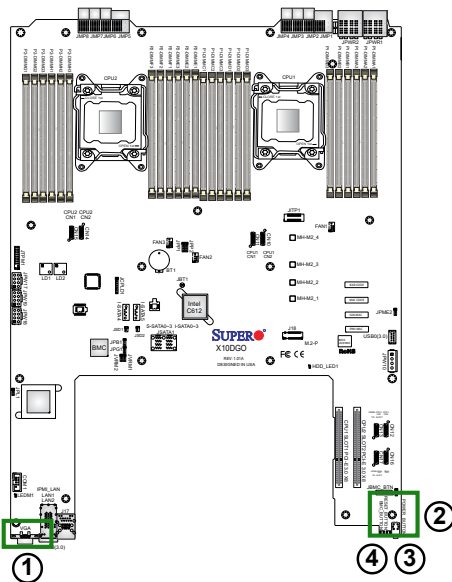
Press this button to turn on the system.

Reset Button

Press this button to force the system to reset.

BMC Button

The BMC button function is determined by the JBMC_BTN. It will function as the BMC UID button when JBMC_BTN(1-2) is connected. Pressing this button will toggle UID LED. It will also function as the BMC Reset button when JBMC_BTN(2-3) is connected. Pressing this button will reset the BMC chip.



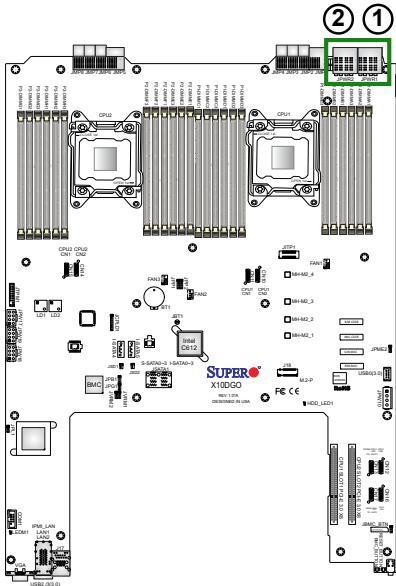
1. VGA
2. Power Button
3. Reset Button
4. BMC Button

2-7 Connecting Cables

Power Connectors

Two 48-pin (JPWR1/2) main power connectors are located on the motherboard. See the tables on the right for pin definitions.

Midplane Power Connector (JPW2/JPW1)			
Pin #	Definition	Pin #	Definition
B1[A:F]	12V	A1[A:F]	GND
B2[A:F]	12V	A2[A:F]	GND
B3[A:F]	12V	A3[A:F]	GND
B4[A:F]	12V	A4[A:F]	GND



1. JPWR1
2. JPWR2

Fan Headers

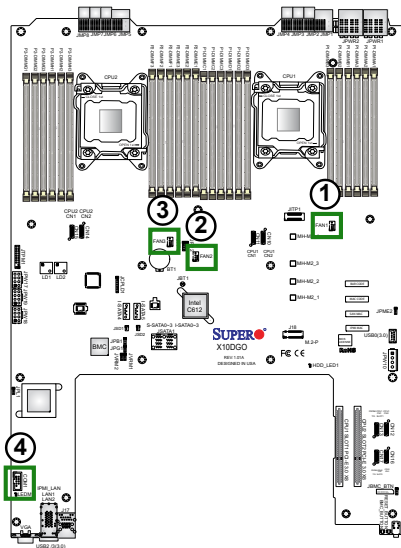
This motherboard has 3 system/CPU fan headers (Fan 1 - Fan 3) on the motherboard. All these 4-pin fans headers are backward compatible with the traditional 3-pin fans. However, fan speed control is available for 4-pin fans only. The fan speeds are controlled by Thermal Management via IPMI 2.0 interface. See the table on the right for pin definitions.

Fan Header Pin Definitions	
Pin#	Definition
1	Ground
2	+12V
3	Tachometer
4	PWR Modulation

Serial Port Header

A serial port (COM1) is located on the front panel on the motherboard. This connection provides serial connection support. See the table on the right for pin definitions.

Serial COM) Ports Pin Definitions			
Pin #	Definition	Pin #	Definition
1	DCD	6	DSR
2	RXD	7	RTS
3	TXD	8	CTS
4	DTR	9	RI
5	Ground	10	N/A

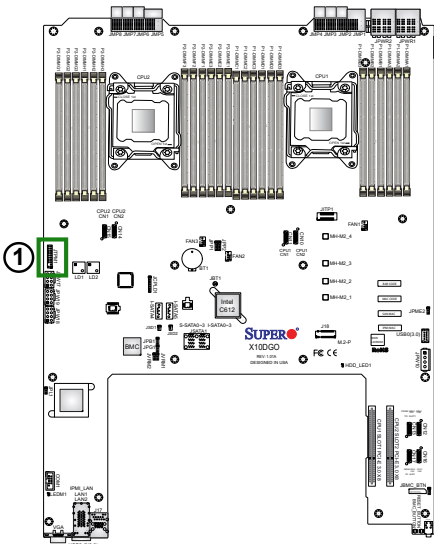


1. Fan 1
2. Fan 2
3. Fan 3
4. COM1

TPM/Port 80 Header

A Trusted Platform Module/Port 80 header, located at JTPM1, provides TPM support and Port 80 connection. Use this header to enhance system performance and data security. See the table on the right for pin definitions.

TPM/Port 80 Header Pin Definitions			
Pin #	Definition	Pin #	Definition
1	LCLK	2	GND
3	LFRAME#	4	<(KEY)>
5	LRESET#	6	+5V (X)
7	LAD 3	8	LAD 2
9	+3.3V	10	LAD1
11	LAD0	12	GND
13	SMB_CLK4	14	SMB_DAT4
15	+3V_DUAL	16	SERIRQ
17	GND	18	CLKRUN# (X)
19	LPCPD#	20	LDRQ# (X)



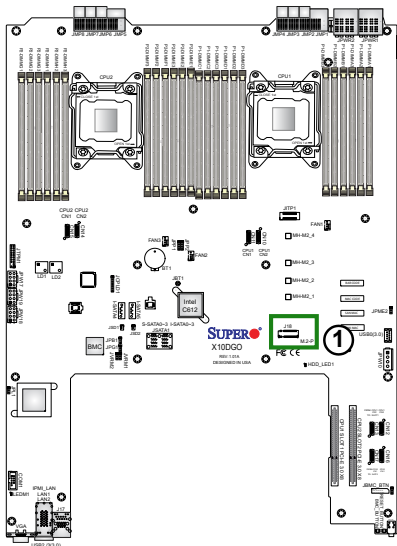
1. TPM/Port 80

M.2 Slot

M.2 is formerly known as Next Generation Form Factor (NGFF). The M.2 slot is designed for internal mounting devices. The X10DGO (-T) motherboard deploys an M key only dedicated for SSD devices with the ultimate performance capability for native PCI-E SSD support.

M.2 specifications supported by X10DGO:

M:2 socket type	:	Socket 3
M.2 PCIe bus width	:	PCIe Gen2 X4 from PCH
M.2 adapter Key	:	M Key or B+M Key
M.2 adapter type	:	PCI-e SSD only



1. M.2 Slot

Powered SATA DOM (SuperDOM) Connectors

Two powered SATADOM (Device-on-Module) connectors are located at JSD1/JSD2 on the motherboard. These connectors are used with Supermicro SuperDOMs, which are yellow SATA devices with power-pins built in, and can be used to provide backward-compatible power support to non-Supermicro SATADOMs that require external power supply without the need for separate power cables.

SuperDOM Connector Pin Definitions	
Pin#	Definition
1	+5V
2	Ground
3	Ground

JPW17~19

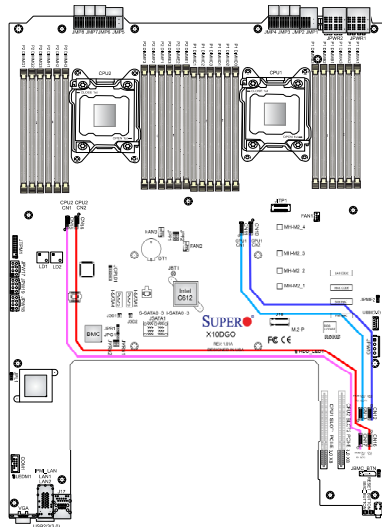
JPW17~19 are used to provide power of HDD midplane.

VGA Enable Jumper Settings			
pin #	Definition	pin #	Definition
1	GND	5	12V
2	GND	6	12V
3	GND	7	12V
4	GND	8	12V

CN10~17

These OCLINK connectors are used to connect PCIe bus from CPU to PCIe slot. CN10 contains CPU1 PCIe port 1A[3:0] CN11 contains CPU1 PCIe port 1A[7:4] CN14 contains CPU1 PCIe port 1A[3:0] CN15 contains CPU2 PCIe port 1A[7:4] CN12 contains Slot2 PCIe[3:0] CN13 contains Slot2 PCIe[7:4] CN16 contains Slot1 PCIe[3:0] CN17 contains Slot1 PCIe[7:4]


Both Slot1 and Slot2 are lane reversal, which means PCIe[7] is connected to LANE[0], first Lane, of Slot.

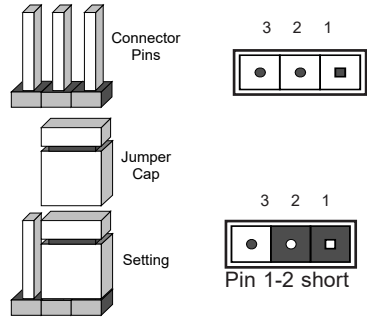


2-8 Jumper Settings

Explanation of Jumpers

To modify the operation of the motherboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. See the motherboard layout pages for jumper locations.

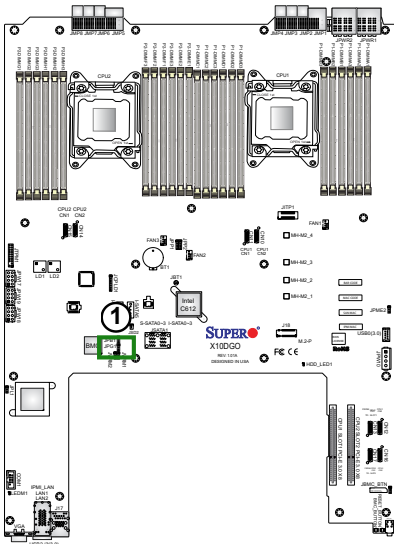
 **Note:** On two-pin jumpers, "Closed" means the jumper is on and "Open" means the jumper is off the pins.



VGA Enable

Jumper JPG1 allows the user to enable the onboard VGA connector. The default setting is on pins 1-2 to enable the connection. See the table on the right for jumper settings.


VGA Enable Jumper Settings	
Pin#	Definition
1-2	Enabled (Default)
2-3	Disabled




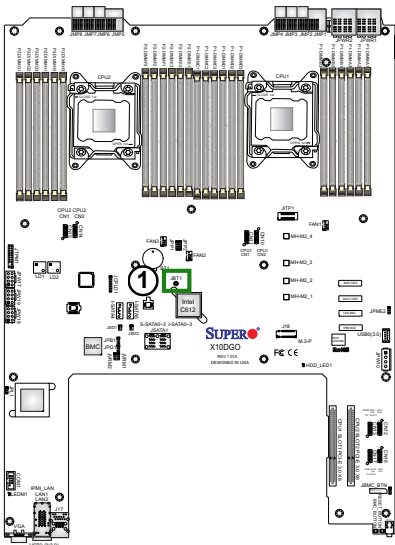
1. VGA Enable

CMOS Clear

JBT1 is used to clear CMOS. Instead of pins, this "jumper" consists of contact pads to prevent accidental clearing of CMOS. To clear CMOS, use a metal object such as a small screwdriver to touch both pads at the same time to short the connection.

 **Note 1.** For an ATX power supply, you must completely shut down the system, remove the AC power cord, and then short JBT1 to clear CMOS.

 **Note 2.** Clearing CMOS will also clear all passwords.



1. Clear CMOS

BMC Enable

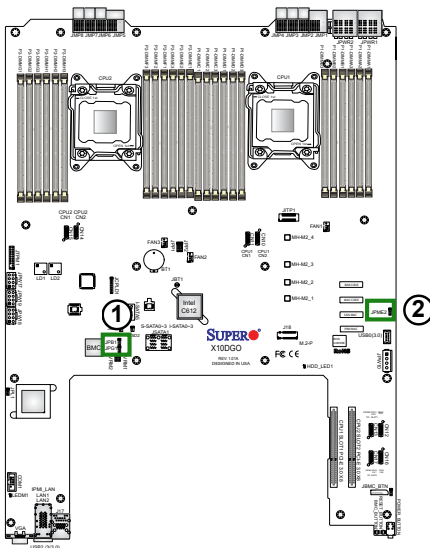
Jumper JPB1 is used to enable or disable the embedded AST2400 BMC (Baseboard Management Controller) that provides IPMI 2.0/KVM support on the motherboard. See the table on the right for jumper settings.

BMC Enable Jumper Settings	
Pin#	Definition
1-2	BMC Enable (Default)
2-3	Disabled

ME Manufacture Mode Select

Close pins 2 and 3 of Jumper JPME2 to bypass SPI flash security and force the system to operate in the Manufacturer mode, which will allow the user to flash the system firmware from a host server for system setting modifications. See the table on the right for jumper settings.

ME Mode Select Jumper Settings	
Pin#	Definition
1-2	Normal (Default)
2-3	Manufacture Mode



1. BMC Enable
2. JPME2

I²C Bus for VRM

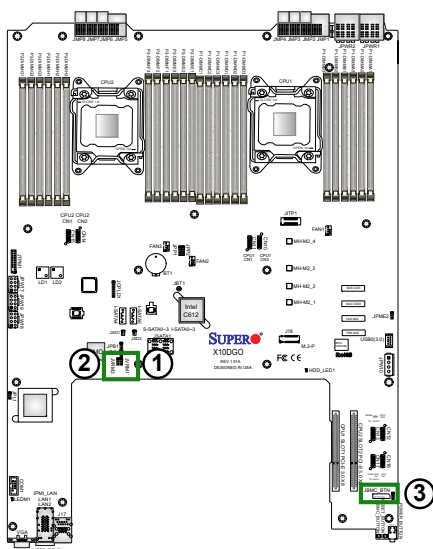
Jumpers JVRM1 and JVRM2 allow the BMC or the PCH to access CPU and memory VRM controllers. See the table on the right for jumper settings.

VRM Pin Definitions	
Pin #	Definition
1-2	BMC (Default)
2-3	PCH

JBMC_BTN

Jumper JBMC_BTN controls the function of the BMC Button. Connecting pins 1-2 will enable the UID LED/. Connecting pins 2-3 will allow the BMC Button to function as the BMC chip reset.

JBMC_BTN Pin Definition	
Pin #	Definition
1-2	press BMC_BTN will toggle UID LED
2-3	press BMC_BTN will reset BMC chip.

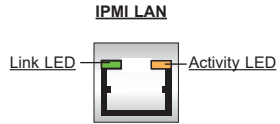


1. JVRM1
2. JVRM2
3. JBMC_BTN

2-9 Onboard LED Indicators

Dedicated IPMI LAN LEDs

A dedicated IPMI LAN is located on the I/O front panel of the motherboard. The amber LED on the right indicates activity, while the green LED on the left indicates the speed of the connection. See the table on the right for more information.

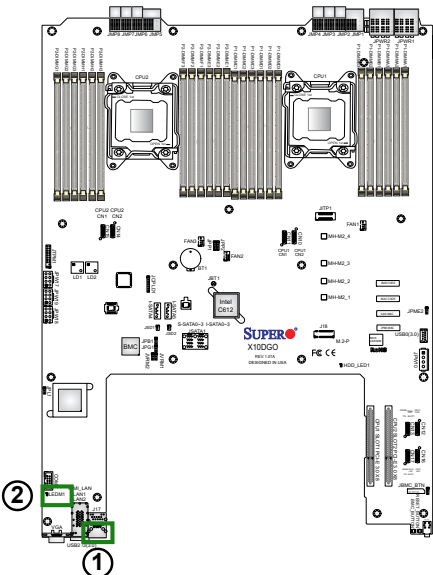


IPMI LAN Link LED (Left) & Activity LED (Right)		
	Color/State	Definition
Link (Left)	Green: Solid	100 Mbps
	Amber: Solid	1 Gbps
Activity (Right)	Orange: Blinking	Active

BMC Heartbeat LED

A BMC Heartbeat LED is located at LEDM1 on the motherboard. When LED is blinking green, BMC functions normally. See the table on the right for more information.

BMC Heartbeat LED States	
Color/State	Definition
Green: Blinking	BMC: Normal



1. IPMI LAN LED
2. BMC Heartbeat LED

2-10 SATA Connections

SATA 3.0 Ports

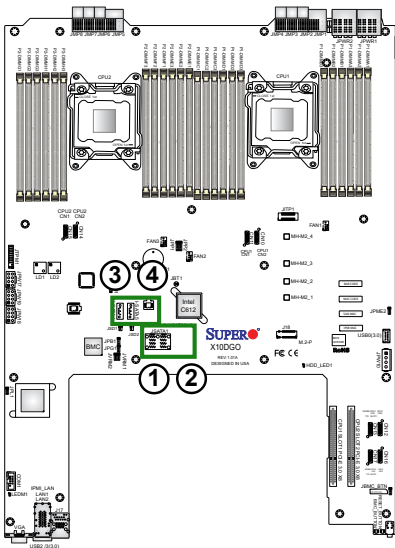
There are ten SATA 3.0 ports (I-SATA 0~3 & S-SATA0~3) on the motherboard. I-SATA ports are supported by the Intel PCH C612, and S-SATA ports are supported by the Intel SCU chip. I-SATA 4/5 support SuperDOMs, which are SATA DOM (Device-on-Module) devices with power supply supported. These SATA ports provide serial-link signal connections, which are faster than the connections of Parallel ATA. See the table on the right for pin definitions.



Note: For more information on SATA HostRAID configuration, please refer to the Intel SATA HostRAID User's Guide posted on our website at <http://www.supermicro.com>.

SATA Connectors Pin Definitions	
Pin#	Signal
1	Ground
2	SATA_TXP
3	SATA_TXN
4	Ground
5	SATA_RXN
6	SATA_RXP
7	Ground

Mini SAS HD			
pin #	Definition	pin #	Definition
A1	Pull_down	C1	SDATAXOUT0
A2	SATA_X-SCLK	C2	GND
A3	GND	C3	GND
A4	RX1P	C4	TX1P
A5	RX1N	C5	TX1N
A6	GND	C6	GND
A7	RX3P	C7	TX3P
A8	RX3N	C8	TX3N
A9	GND	C9	GND
B1	GND	D1	NC
B2	SB1	D2	Pull_down
B3	GND	D3	GND
B4	RX0P	D4	TX0P
B5	RX0N	D5	TX0N
B6	GND	D6	GND
B7	RX2P	D7	TX2P
B8	RX2N	D8	TX2N
B9	GND	D9	GND
		CG1	NC
		CG2	NC
		CG3	NC
		CG4	NC
		CG5	NC



1. S-SATA0~3
2. I-SATA0~3
3. I-SATA4
4. I-SATA5

Chapter 3

Troubleshooting

3-1 Troubleshooting Procedures

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the "Technical Support Procedures" and/or "Returning Merchandise for Service" section(s) in this chapter.



Note: Always disconnect the power cord before adding, changing or installing any hardware components.

Before Power On

1. Make sure that there are no short circuits between the motherboard and chassis.
2. Disconnect all ribbon/wire cables from the motherboard, including those for the keyboard and mouse.
3. Remove all add-on cards.
4. Install CPU 1 first (making sure it is fully seated) and connect the front panel connectors to the motherboard.

No Power

1. Make sure that no short circuits between the motherboard and the chassis.
2. Make sure that the ATX power connectors are properly connected.
3. Check that the 115V/230V switch on the power supply is properly set, if available.
4. Turn the power switch on and off to test the system, if applicable.
5. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

No Video

1. If the power is on, but you have no video, remove all the add-on cards and cables.
2. Use the speaker to determine if any beep codes exist. Refer to Appendix A for details on beep codes.

System Boot Failure

If the system does not display POST or does not respond after the power is turned on, check the following:

1. Check for any error beep from the motherboard speaker.
 - If there is no error beep, try to turn on the system without DIMM modules installed. If there is still no error beep, try to turn on the system again with only one processor installed in CPU Socket#1. If there is still no error beep, replace the motherboard.
 - If there are error beeps, clear the CMOS settings by unplugging the power cord and contracting both pads on the CMOS Clear Jumper (JB1). (Refer to Section 2-8 in Chapter 2.)
2. Remove all components from the motherboard, especially the DIMM modules. Make sure that system power is on, and memory error beeps are activated.
3. Turn on the system with only one DIMM module installed. If the system boots, check for bad DIMM modules or slots by following the Memory Errors Troubleshooting procedure in this Chapter.

Losing the System's Setup Configuration

1. Make sure that you are using a high quality power supply. A poor quality power supply may cause the system to lose the CMOS setup information. Refer to Section 1-6 for details on recommended power supplies.
2. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.
3. If the above steps do not fix the Setup Configuration problem, contact your vendor for repairs.

Memory Errors

When a No-Memory Beep Code is issued by the system, check the following:

1. Make sure that the memory modules are compatible with the system and that the DIMM modules are properly and fully installed. (For memory compatibility, refer to the Memory Compatibility Chart posted on our website @ <http://www.supermicro.com>.)
2. Check if different speeds of DIMMs have been installed. It is strongly recommended that you use the same RAM type and speed for all DIMMs in the system.
3. Make sure that you are using the correct type of Registered (RDIMM) DDR4 ECC modules recommended by the manufacturer.
4. Check for bad DIMM modules or slots by swapping a single module among all memory slots and check the results.
5. Make sure that all memory modules are fully seated in their slots. Follow the instructions given in Section 2-5 in Chapter 2.
6. Please follow the instructions given in the DIMM Population Tables listed in Section 2-5 to install your memory modules.

When the System Becomes Unstable

A. When the system becomes unstable during or after OS installation, check the following:

1. CPU/BIOS support: Make sure that your CPU is supported, and you have the latest BIOS installed in your system.
2. Memory support: Make sure that the memory modules are supported by testing the modules using memtest86 or a similar utility.



Note: Refer to the product page on our website <http://www.supermicro.com> for memory and CPU support and updates.

3. HDD support: Make sure that all hard disk drives (HDDs) work properly. Replace the bad HDDs with good ones.
4. System cooling: Check system cooling to make sure that all heatsink fans, and CPU/system fans, etc., work properly. Check Hardware Monitoring settings in the IPMI to make sure that the CPU and System temperatures are

within the normal range. Also check the front panel Overheat LED, and make sure that the Overheat LED is not on.

5. Adequate power supply: Make sure that the power supply provides adequate power to the system. Make sure that all power connectors are connected. Please refer to our website for more information on minimum power requirement.
6. Proper software support: Make sure that the correct drivers are used.

B. When the system becomes unstable before or during OS installation, check the following:

1. Source of installation: Make sure that the devices used for installation are working properly, including boot devices such as CD/DVD disc, CD/DVD-ROM.
2. Cable connection: Check to make sure that all cables are connected and working properly.
3. Using minimum configuration for troubleshooting: Remove all unnecessary components (starting with add-on cards first), and use minimum configuration (with a CPU and a memory module installed) to identify the trouble areas. Refer to the steps listed in Section A above for proper troubleshooting procedures.
4. Identifying bad components by isolating them: If necessary, remove a component in question from the chassis, and test it in isolation to make sure that it works properly. Replace a bad component with a good one.
5. Check and change one component at a time instead of changing several items at the same time. This will help isolate and identify the problem.
6. To find out if a component is good, swap this component with a new one to see if the system will work properly. If so, then the old component is bad. You can also install the component in question in another system. If the new system works, the component is good and the old system has problems.

3-2 Technical Support Procedures

Before contacting Technical Support, please take the following steps. Also, please note that as a motherboard manufacturer, Supermicro also sells motherboards through its channels, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problem(s) with the specific system configuration that was sold to you.

1. Please go through the 'Troubleshooting Procedures' and 'Frequently Asked Question' (FAQ) sections in this chapter or see the FAQs on our website (<http://www.supermicro.com/>) before contacting Technical Support.
 2. BIOS upgrades can be downloaded from our website (<http://www.supermicro.com>).
 3. If you still cannot resolve the problem, include the following information when contacting Supermicro for technical support:
 - Motherboard model and PCB revision number
 - BIOS release date/version (This can be seen on the initial display when your system first boots up.)
 - System configuration
 4. An example of a Technical Support form is on our website at (<http://www.supermicro.com>).
- Distributors: For immediate assistance, please have your account number ready when placing a call to our technical support department. We can be reached by e-mail at support@supermicro.com.

3-3 Battery Removal and Installation

Battery Removal

To remove the onboard battery, follow the steps below:

1. Power off your system and unplug your power cable.
2. Locate the onboard battery as shown below.
3. Using a tool such as a pen or a small screwdriver, push the battery lock outwards to unlock it. Once unlocked, the battery will pop out from the holder.
4. Remove the battery.

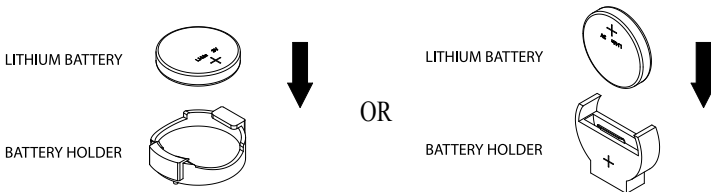
Proper Battery Disposal

Warning: Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

Battery Installation

To install an onboard battery, follow the steps below:

1. Power off your system and unplug your power cable.
2. Locate the onboard battery as shown below.
3. Identify the battery's polarity. The positive (+) side should be facing up.
4. Insert the battery into the battery holder and push it down until you hear a click to ensure that the battery is securely locked.



Note: When replacing a battery, be sure to only replace it with the same type.

3-4 Frequently Asked Questions

Question: What are the various types of memory that my motherboard can support?

Answer: The motherboard supports up to 1536GB of LRDIMM (Load Reduced) and RDIMM (Registered) ECC/Non-ECC DDR4 (288-pin) memory at up to 2400MT/s in 24 DIMM slots. To enhance memory performance, do not mix memory modules of different speeds and sizes. Please follow all memory installation instructions given on Section 2-5 in Chapter 2.

Question: How do I update my BIOS under DOS?

It is recommended that you **do not** upgrade your BIOS if you are not experiencing any problems with your system. Updated BIOS files are located on our website at <http://www.supermicro.com>. Please check our BIOS warning message and the information on how to update your BIOS on our website. Select your motherboard model and download the BIOS file to your computer. Also, check the current BIOS revision to make sure that it is newer than your BIOS before downloading. You can choose from the zip file and the .exe file. If you choose the zip BIOS file, please unzip the BIOS file onto a bootable USB device. Run the batch file using the format FLASH.bat filename.rom from your bootable USB device to flash the BIOS. Then, your system will automatically reboot.

Warning: Do not shut down or reset the system while updating the BIOS to prevent possible system boot failure!



Note: The SPI BIOS chip used on this motherboard cannot be removed. Send your motherboard back to our RMA Department at Supermicro for repair. For BIOS Recovery instructions, please refer to the AMI BIOS Recovery Instructions posted at <http://www.supermicro.com>.

Question: How do I update my BIOS under UEFI Shell?



Note: We do not recommend that you update your BIOS if you are not experiencing a BIOS-related problem. If you need to update your BIOS, please follow the steps below to properly update your BIOS under UEFI Shell.

1. Download and save the BIOS update package to your computer.
2. Extract the files from the UEFI folder of the BIOS package to a USB stick.



Note: The USB stick doesn't have to be bootable; however, it has to be formatted with the FAT/FAT32 file system.

3. Insert the USB stick into a USB port, boot to the UEFI Build-In Shell, and type the following to start the BIOS update.

```
Shell> fs0:
```

```
fs0:\> cd UEFI
```

```
fs0:\UEFI> flash.nsh BIOSname#.###
```

4. The FLASH.NSH script will compare the Flash Descriptor Table (FDT) code in the new BIOS with the existing one in the motherboard:

a. If a different FDT is found

- A new file, STARTUP.NSH, will be created, and the system will automatically reboot in 10 seconds without you pressing any key. BIOS will be updated after the system reboots.
- You can also press <Y> to force an immediate system reboot to shorten the process. During system reboot, press the <F11> key to invoke the boot menu and boot into the build-in UEFI Shell. Your BIOS will be updated automatically.

b. If the FDT is the same

- BIOS update will be immediately performed without a system reboot initiated.

Warning: Do not shut down or reset the system while updating the BIOS to prevent possible boot failure!

5. Perform an A/C power cycle after the message indicating the BIOS update has completed.
6. Go to the BIOS setup utility, and restore the BIOS settings.

Question: How do I handle the used battery?

Answer: Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

3-5 Returning Merchandise for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning the motherboard to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton, and the shipping package is mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete. For faster service, You can also request a RMA authorization online (<http://www.supermicro.com/RmaForm/>).

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alternation, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.

Chapter 4


BIOS

4-1 Introduction


This chapter describes the AMI BIOS Setup utility for the X10DGO(-T)(-SXM). It also provides the instructions on how to navigate the AMI BIOS Setup utility screens. The AMI ROM BIOS is stored in a Flash EEPROM and can be easily updated.

Starting BIOS Setup Utility


To enter the AMI BIOS Setup utility screens, press the key while the system is booting up.


 **Note:** In most cases, the key is used to invoke the AMI BIOS setup screen. There are a few cases when other keys are used, such as <F3>, <F4>, etc.

Each main BIOS menu option is described in this manual. The Main BIOS setup menu screen has two main frames. The left frame displays all the options that can be configured. Grayed-out options cannot be configured. Options in blue can be configured by the user. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it.

 **Note:** The AMI BIOS has default text messages built in. The manufacturer retains the option to include, omit, or change any of these text messages.

The AMI BIOS Setup utility uses a key-based navigation system called "hot keys." Most of the AMI BIOS setup utility "hot keys" can be used at any time during setup navigation. These keys include <F3>, <F4>, <Enter>, <ESC>, arrow keys, etc.

 **Note 1:** Options printed in **Bold** are default settings.

 **Note 2:** <F3> is used to load optimal default settings. <F4> is used to save the settings and exit the setup utility.

How To Change the Configuration Data

The configuration data that determines the system parameters may be changed by entering the AMI BIOS Setup utility. This Setup utility can be accessed by pressing <F2> at the appropriate time during system boot.



Note: For AMI UEFI BIOS Recovery, please refer to the UEFI BIOS Recovery User Guide posted @ <http://www.supermicro.com/support/manuals/>.

Starting the Setup Utility

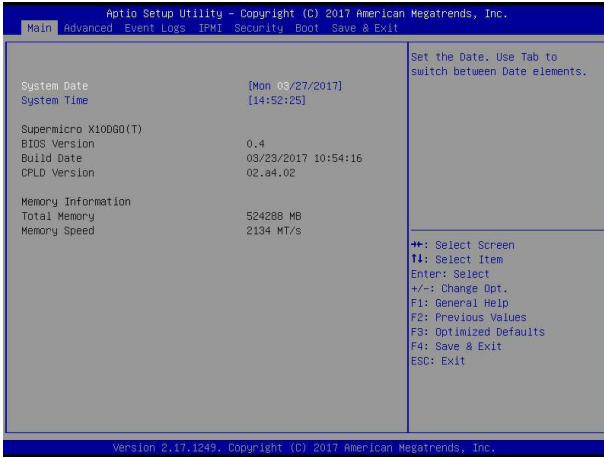
Normally, the only visible Power-On Self-Test (POST) routine is the memory test. As the memory is being tested, press the <F2> key to enter the main menu of the AMI BIOS Setup utility. From the main menu, you can access the other setup screens. An AMI BIOS identification string is displayed at the left bottom corner of the screen below the copyright message.

Warning: Do not upgrade the BIOS unless your system has a BIOS-related issue. Flashing the wrong BIOS can cause irreparable damage to the system. In no event shall the manufacturer be liable for direct, indirect, special, incidental, or consequential damage arising from a BIOS update. If you have to update the BIOS, do not shut down or reset the system while the BIOS is being updated to avoid possible boot failure.

4-2 Main Setup

When you first enter the AMI BIOS Setup utility, you will enter the Main setup screen. You can always return to the Main setup screen by selecting the Main tab on the top of the screen. The Main BIOS Setup screen is shown below.

The AMI BIOS main menu displays the following information:



System Date

This item displays the system date in Day MM/DD/YY format (e.g. Wed 10/12/2011).

System Time

This item displays the system time in HH:MM:SS format (e.g. 15:32:52).

Supermicro X10DGO(-T)(-SXM)

BIOS Version

This item displays the version of the BIOS ROM used in this system.

Build Date

This item displays the date that the BIOS Setup utility was built.

Memory Information

Total Memory

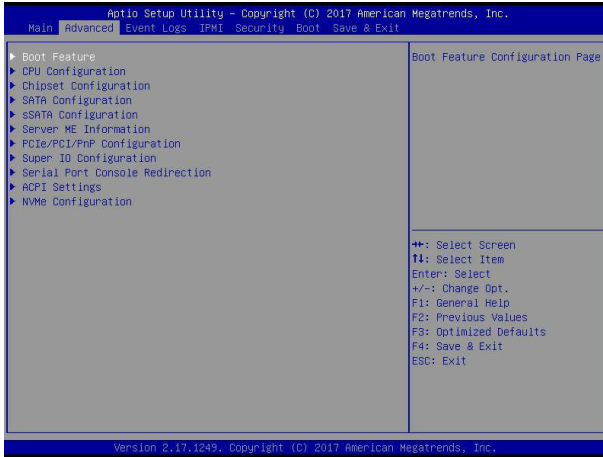
This displays the amount of memory that is available in the system.

Memory Speed

This displays the detected system memory speed.

4-3 Advanced Setup Configurations

Use the arrow keys to select Advanced Setup and press <Enter> to access the following submenu items.



► Boot Features

Quiet Boot

This feature selects the bootup screen display between POST messages and the OEM logo. Select Disabled to display the POST messages. Select Enabled to display the OEM logo instead of the normal POST messages. The options are Enabled and **Disabled**.

AddOn ROM Display Mode

This item sets the display mode for the Option ROM. Select Keep Current to use the current AddOn ROM Display setting. Select Force BIOS to use the Option ROM display mode set by the system BIOS. The options are **Force BIOS** and Keep Current.

Bootup Num-Lock State

This feature sets the Power-on state for the Numlock key. The options are Off and **On**.

Wait For 'F1' If Error

Select Enabled to force the system to wait until the 'F1' key is pressed if an error occurs. The options are Disabled and **Enabled**.

Interrupt 19 Capture

Interrupt 19 is the software interrupt that handles the boot disk function. When this item is set to Enabled, the ROM BIOS of the host adaptors will "capture" Interrupt 19 at bootup and allow the drives that are attached to these host adaptors to function as bootable disks. If this item is set to Disabled, the ROM BIOS of the host adaptors will not capture Interrupt 19, and the drives attached to these adaptors will not function as bootable devices. The options are **Immediate** and Postponed.

Retry Boot

Select Enabled to force the system to reboot when system fails to boot. The options are **Disabled** and Enabled.

Power Configuration**Watch Dog Function**

If enabled, the Watch Dog timer will allow the system to reboot when it is inactive for more than 5 minutes. The options are Enabled and **Disabled**.

Power Button Function

If this feature is set to Instant_Off, the system will power off immediately as soon as the user presses the power button. If this feature is set to 4_Second_Override, the system will power off when the user presses the power button for 4 seconds or longer. The options are **Instant_Off** and 4_Second_Override.

Restore on AC Power Loss

This feature sets the power state after a power outage. Select Power-Off for the system power to remain off after a power loss. Select Power-On for the system power to be turned on after a power loss. Select Last State to allow the system to resume its last state before a power loss. The options are Power-On, Stay-Off and **Last State**.

► CPU Configuration

This submenu displays the information of the CPU as detected by the BIOS. It also allows the user to configuration CPU settings.

Socket 0 CPU Information/Socket 1 CPU Information

This submenu displays the following information regarding the CPU installed in Socket 1 and (or) Socket 2 as detected by the BIOS.

- Processor Socket
- Processor ID
- Processor Frequency
- Processor Maximum Ratio
- Processor Minimum Ratio
- Microcode Revision
- L1 Cache RAM
- L2 Cache RAM
- L3 Cache RAM
- CPU0 Version
- CPU1 Version

Clock Spread Spectrum

Select Enable to enable Clock Spectrum support, which will allow the BIOS to monitor and attempt to reduce the level of Electromagnetic Interference caused by the components whenever needed. The options are **Disabled** and Enabled.

Hyper-Threading (ALL)

Select Enabled to support Intel Hyper-threading Technology to enhance CPU performance. The options are **Enabled** and Disabled.

Cores Enabled

Set a numeric value to enable the number of cores. (Please refer to Intel's website for more information.) Enter **0** to enable all cores.

Execute Disable Bit (Available if supported by the OS & the CPU)

Select Enabled to enable the Execute-Disable Bit which will allow the processor to designate areas in the system memory where an application code can execute and where it cannot, thus preventing a worm or a virus from flooding illegal codes to overwhelm the processor or damage the system during an attack. The default is **Enabled**. (Refer to Intel and Microsoft Web sites for more information.)

PPIN Control

Select Enable to unlock the PPIN control. The options are **Enabled** and Disabled.

Hardware Prefetcher (Available when supported by the CPU)

If set to Enabled, the hardware prefetcher will prefetch streams of data and instructions from the main memory to the L2 cache to improve CPU performance. The options are Disabled and **Enabled**.

Adjacent Cache Line Prefetch (Available when supported by the CPU)

The CPU prefetches the cache line for 64 bytes if this feature is set to Disabled. The CPU prefetches both cache lines for 128 bytes as comprised if this feature is set to **Enabled**.

DCU Streamer Prefetcher (Available when supported by the CPU)

Select Enabled to enable the DCU (Data Cache Unit) Streamer Prefetcher which will stream and prefetch data and send it to the Level 1 data cache to improve data processing and system performance. The options are Disabled and **Enabled**.

DCU IP Prefetcher (Available when supported by the CPU)

Select Enabled for DCU (Data Cache Unit) IP Prefetcher support, which will prefetch IP addresses to improve network connectivity and system performance. The options are Disabled and **Enabled**.

Direct Cache Access (DCA Support)

Select Enabled to use Intel's DCA (Direct Cache Access) Technology to improve data transfer efficiency. The options are **Enabled** and Disabled.

X2APIC

Select Enable to activate APIC (Advanced Programmable Interrupt Controller) support. The options are **Enabled** and Disabled.

AES-NI

Select Enable to use the Intel Advanced Encryption Standard (AES) New Instructions (NI) to ensure data security. The options are **Enabled** and Disabled.

Intel® Virtualization Technology (Available when supported by the CPU)

Select Enabled to support Intel® Virtualization Technology, which will allow one platform to run multiple operating systems and applications in independent partitions, creating multiple "virtual" systems in one physical computer. The options are **Enabled** and Disabled.



Note: If a change is made to this setting, you will need to reboot the system for the change to take effect. Refer to Intel's website for detailed information.

► Advanced Power Management Configuration

This section is used to configure the following CPU Power Management settings.

Power Technology

Select Energy Efficiency to support power-saving mode. Select Custom to customize system power settings. Select Disabled to disable power-saving settings.

The options are Disable, **Energy Efficiency**, and Custom.

If the above is set to 'Custom' the following options are displayed:

► CPU P State Control

EIST (P-States)

EIST (Enhanced Intel SpeedStep Technology) allows the system to automatically adjust processor voltage and core frequency to reduce power consumption and heat dissipation. The options are Disabled, and **Enabled**.

Turbo Mode (Available when Intel® EIST Technology is enabled)

Select Enabled to use the Turbo Mode to boost system performance. The options are **Enabled** and Disabled.

P-State Coordination

This feature allows the user to change the P-State (Power-Performance State) coordination type. P-State is also known as "SpeedStep" for Intel processors. Select HW_ALL to change the P-State coordination type for hardware components only. Select SW_ALL to change the P-State coordination type for all software installed in the system. Select SW_ANY to change the P-State coordination type for a software program in the system. The options are **HW_All**, SW_ALL, and SW_ANY.

► CPU HWPM State Control

Enable CPU HWPM

This setting allows the user to select between OS and hardware-controlled P-states. Selecting Native Mode allows the OS to choose a P-state. Selecting Out of Band Mode allows the hardware to autonomously choose a P-state without OS guidance. Selecting Native Mode with No Legacy Support functions as Native Mode with no support for older hardware.

Enable CPU Autonomous Cstate

Enabling this setting allows the hardware to autonomously choose to enter a C-state based on power consumption and clock speed.

► CPU C State Control

Package C State limit

This feature allows the user to set the limit on the C-State package register. The options are C0/C1 State, C2 State, C6 (Non Retention) State, and **C6 (Retention) State**.

CPU C3 Report

Select Enabled to allow the BIOS to report the CPU C3 State (ACPI C2) to the operating system. During the CPU C3 State, the CPU clock generator is turned off. The options are Enabled and **Disabled**.

CPU C6 Report

Select Enabled to allow the BIOS to report the CPU C6 State (ACPI C3) to the operating system. During the CPU C6 State, the power to all cache is turned off. The options are **Enabled** and Disabled.

Enhanced Halt State (C1E)

Select Enabled to use Enhanced Halt-State technology, which will significantly reduce the CPU's power consumption by reducing the CPU's clock cycle and voltage during a Halt-state. The options are Disable and **Enable**.

► CPU T State Control

ACPI (Advanced Configuration Power Interface) T-States

Select Enable to support CPU throttling by the operating system to reduce power consumption. The options are **Enable** and Disable.

► Chipset Configuration

► North Bridge

This feature allows the user to configure the following North Bridge settings.

► IIO Configuration

EV DFX (Device Function On-Hide) Feature

When this feature is set to Enable, the EV_DFX Lock Bits that are located on a processor will always remain clear during electric tuning. The options are **Disable** and **Enable**.

► IIO1 Configuration / IIO2 Configuration

IIO1 PORT 1A Link Speed

This item configures the link speed of a PCI-E port specified by the user. The options are Gen 1 (Generation 1) (2.5 GT/s), Gen 2 (Generation 2) (5 GT/s) and **Gen 3 (Generation 3) (8 GT/s)**.

IIO1 PORT 2A Link Speed

Use this item to configure the link speed of a PCI-E port specified by the user. The options are Gen 1 (Generation 1) (2.5 GT/s), Gen 2 (Generation 2) (5 GT/s) and **Gen 3 (Generation 3) (8 GT/s)**.

IIO1 PORT 3A Link Speed

Use this item to configure the link speed of a PCI-E port specified by the user. The options are Gen 1 (Generation 1) (2.5 GT/s), Gen 2 (Generation 2) (5 GT/s) and **Gen 3 (Generation 3) (8 GT/s)**.

► IIO2 Configuration

IIO2 Port 1A Link Speed

Select GEN1 to enable PCI-Exp Generation 1 support for Port 1A. Select GEN2 to enable PCI-Exp Generation 2 support for Port 1A. Select GEN3 to enable PCI-Exp Generation 3 support for Port 1A. The options are GEN1, GEN2, and **GEN3**.

II02 Port 2A Link Speed

Select GEN1 to enable PCI-Exp Generation 1 support for Port 2A. Select GEN2 to enable PCI-Exp Generation 2 support for Port 2A. Select GEN3 to enable PCI-Exp Generation 3 support for Port 2A. The options are GEN1, GEN2, and **GEN3**.

II02 Port 3A Link Speed

Select GEN1 to enable PCI-Exp Generation 1 support for Port 3A. Select GEN2 to enable PCI-Exp Generation 2 support for Port 3A. Select GEN3 to enable PCI-Exp Generation 3 support for Port 3A. The options are GEN1, GEN2, and **GEN3**.

►IOAT (Intel® IO Acceleration) Configuration

Enable IOAT

Select Enable to enable Intel I/OAT (I/O Acceleration Technology) support, which significantly reduces CPU overhead by leveraging CPU architectural improvements and freeing the system resource for other tasks. The options are **Enable** and Disable.

No Snoop

Select Enable to support no-snoop mode for each CB device. The options are **Disable** and Enable.

Relaxed Ordering

Select Enable to enable Relaxed Ordering support which will allow certain transactions to violate the strict-ordering rules of PCI bus for a transaction to be completed prior to other transactions that have already been enqueued. The options are **Disable** and Enable.

►Intel® VT for Directed I/O (VT-d)

Intel® VT for Directed I/O (VT-d)

Select Enable to use Intel® Virtualization Technology support for Direct I/O VT-d support by reporting the I/O device assignments to the VMM (Virtual Machine Monitor) through the DMAR ACPI Tables. This feature offers fully-protected I/O resource sharing across Intel platforms, providing greater reliability, security and availability in networking and data-sharing. The options are **Enable** and Disable.

Interrupt Remapping

Select Enable for Interrupt Remapping support to enhance system performance. The options are **Enable** and Disable.

►QPI (Quick Path Interconnect) Configuration

QPI Status

The following information will display:

- Number of CPU
- Number of IIO
- Current QPI Link Speed
- Current QPI Link Frequency
- QPI Global MMIO Low Base/Limit
- QPI Global MMIO High Base/Limit
- QPI PCIe Configuration Base/Size

Link Frequency Select

Use this item to select the CPU Frequency. The options are 6.4 GB/s, 8.0 GB/s, 9.6 GB/s, **Auto**, Auto Limited.

Link L0p Enable

Select Enable for Link L0p support. The options are **Enable** and Disable.

Link L1 Enable

Select Enable for Link L1 support. The options are **Enable** and Disable.

COD Enable (Available when the OS and the CPU support this feature)

Select Enabled for Cluster-On-Die support to enhance system performance in cloud computing. The options are Enable, Disable, and **Auto**.

Early Snoop (Available when the OS and the CPU support this feature)

Select Enabled for Early Snoop support to enhance system performance. The options are Enable, Disable, and **Auto**.

Home Dir Snoop with IVT- Style OSB

Enable/Disable Home Directory Snoop mode to configure Opportunistic Snoop Broadcast (OSB) for advanced operations. Enable this setting for workloads with high cache transfers. The effect depends on applications in use.

Isoc Mode

Select Enabled for Isochronous support to meet QoS (Quality of Service) requirements. This feature is especially important for Virtualization Technology. The options are Enable and **Disable**.

► Memory Configuration

Enforce POR

Select Enable to enforce POR restrictions on DDR4 frequency and voltage programming. The options are **Enabled** and Disabled.

Memory Frequency

Use this feature to set the maximum memory frequency for onboard memory modules. The options are **Auto**, 1333, 1400, 1600, 1800, 1867, 2000, 2133, 2200, 2400, 2600, 2667, and Reserved (Do not select Reserved).

Data Scrambling

Select Enabled to enable data scrambling to enhance system performance and data integrity. The options are **Auto**, Disabled and Enabled.

Enable ADR

Select Enable for ADR (Automatic Diagnostic Repository) support to enhance memory performance. The options are Disable and **Enable**.

Erase-Arm NVDIMMs

This function arms the NVDIMMs to save operations in the event of a power loss.

Restore NVDIMMs

This feature allows NVDIMM data to be restored upon power-up.

Interleave NVDIMMs

This setting allows the user to decide if NVDIMMs are interleaved together or not.

ACPI Shutdown trigger ADR

Enabling this setting will allow the BIOS to send an ADR (Automatic Diagnostic Repository) trigger command to the BMC during a graceful shutdown. If the BMC is out of order, the system will freeze and will need to be powered down manually.

DRAM RAPL Baseline

Use this feature to set the run-time power-limit baseline for DRAM modules. The options are Disable, DRAM RAPL Mode 0, and **DRAM RAPL Mode 1**.

Set Throttling Mode

Throttling improves reliability and reduces power consumption in the processor via automatic voltage control during processor idle states. The options are Disabled and **CLTT** (Closed Loop Thermal Throttling).

Socket Interleave Below 4GB

Select Enabled for the memory above the 4G Address space to be split between two sockets. The options are Enable and **Disable**.

A7 Mode

Select Enabled to support the A7 (Addressing) mode to improve memory performance. The options are **Enable** and Disable.

►DIMM Information

This item displays the status of a DIMM module specified by the user.

- P1-DIMMA1 - P1-DIMMD1
- P2-DIMME1 - P2-DIMMH1

►Memory RAS (Reliability_Availability_Serviceability) Configuration

Use this submenu to configure the following Memory RAS settings.

RAS Mode

When Disable is selected, RAS is not supported. When Mirror is selected, the motherboard maintains two identical copies of all data in memory for data backup. When Lockstep is selected, the motherboard uses two areas of memory to run the same set of operations in parallel to boost performance. The options are **Disable**, Mirror, and Lockstep Mode.

Memory Rank Sparing

Select Enable to enable memory-sparing support for memory ranks to improve memory performance. The options are **Disabled** and Enabled.

Patrol Scrub

Patrol Scrubbing is a process that allows the CPU to correct correctable memory errors detected on a memory module and send the correction to the requestor (the original source). When this item is set to Enabled, the IO hub will read and write back one cache line every 16K cycles, if there is no delay caused by internal processing. By using this method, roughly 64 GB of memory behind the IO hub will be scrubbed every day. The options are **Enable** and Disable.

Patrol Scrub Interval

This feature allows you to decide how many hours the system should wait before the next complete patrol scrub is performed. Use the keyboard to enter a value from 0-24. The Default setting is **24**.

Demand Scrub

Demand Scrubbing is a process that allows the CPU to correct correctable memory errors found on a memory module. When the CPU or I/O issues a demand-read command, and the read data from memory turns out to be a correctable error, the error is corrected and sent to the requestor (the original source). Memory is updated as well. Select Enable to use Demand Scrubbing for ECC memory correction. The options are **Enable** and Disable.

Device Tagging

Select Enable to support device tagging. The options are **Disable** and Enable.

►South Bridge Configuration

The following South Bridge information will display:

- USB Configuration
- USB Module Version
- USB Devices

Legacy USB Support

Select Enabled to support onboard legacy USB devices. Select Auto to disable legacy support if there are no legacy USB devices present. Select Disabled to have all USB devices available for EFI applications only. The options are **Enabled**, Disabled and Auto.

XHCI Hand-Off

This is a work-around solution for operating systems that do not support XHCI (Extensible Host Controller Interface) hand-off. The XHCI ownership change should be claimed by the XHCI driver. The settings are **Enabled** and Disabled.

EHCI Hand-Off

This item is for operating systems that do not support Enhanced Host Controller Interface (EHCI) hand-off. When this item is enabled, EHCI ownership change will be claimed by the EHCI driver. The settings are Enabled and **Disabled**.

Port 60/64 Emulation

Select Enabled for I/O port 60h/64h emulation support, which in turn, will provide complete legacy USB keyboard support for the operating systems that do not support legacy USB devices. The options are Disabled and **Enabled**.

USB 3.0 Support

Select Enabled for USB 3.0 support. The options are Smart Auto, **Auto**, Enabled, Disabled and Manual.

►SATA Configuration

When this submenu is selected, AMI BIOS automatically detects the presence of the SATA devices that are supported by the Intel PCH chip and displays the following items:

SATA Controller

This item enables or disables the onboard SATA controller supported by the Intel PCH chip. The options are **Enabled** and Disabled.

Configure SATA as

Select IDE to configure a SATA drive specified by the user as an IDE drive. Select AHCI to configure a SATA drive specified by the user as an AHCI drive. Select RAID to configure a SATA drive specified by the user as a RAID drive. The options are IDE, **AHCI**, and RAID.

**If the item above "Configure SATA as" is set to AHCI, the following items will display:*

Support Aggressive Link Power Management

When this item is set to Enabled, the SATA AHCI controller manages the power usage of the SATA link. The controller will put the link to a low power state when the I/O is inactive for an extended period of time, and the power state will return to normal when the I/O becomes active. The options are **Enabled** and Disabled.

SATA Port 0~ Port 5

This item displays the information detected on the installed SATA drive on the particular SATA port.

- Model number of drive and capacity
- Software Preserve Support

Port 0~ Port 5

Select Enabled to enable a SATA port specified by the user. The options are Disabled and Enabled.

Port 0 ~ Port 5 Hot Plug

Select Enabled to enable hot-plugging support for a port specified by the user, which will allow the user to replace a SATA disk drive installed on this port without shutting down the system. The options are **Enabled** and Disabled.

Port 0 ~ Port 5 Spin Up Device

On an edge detect from 0 to 1, set this item to allow the PCH to initialize the device. The options are Enabled and **Disabled**.

Port 0 ~ Port 5 SATA Device Type

Use this item to specify if the SATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are Hard Disk Drive and Solid State Drive.

**If the item above "Configure SATA as" is set to IDE, the following items will display:*

Serial ATA Port 0~ Port 5

This item indicates that a SATA port specified by the user is installed (present) or not.

Port 0 ~ Port 5 SATA Device Type (Available when a SATA port is detected)

Use this item to specify if the SATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are Hard Disk Drive and Solid State Drive.

**If the item above "Configure SATA as" is set to RAID, the following items will display:*

Support Aggressive Link Power Management

When this item is set to Enabled, the SATA AHCI controller manages the power usage of the SATA link. The controller will put the link to a low power state when the I/O is inactive for an extended period of time, and the power state will return to normal when the I/O becomes active. The options are **Enabled** and Disabled.

SATA RAID Option ROM/UEFI Driver

Select EFI to load the EFI driver for system boot. Select Legacy to load a legacy driver for system boot. The options are Disabled, EFI, and **Legacy**.

Serial ATA Port 0~ Port 5

This item displays the information detected on the installed SATA drives on the particular SATA port.

- Model number of drive and capacity
- Software Preserve Support

Port 0~ Port 5

Select Enabled to enable a SATA port specified by the user. The options are Disabled and **Enabled**.

Port 0 ~ Port 5 Hot Plug

Select Enabled to enable hot-plugging support for a port specified by the user, which will allow the user to replace a SATA disk drive installed on this port without shutting down the system. The options are Enabled and **Disabled**.

Port 0 ~ Port 5 Spin Up Device

On an edge detect from 0 to 1, set this item to allow the PCH to start a COMRESET initialization to the device. The options are Enabled and **Disabled**.

Port 0 ~ Port 5 SATA Device Type

Use this item to specify if the SATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are Hard Disk Drive and Solid State Drive.

►sSATA Configuration

When this submenu is selected, AMI BIOS automatically detects the presence of the SATA devices that are supported by the PCH-sSATA controller and displays the following items:

sSATA Controller

This item enables or disables the onboard SATA controller supported by the Intel PCH-sSATA controller. The options are **Enabled** and Disabled.

Configure sSATA as

Select IDE to configure an sSATA drive specified by the user as an IDE drive. Select AHCI to configure an sSATA drive specified by the user as an AHCI drive. Select RAID to configure an sSATA drive specified by the user as a RAID drive. The options are IDE, **AHCI**, and RAID.

**If the item above "Configure sSATA as" is set to AHCI, the following items will display:*

Support Aggressive Link Power Management

When this item is set to Enabled, the SATA AHCI controller manages the power usage of the SATA link. The controller will put the link to a low power state when the I/O is inactive for an extended period of time, and the power state will return to normal when the I/O becomes active. The options are **Enabled** and Disabled.

sSATA Port 0~ Port 3

This item displays the information detected on the installed on the sSATA port. specified by the user.

- Model number of drive and capacity
- Software Preserve Support

sSATA Port 0~ Port 3

Select Enabled to enable an sSATA port specified by the user. The options are Disabled and Enabled.

sSATA Port 0 ~ Port 3 Hot Plug

Select Enabled to enable hot-plugging support for a port specified by the user, which will allow the user to replace a sSATA disk drive installed on this port without shutting down the system. The options are **Enabled** and Disabled.

sSATA Port 0 ~ Port 3 Spin Up Device

On an edge detect from 0 to 1, set this item to allow the PCH to start a COMRESET initialization to the device. The options are Enabled and **Disabled**.

Port 0 ~ Port 3 sSATA Device Type

Use this item to specify if the sSATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are Hard Disk Drive and Solid State Drive.

**If the item above "Configure sSATA as" is set to IDE, the following items will display:*

sSATA Port 0~ Port 3

This item indicates that an sSATA port specified by the user is installed (present) or not.

Port 0 ~ Port 3 sSATA Device Type (Available when a SATA port is detected)

Use this item to specify if the sSATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are Hard Disk Drive and Solid State Drive.

**If the item above "Configure sSATA as" is set to RAID, the following items will display:*

Support Aggressive Link Power Management

When this item is set to Enabled, the SATA AHCI controller manages the power usage of the SATA link. The controller will put the link to a low power state when the I/O is inactive for an extended period of time, and the power state will return to normal when the I/O becomes active. The options are **Enabled** and Disabled.

sSATA RAID Option ROM/UEFI Driver

Select EFI to load the EFI driver for system boot. Select Legacy to load a legacy driver for system boot. The options are Disabled, EFI, and **Legacy**.

SATA/sSATA RAID Boot Select

Use this item to specify which controller for the system to boot from. The options are SATA Controller, **sSATA Controller**, and Both.

sSATA Port 0~ Port 1

This item displays the information detected on the installed sSATA drives on the particular sSATA port.

- Model number of drive and capacity
- Software Preserve Support

sSATA Port 0~ Port 3

Select Enabled to enable an sSATA port specified by the user. The options are Disabled and Enabled.

sSATA Port 0 ~ Port 3 Hot Plug

This feature designates this port for hot plugging. Set this item to Enabled for hot-plugging support, which will allow the user to replace an sSATA drive without shutting down the system. The options are Enabled and **Disabled**.

sSATA Port 0 ~ Port 3 Spin Up Device

On an edge detect from 0 to 1, set this item to allow the PCH to start a COMRE-SET initialization to the device. The options are Enabled and **Disabled**.

Port 0 ~ Port 3 sSATA Device Type

Use this item to specify if the sSATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are Hard Disk Drive and Solid State Drive.

► Server ME (Management Engine) Configuration

This feature displays the following system ME configuration settings.

- General ME Configuration
- Operational Firmware Version
- ME Firmware Type
- Recovery Firmware Version
- ME Firmware Features
- ME Firmware Status #1
- ME Firmware Status #2

- Current State
- Error Code

► PCIe/PCI/PnP Configuration

The following PCI information will be displayed:

- PCI Bus Driver Version

PCI Latency Timer

Use this item to configure the PCI latency timer for a device installed on a PCI bus. Select 32 to set the PCI latency timer to 32 PCI clock cycles. The options are **32**, 64, 96, 128, 160, 192, 224 and 248 (PCI Bus Clocks).

PCI PERR# Generation

Enabling this setting will allow the BIOS to generate a PERR number when a runtime PERR (PCI/PCI-E Parity Error) event occurs on a PCI/PCI-E slot for PERR reporting. The options are Enabled and Disabled.

PCI SERR# Support

Enabling this setting will allow the BIOS to generate a SERR number when a runtime SERR (System Error) event occurs on a PCI/PCI-E slot for SERR reporting. The options are Enabled and **Disabled**.

PCI PERR/SERR Support

Select Enabled to allow a PCI device to generate a PERR/SERR number for a PCI Bus Signal Error Event. The options are Enabled and **Disabled**.

Above 4G Decoding (Available if the system supports 64-bit PCI decoding)

Select Enabled to decode a PCI device that supports 64-bit in the space above 4G Address. The options are Enabled and **Disabled**.

SR-IOV (Available if the system supports Single-Root Virtualization)

Select Enabled for Single-Root IO Virtualization support. The options are Enabled and **Disabled**.

Maximum Payload

Select Auto for the system BIOS to automatically set the maximum payload value for a PCI-E device to enhance system performance. The options are **Auto**, 128 Bytes, and 256 Bytes.

Maximum Read Request

Select Auto for the system BIOS to automatically set the maximum size for a read request for a PCI-E device to enhance system performance. The options are **Auto**, 128 Bytes, 256 Bytes, 512 Bytes, 1024 Bytes, 2048 Bytes, and 4096 Bytes.

ASPM Support

Use this item to set the Active State Power Management (ASPM) level for a PCI-E device. Select Auto for the system BIOS to automatically set the ASPM level based on the system configuration. Select Disabled to disable ASPM support. The options are **Disabled**, and Auto.

Warning: Enabling ASPM support may cause some PCI-E devices to fail!

MMIOHBase

Use this item to select the base memory size according to memory-address mapping for the IO hub. The base memory size must be between 4032G to 4078G. The options are **56T**, 48T, 24T, 512G, and 256G.

MMIO High Size

Use this item to select the high memory size according to memory-address mapping for the IO hub. The options are 256G, 128G, **512G**, and 1024G.

CPU2 SLOT1 PCI-E 3.0 x8 OPRPM**CPU1 SLOT2 PCI-E 3.0 x8 OPRPM****CPU2 GPU SLOT1 PCI-E 3.0 x16 OPRPM****CPU2 GPU SLOT2 PCI-E 3.0 x16 OPRPM****CPU1 GPU SLOT3 PCI-E 3.0 x16 OPRPM****CPU1 GPU SLOT4 PCI-E 3.0 x16 OPRPM**

Select Enabled to enable Option ROM support to boot the computer using a device installed on the slot specified by the user. The options are Disabled, **Legacy** and EFI.

VGA Priority

Use this item to select the graphics device to be used as the primary video display for system boot. The options are **Onboard** and Offboard.

Onboard LAN Option ROM Type

Select Enabled to enable Option ROM support to boot the computer using a network device specified by the user. The options are Disabled, **Legacy** and EFI.

Onboard LAN1 OPROM

Onboard LAN2 OPROM

Use this option to select the type of device installed in LAN Port1, LAN Port2, LAN Port3, LAN Port4, or the onboard video device used for system boot. The default setting for LAN1 Option ROM is **PXE, Disabled** for LAN2 Option ROM, and **Legacy** for Onboard Video Option ROM.

►Super IO Configuration

Super IO Chip AST2400

►Serial Port 1 Configuration/Serial Port 2 Configuration

Serial Port 1/Serial Port 2

Select Enabled to enable the onboard serial port specified by the user. The options are **Enabled** and Disabled.

Device Settings

This item displays the base I/O port address and the Interrupt Request address of a serial port specified by the user.

Change Port 1 Settings/Change Port 2 Settings

This feature specifies the base I/O port address and the Interrupt Request address of Serial Port 1 or Serial Port 2. Select **Auto** for the BIOS to automatically assign the base I/O and IRQ address to a serial port specified.

The options for Serial Port 1 are **Auto**, (IO=3F8h; IRQ=4), (IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12); (IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), and (IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12).

The options for Serial Port 2 are **Auto**, (IO=3F8h; IRQ=4), (IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12); (IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), and (IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12).

Serial Port 2 Attribute

Select SOL to use COM Port 2 as a Serial_Over_LAN (SOL) port for console redirection. The options are COM and **SOL**.

►Serial Port Console Redirection

COM 1

COM 1 Console Redirection

Select Enabled to enable COM Port 1 Console Redirection, which will allow a client machine to be connected to a host machine at a remote site for networking. The options are **Disabled** and Enabled.

**If the item above set to Enabled, the following items will become available for configuration:*

►COM1 Console Redirection Settings

Terminal Type

This feature allows the user to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are ANSI, VT100, **VT100+**, and VT-UTF8.

Bits Per second

Use this item to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and **115200** (bits per second).

Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 (Bits) and **8 (Bits)**.

Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark and Space.

Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and 2.

Flow Control

Use this item to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are **Enabled** and Disabled.

Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

Legacy OS Redirection Resolution

Use this item to select the number of rows and columns used in Console Redirection for legacy OS support. The options are 80x24 and **80x25**.

Putty KeyPad

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SCO, ESCN, and VT400.

Redirection After BIOS POST

Use this feature to enable or disable legacy Console Redirection after BIOS POST. When the option-Bootloader is selected, legacy Console Redirection is disabled before booting the OS. When the option- Always Enable is selected, legacy Console Redirection remains enabled upon OS bootup. The options are **Always Enable** and **Bootloader**.

SOL/COM2

Console Redirection

Select Enabled to use the SOL port for Console Redirection. The options are **Enabled** and **Disabled**.

**If the item above set to Enabled, the following items will become available for user's configuration:*

► SOL/COM2 Console Redirection Settings

Use this feature to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are ANSI, VT100, **VT100+**, and VT-UTF8.

Bits Per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and **115200** (bits per second).

Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 (Bits) and **8 (Bits)**.

Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark and Space.

Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and 2.

Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start data-sending when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are **Enabled** and Disabled.

Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

Legacy OS Redirection Resolution

Use this feature to select the number of rows and columns used in Console Redirection for legacy OS support. The options are 80x24 and **80x25**.

Putty KeyPad

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SCO, ESCN, and VT400.

Redirection After BIOS POST

Use this feature to enable or disable legacy Console Redirection after BIOS POST (Power-On Self-Test). When this feature is set to Bootloader, legacy Console Redirection is disabled before booting the OS. When this feature is set to Always Enable, legacy Console Redirection remains enabled upon OS boot. The options are **Always Enable** and Bootloader.

► EMS Console Redirection Settings

Out-of-Band Management Port

The feature selects a serial port in a client server to be used by the Windows Emergency Management Services (EMS) to communicate with a remote host server. The options are **COM1 (Console Redirection)** and COM2/SOL (Console Redirection).

Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII character set. Select VT100+ to add color and function key support. Select ANSI to use the extended ASCII character set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are ANSI, VT100, VT100+, and **VT-UTF8**.

Bits Per Second

This item sets the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in both host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 57600, and **115200** (bits per second).

Flow Control

Use this item to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop data-sending when the receiving buffer is full. Send a "Start" signal to start data-sending when the receiving buffer is empty. The options are **None**, Hardware RTS/CTS, and Software Xon/Xoff.

The setting for each these features is displayed:

Data Bits, Parity, Stop Bits

►ACPI Settings

WHEA Support

Select Enabled to support the Windows Hardware Error Architecture (WHEA) platform and provide a common infrastructure for the system to handle hardware errors within the Windows OS environment to reduce system crashes and to enhance system recovery and health monitoring. The options are Enabled and **Disabled**.

High Precision Timer

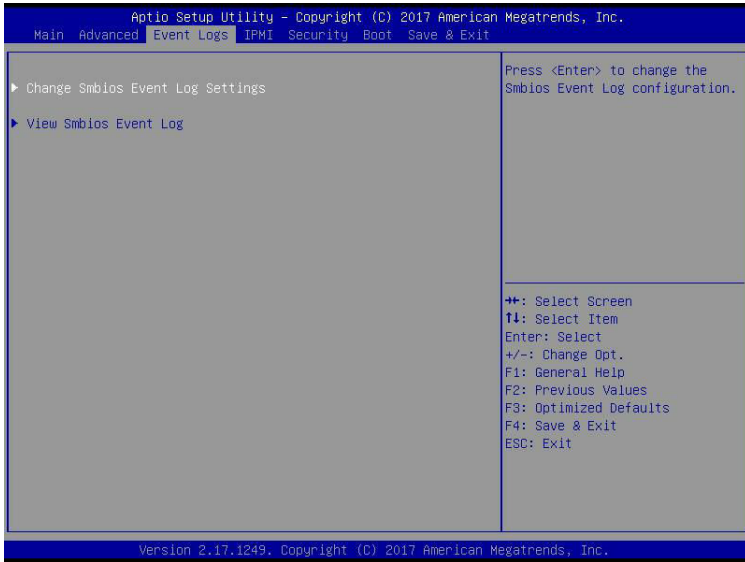
Select Enabled to activate the High Precision Event Timer (HPET) that produces periodic interrupts at a much higher frequency than a Real-time Clock (RTC) does in synchronizing multimedia streams, providing smooth playback and reducing the dependency on other timestamp calculation devices, such as an x86 RDTSC Instruction embedded in the CPU. The High Performance Event Timer is used to replace the 8254 Programmable Interval Timer. The options are **Enabled** and Disabled.

NUMA (Available when the OS supports this feature)

Select Enabled to enable Non-Uniform Memory Access support to enhance system performance. The options are **Enabled** and Disabled.

4-4 Event Logs

Use this feature to configure Event Log settings.



► Change SMBIOS Event Log Settings

This feature allows the user to configure SMBIOS Event settings.

Enabling/Disabling Options

SMBIOS Event Log

Select Enabled to enable SMBIOS (System Management BIOS) Event Logging during system boot. The options are **Enabled** and Disabled.

Runtime Error Logging Support

Select Enabled to support Runtime Error Logging. The options are **Enabled** and Disabled. If this item is set to Enable, the following item will be available for configuration:

Erasing Settings

Erase Event Log

Select Enabled to erase all error events in the SMBIOS (System Management BIOS) log before an event logging is initialized at bootup. The options are **No** and Yes.

When Log is Full

Select Erase Immediately to immediately erase all errors in the SMBIOS event log when the event log is full. Select Do Nothing for the system to do nothing when the SMBIOS event log is full. The options are **Do Nothing** and Erase Immediately.

SMBIOS Event Log Standard Settings

Log System Boot Event

Select Enabled to log system boot events. The options are **Disabled** and Enabled.

MECI (Multiple Event Count Increment)

Enter the increment value for the multiple event counter. Enter a number between

1 to 255. The default setting is **1**.

METW (Multiple Event Count Time Window)

This item is used to determine how long (in minutes) should the multiple event counter wait before generating a new event log. Enter a number between 0 to 99. The default setting is **60**.



Note: Please reboot the system for the changes to take effect.

►View SMBIOS Event Log

This item allows the user to view the event in the SMBIOS event log. Select this item and press <Enter> to view the status of an event in the log. The following categories are displayed:

Date/Time/Error Code/Severity

4-5 IPMI

Use this feature to configure Intelligent Platform Management Interface (IPMI) settings.



IPMI Firmware Revision

This item indicates the IPMI firmware revision used in your system.

Status of BMC

This item indicates the status of the IPMI firmware installed in your system.

► System Event Log

Enabling/Disabling Options

SEL Components

Select **Enabled** to enable all system event logging support at bootup. The options are **Enabled** and **Disabled**.

Erasing Settings

Erase SEL

Select Yes, On next reset to erase all system event logs upon next system reboot.
Select Yes, On every reset to erase all system event logs upon each system reboot.
Select No to keep all system event logs after each system reboot. The options are **No**, Yes, On next reset, and Yes, On every reset.

When SEL is Full

This feature allows the user to determine what the BIOS should do when the system event log is full. Select Erase Immediately to erase all events in the log when the system event log is full. The options are **Do Nothing** and Erase Immediately.



Note: After making changes on a setting, be sure to reboot the system for the changes to take effect.

Custom EFI Logging Options

Log EFI Status Codes

This feature allows the user to determine whether the BIOS will log EFI Status Codes, error code only, progress code only, or both. The options are Disabled, Both, **Error code**, and Progress code

►BMC Network Configuration

The following items will be displayed:

- IPMI LAN Selection
- Current Configuration Address course
- Station IP Address
- Subnet Mask
- Station MAC Address
- Gateway IP Address
- VLAN

Update IPMI LAN Configuration

Select Yes for the system BIOS to automatically reset the following IPMI settings at next system boot. The options are Yes and **No**.

Configuration Address Source (Available when the item above - Update IPMI LAN Configuration is set to Yes)

Use this item to select the IP address source for this computer. If Static is selected, you will need to know the IP address of this computer and enter it to the system manually in the field. If DHCP is selected, AMI BIOS will search for a DHCP (Dynamic Host Configuration Protocol) server attached to the network and request the next available IP address for this computer. The options are **DHCP** Unspecified, and Static.

Station IP Address

This item displays the Station IP address for this computer. This should be in decimal and in dotted quad form (i.e., 192.168.10.253).

Subnet Mask

This item displays the sub-network that this computer belongs to. The value of each three-digit number is separated by dots and it should not exceed 255.

Station MAC Address

This item displays the Station MAC address for this computer. Mac addresses are 6 two-digit hexadecimal numbers.

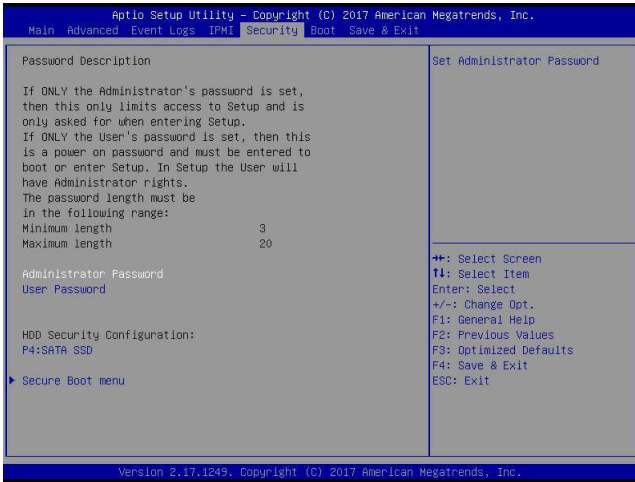
Gateway IP Address

This item displays the Gateway IP address for this computer. This should be in decimal and in dotted quad form (i.e., 192.168.10.253).

When you have completed the system configuration changes, select this option to save the changes and reboot the computer so that the new system configuration settings can take effect. Select Save Changes and Exit, and press <Enter>. When the dialog box appears, asking you if you want to exit the BIOS setup without saving, click **Yes** to quit BIOS without saving the changes, or click No to quit the BIOS and save changes.

4-6 Security Settings

This menu allows the user to configure the following security settings for the system.



Password Description

Administrator Password

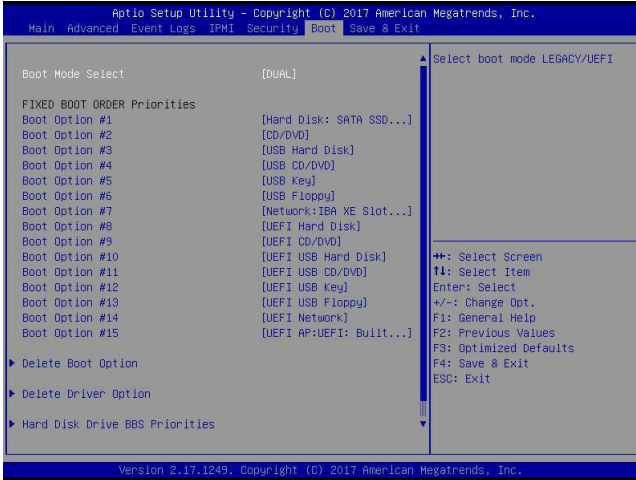
Use this feature to set the administrator password which is required before entering the BIOS setup utility. The length of the password should be from 3 characters to 20 characters long.

User Password

Use this feature to set the user password which is required to enter the BIOS setup utility. The length of the password should be from 3 characters to 20 characters long.

4-7 Boot Settings

Use this feature to configure Boot Settings:



Boot Configuration

Boot Mode Select

Use this item to select the type of device to be used for system boot. The options are Legacy, UEFI, and **Dual**.

Fixed Boot Order Priorities

This option prioritizes the order of bootable devices from which the system will boot. Press <Enter> on each entry from top to bottom to select devices.

- Dual Boot Order #1
- Dual Boot Order #2
- Dual Boot Order #3
- Dual Boot Order #4
- Dual Boot Order #5
- Dual Boot Order #6
- Dual Boot Order #7

- Dual Boot Order #8
- Dual Boot Order #9
- Dual Boot Order #10
- Dual Boot Order #11
- Dual Boot Order #12
- Dual Boot Order #13
- Dual Boot Order #14
- Dual Boot Order #15

► **Delete Boot Option**

Use this item to select a boot device to delete from the boot priority list.

Delete Boot Option

Select the target boot device to delete.

► **Delete Driver Option**

Use this item to select a boot device to delete from the boot priority list.

Delete Driver Option

Select the target driver device to delete.

► **Hard Disk Drive BBS Priorities**

- Legacy Boot Order #1 - Legacy Boot Order #10

► **Network Drive BBS Priorities**

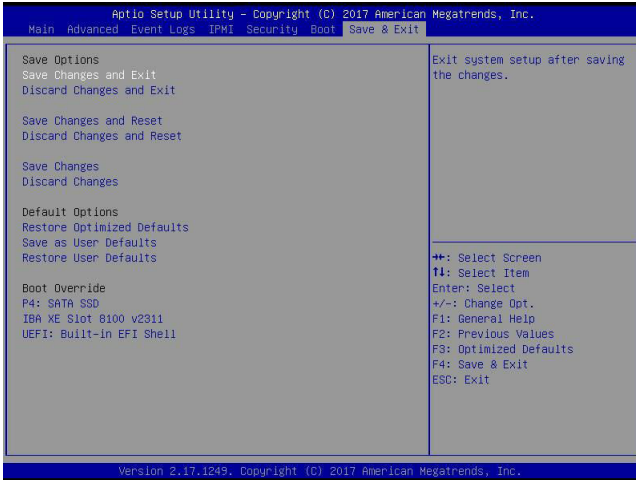
- Legacy Boot Order #1 - Legacy Boot Order #3

► **UEFI Application Boot Priorities**

- UEFI Boot Order #1

4-8 Save & Exit

Select the Save & Exit tab from the BIOS setup screen to configure the settings below.



Save Options

Save Changes and Exit

When you have completed the system configuration changes, select this option to leave the BIOS setup utility and reboot the computer for the new system configuration parameters can take effect. Select Save Changes and Exit from the Exit menu and press <Enter>.

Discard Changes and Exit

Select this option to quit the BIOS setup without making any permanent changes to the system configuration, and reboot the computer. Select Discard Changes and Exit from the Exit menu and press <Enter>.

Save Changes

When you have completed the system configuration changes, select this option to save all changes made. This will not reset (reboot) the system.

Discard Changes

Select this option and press <Enter> to discard all the changes and return to the AMI BIOS Utility Program.

Default Options

Restore Optimized Defaults

To set this feature, select Restore Defaults from the Exit menu and press <Enter>. These are manufacture default settings designed for maximum system performance but not for maximum stability.

Save As User Defaults

To set this feature, select Save as User Defaults from the Exit menu and press <Enter>. This enables the user to save any changes to the BIOS setup for future use.

Restore User Defaults

To set this feature, select Restore User Defaults from the Exit menu and press <Enter>. Use this feature to retrieve user-defined settings that were saved previously.

Boot Override

This feature allows the user to override the Boot priorities sequence in the Boot menu, and immediately boot the system with another device specified by the user. This is a one-time override.

Notes

Appendix A

BIOS Error Beep Codes

During the POST (Power-On Self-Test) routines, which are performed at each system boot, errors may occur.

Non-fatal errors are those which, in most cases, allow the system to continue to boot. The error messages normally appear on the screen.

Fatal errors will not allow the system to continue with bootup procedure. If a fatal error occurs, you should consult with your system manufacturer for possible repairs.

These fatal errors are usually communicated through a series of audible beeps. The numbers on the fatal error list correspond to the number of beeps for the corresponding error.

A-1 BIOS Error Beep Codes

Motherboard BIOS Error Beep Codes		
Beep Code/LED	Error Message	Description
1 beep	Refresh	Ready to boot
5 short beeps + 1 long beep	Memory error	No memory detected in the system
5 beeps	No Con-In or No Con-Out devices	Con-In includes USB or PS/2 keyboard, PCI or Serial Console Redirection, IPMI KVM or SOL. Con-Out includes Video Controller, PCI or Serial Console Redirection, IPMI SOL.
1 beep per device	Refresh	1 beep or each USB device detected
X9 IPMI Error Codes		
1 Continuous Beep	System OH	System Overheat

Notes

Appendix B

Software Installation Instructions

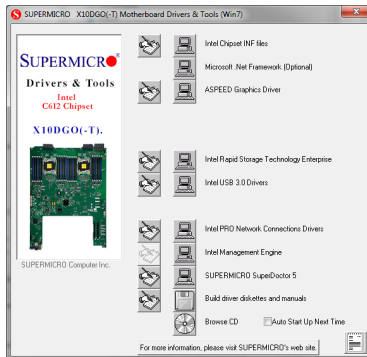
B-1 Installing Software Programs

The Supermicro ftp site contains drivers and utilities for your system at <ftp://ftp.supermicro.com>. Some of these must be installed, such as the chipset driver.

After accessing the ftp site, go into the CDR_Images directory and locate the ISO file for your motherboard. Download this file to create a CD/DVD of the drivers and utilities it contains. (You may also use a utility to extract the ISO file if preferred.)

Another option is to go to the Supermicro Website at <http://www.supermicro.com/products/>. Find the product page for your motherboard here, where you may download individual drivers and utilities.

After creating a CD/DVD with the ISO files, insert the disk into the CD/DVD drive on your system and the display shown below should appear.



Driver/Tool Installation Display Screen



Note 1: Click the icons showing a hand writing on paper to view the readme files for each item. Click the computer icons to the right of these items to install each item (from top to the bottom) one at a time. **After installing each item, you must re-boot the system before moving on to the next item on the list.** The bottom icon with a CD on it allows you to view the entire contents.

Note 2: When making a storage driver diskette by booting into a Driver CD, please set the SATA Configuration to "Compatible Mode" and configure SATA as IDE in the BIOS Setup. After making the driver diskette, be sure to change the SATA settings back to your original settings.

B-2 Configuring SuperDoctor 5

The Supermicro SuperDoctor® 5 is a hardware monitoring program that functions in a command-line or web-based interfaces. The program monitors system health information such as CPU temperature, system voltages, system power consumption, fan speed, and provides alerts via email or Simple Network Management Protocol (SNMP).

SuperDoctor 5 comes in local and remote management versions and can be used with Nagios to maximize your system monitoring needs. With SuperDoctor 5 Management Server (SSM Server), you can remotely control power on/off and reset chassis intrusion for multiple systems with SuperDoctor 5 or IPMI. SD5 Management Server monitors HTTP, FTP, and SMTP services to optimize the efficiency of your operation.



Note: The default Username and Password for SuperDoctor 5 is admin/admin.



Note: The SuperDoctor 5 program and User's Manual can be downloaded from the Supermicro web site at http://www.supermicro.com/products/nfo/sms_sd5.cfm.

B-3 Logging into the BMC (Baseboard Management Controller)

Supermicro ships standard products with a unique password for the BMC user. This password can be found on a label on the motherboard.

When logging in to the BMC for the first time, please use the unique password provided by Supermicro to log in. You can change the unique password to a user name and password of your choice for subsequent logins.

For more information regarding BMC passwords, please visit our website at <http://www.supermicro.com/bmcpassword>.

Appendix C

UEFI BIOS Recovery Instructions

Warning: Do not upgrade the BIOS unless your system has a BIOS-related issue. Flashing the wrong BIOS can cause irreparable damage to the system. In no event shall Supermicro be liable for direct, indirect, special, incidental, or consequential damages arising from a BIOS update. If you need to update the BIOS, do not shut down or reset the system while the BIOS is updating to avoid possible boot failure.

C-1 An Overview to the UEFI BIOS

The Unified Extensible Firmware Interface (UEFI) provides a software-based interface between the operating system and the platform firmware in the pre-boot environment. The UEFI specification supports an architecture-independent mechanism for add-on card initialization to allow the UEFI OS loader, which is stored in the add-on card, to boot the system. The UEFI offers a clean, hands-off control to a computer system at bootup.

C-2 How to Recover the UEFI BIOS Image (-the Main BIOS Block)

A UEFI BIOS flash chip consists of a recovery BIOS block and a main BIOS block (a main BIOS image). The boot block contains critical BIOS codes, including memory detection and recovery codes for the user to flash a new BIOS image if the original main BIOS image is corrupted. When the system power is on, the boot block codes execute first. Once it is completed, the main BIOS code will continue with system initialization and bootup.




Note: Follow the BIOS recovery instructions below for BIOS recovery when the main BIOS boot crashes. However, when the BIOS boot block crashes, you will need to follow the procedures below for BIOS recovery.

C-3 To Recover the Main BIOS Block Using a USB-Attached Device

This feature allows the user to recover a BIOS image using a USB-attached device without additional utilities used. A USB flash device such as a USB Flash Drive, or a USB CD/DVD ROM/RW device can be used for this purpose. However, a USB Hard Disk drive cannot be used for BIOS recovery at this time.

To perform UEFI BIOS recovery using a USB-attached device, follow the instructions below.


1. Using a different machine, copy the "Super.ROM" binary image file into the disc Root "\\" Directory of a USB device or a writeable CD/DVD.

 **Note:** If you cannot locate the "Super.ROM" file in your driver disk, visit our website at www.supermicro.com to download the BIOS image into a USB flash device and rename it "Super.ROM" for BIOS recovery use.

2. Insert the USB device that contains the new BIOS image ("Super.ROM") into your USB drive and power on the system
3. While powering on the system, please keep pressing <Ctrl> and <Home> simultaneously on your keyboard until the following screen (or a screen similar to the one below) displays.

Warning!! Please **stop** pressing the <Ctrl> and <Home> keys immediately when you see the screen (or a similar screen) below; otherwise, it will trigger a system reboot.



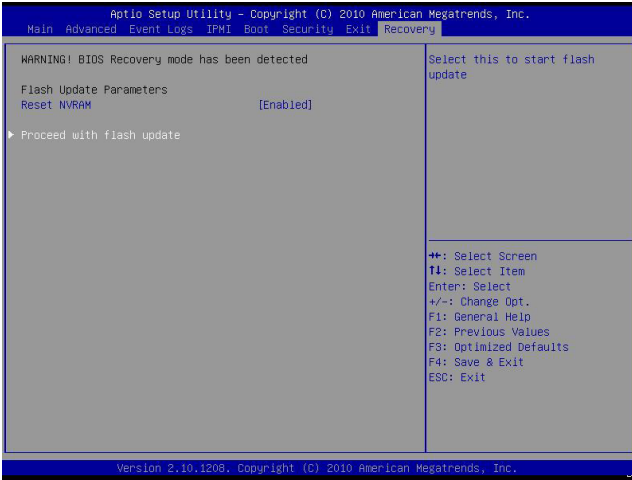
 **Note:** On the other hand, if the following screen displays, please load the "Super.ROM" file to the root folder and connect this folder to the system. (You can do so by inserting a USB device that contains the new "Super.ROM" image to your machine for BIOS recovery.)



- After locating the new BIOS binary image, the system will enter the BIOS Recovery menu as shown below.



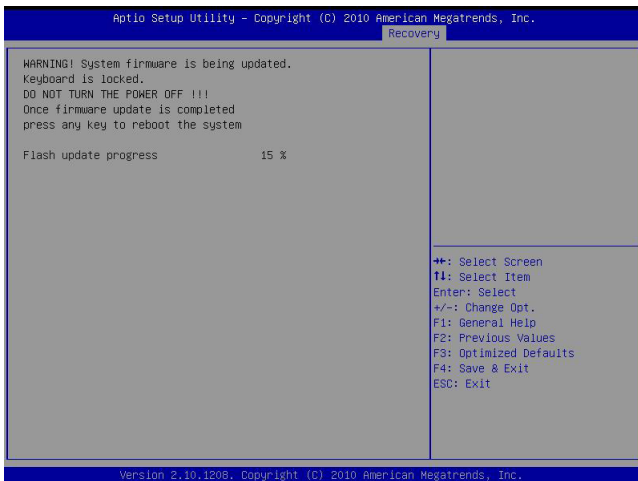
Note: At this point, you may decide if you want to start with BIOS recovery. If you decide to proceed with BIOS recovery, follow the procedures below.



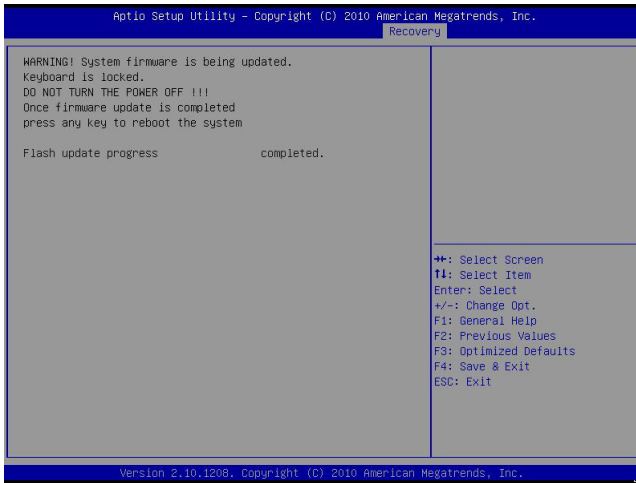
- When the screen as shown above displays, using the arrow key, select the item "Proceed with flash update" and press the <Enter> key. You will see the progress of BIOS recovery as shown in the screen below.



Note: Do not interrupt the process of BIOS flashing until it is completed.



6. After the process of BIOS recovery is completed, press any key to reboot the system.



7. Using a different system, extract the BIOS package into a bootable USB flash drive.
8. When a DOS prompt appears, enter FLASH.BAT BIOSName.### at the prompt.



Note: *Do not interrupt this process* until BIOS flashing is completed.

9. After seeing the message that BIOS update is completed, unplug the AC power cable from the power supply to clear the CMOS, and then plug the AC power cable in the power supply again to power on the system.
10. Press continuously to enter the BIOS Setup utility.
11. Press <F3> to load default settings.
12. After loading default settings, press <F4> to save the settings and exit the BIOS Setup utility.

Appendix D

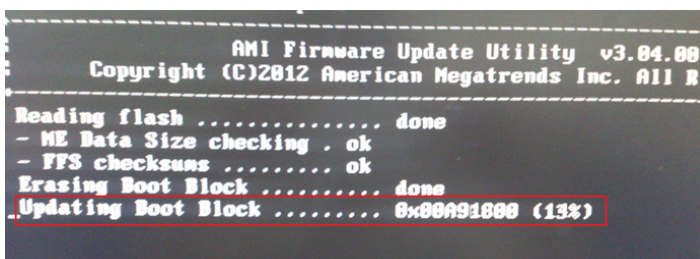
Dual Boot Block

D-1 Introduction

This motherboard supports the Dual Boot Block feature, which is the last-ditch mechanism to recover the BIOS boot block. This section provides an introduction to the feature.

BIOS Boot Block

A BIOS boot block is the minimum BIOS loader required to enable necessary hardware components for the BIOS crisis recovery flash that will update the main BIOS block. An on-call BIOS boot-block corruption may occur due to a software tool issue (see the image below) or an unexpected power outage during BIOS updates.



```
-----  
                AMI Firmware Update Utility v3.04.00  
                Copyright (C)2012 American Megatrends Inc. All Rights Reserved  
-----  
Reading flash ..... done  
- ME Data Size checking . ok  
- FFS checksums ..... ok  
Erasing Boot Block ..... done  
Updating Boot Block ..... 6x88A91800 (13%)
```

BIOS Boot Block Corruption Occurrence

When a BIOS boot block is corrupted due to an unexpected power outage or a software tool malfunctioning during BIOS updates, you can still reboot the system by closing pins 2 and 3 using a cap on Jumper JBR1. When JBR1 is set to pins 2 and 3, the system will boot from a backup boot block pre-loaded in the BIOS by the manufacturer.

D-2 Steps to Reboot the System by Using Jumper JBR1

1. Power down the system.
2. Close pins 2-3 on Jumper JBR1, and power on the system.
3. Follow the BIOS recovery SOP listed in the previous chapter (Appendix C).
4. After completing the steps above, power down the system.
5. Close pins 1-2 on Jumper JBR1, and power on the system.