X10SDD-F

USER MANUAL

Revision 1.0

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Manual Revision 1.0

Release Date: May 26, 2016

Preface

About This Manual

This manual is written for system integrators, IT technicians and knowledgeable end users. It provides information for the installation and use of the X10SDD-F motherboard.

About This Motherboard

The Super X10SDD-F motherboard supports an Intel® Xeon-D or Pentium processor in a System-on-a-Chip (SoC). With support for the following features: DDR4 memory, SATA 3.0, M.2 M-key, and Trusted Platform Module (TPM), this motherboard is ideal for small form factor, high density server platforms. Please note that this motherboard is intended to be installed and serviced by professional technicians only. For processor/memory updates, please refer to our website at http://www.supermicro.com/products/.

Conventions Used in the Manual

Special attention should be given to the following symbols for proper installation and to prevent damage done to the components or injury to yourself:

Warning! Indicates important information given to prevent equipment/property damage or personal injury.

Warning! Indicates high voltage may be encountered when performing a procedure.

Important: Important information given to ensure proper system installation or to relay safety precautions.

Note: Additional Information given to differentiate various models or provides information for correct system setup.

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Table of Contents

Chapter 1

Introduction

Congratulations on purchasing your computer motherboard from an industry leader. Supermicro boards are designed to provide you with the highest standards in quality and performance.

In additon to the motherboard and chassis, several important parts that are included with the system are listed below. If anything listed is damaged or missing, please contact your retailer.

1.1 Checklist

Main Parts List		
Description	**Part Number**	**Quantity**
Supermicro Motherboard	X10SDD-F	1
Quick Reference Guide	MNL-1839-QRG	1

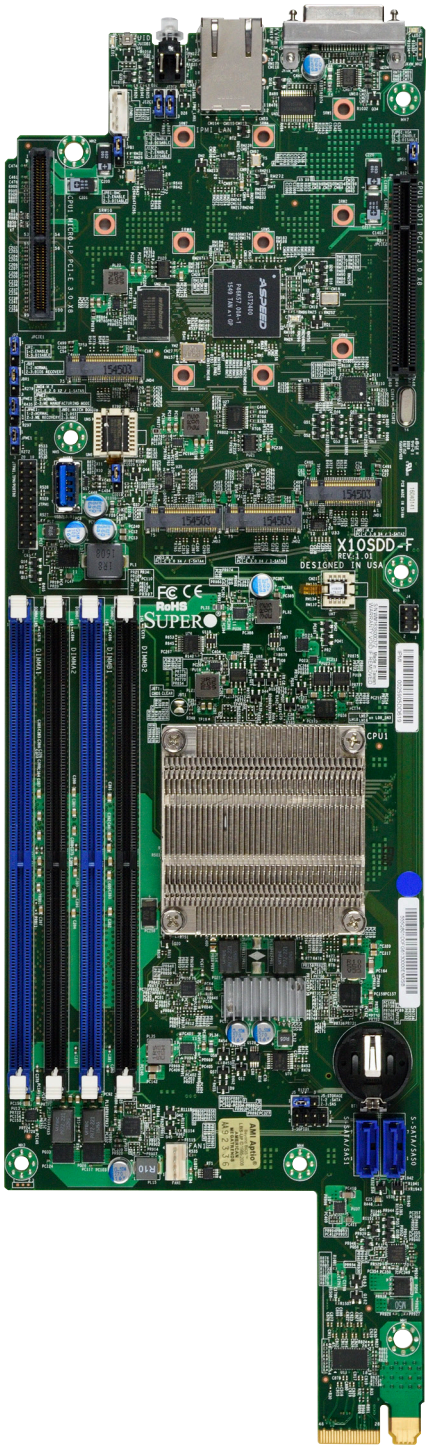
Important Links

For your system to work properly, please follow the links below to download all necessary drivers/utilities and the user's manual for your server.

- Supermicro product manuals: http://www.supermicro.com/support/manuals/
- Product drivers and utilities: ftp://ftp.supermicro.com
- Product safety info: http://www.supermicro.com/about/policies/safety_information.cfm
- If you have any questions, please contact our support team at: support@supermicro.com

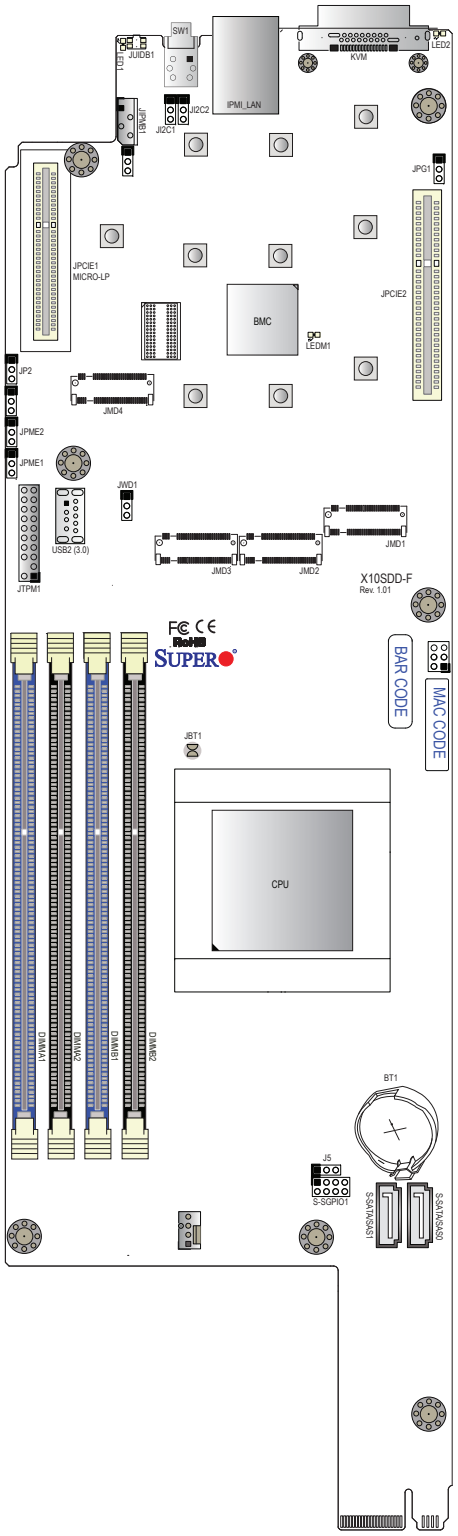
This manual may be periodically updated without notice. Please check the Supermicro website for possible updates to the manual revision level.

Figure 1-1. X10SDD-F Motherboard Image



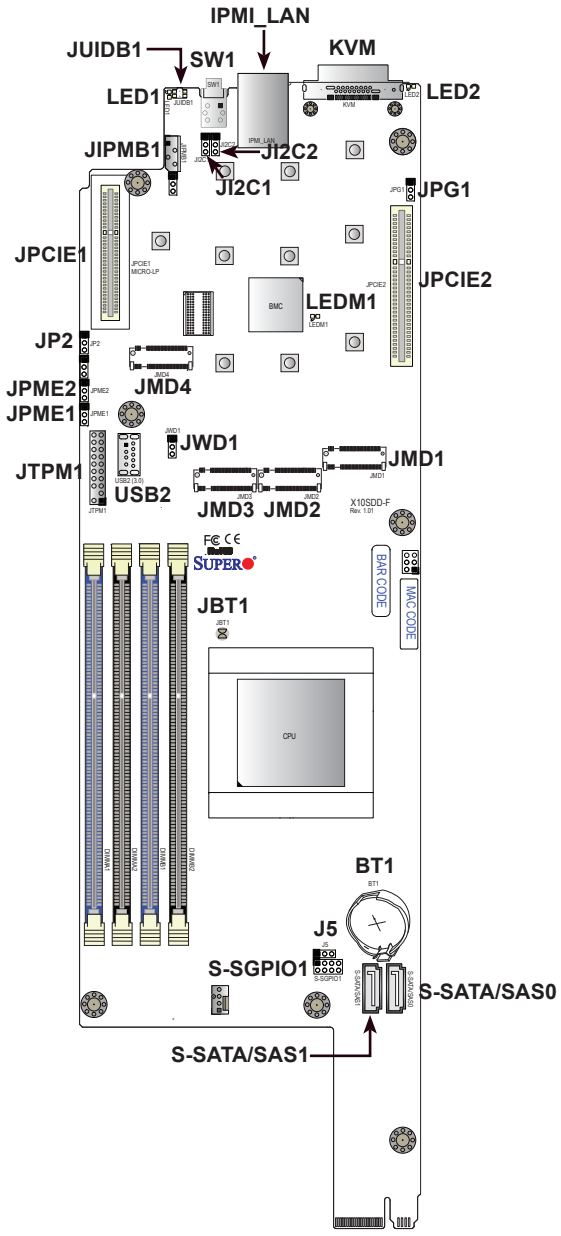
Note: All graphics shown in this manual were based upon the latest PCB revision available at the time of publication of the manual. The motherboard you received may or may not look exactly the same as the graphics shown in this manual.

Figure 1-2. X10SDD-F Motherboard Layout
(not drawn to scale)



Note: Components not documented are for internal testing only.

Quick Reference



Notes:

- See Chapter 2 for detailed information on jumpers, I/O ports, and JF1 front panel connections.
- "■" indicates the location of Pin 1.
- Jumpers/LED indicators not indicated are used for testing only.
- Use only the correct type of onboard CMOS battery as specified by the manufacturer. Do not install the onboard battery upside down to avoid possible explosion.

Quick Reference Table

Jumper	Description	Default Setting
J5	HDD BP Selection	Pins 1-2 (SATA)
JBT1	Clear CMOS	See Chapter 2
JI²C1/JI²C2	SMB to PCI Slots	Pins 2-3 (Disabled)
JP2	M.2 slots SMB Enable/Disable	Pins 1-2 (Enabled)
JPG1	VGA Enable	Pins 1-2 (Enabled)
JPME1	ME Recovery	Pins 1-2 (Normal)
JPME2	Manufacturing Mode Select	Pins 1-2 (Normal)
JWD1	Watch Dog Enable	Pins 1-2 (Reset)

LED	Description	Status
LED1	Rear UID LED	Blue: On; Unit Identified
LED2	Power Fail/Fan Fail/ Overheat LED	Red: On; Power Fail, Fan Fail, or Overheating
LEDM1	BMC Heartbeat LED	Green: Blinking; BMC Normal

Connector	Description
BT1	Onboard Battery
IPMI_LAN	Dedicated IPMI Gigabit (RJ45) Port
JIPMB1	4-pin External BMC I²C Header (for an IPMI Card)
JMD1	M.2 PCI-E 3.0 x4/I-SATA2 (Supports 2242, 2280 and 22110)
JMD2	M.2 PCI-E 3.0 x4/I-SATA3 (Supports 2242, 2280 and 22110)
JMD3	M.2 PCI-E 2.0 x4/I-SATA4 (Supports 2242, 2280 and 22110)
JMD4	M.2 PCI-E 2.0 x2/I-SATA5 (Supports 2242 only)
JPCIE1	Micro-LP PCI-E 3.0 x8
JPCIE2	Slot1 PCI-E 3.0 x8
JTPM1	Trusted Platform Module (TPM)/Port 80 Connector
JUIDB1	UID (Unit Identification) Switch
KVM	Keyboard/VGA/Mouse Connector
S-SATA/SAS0	For SAS AOC use
S-SATA/SAS1	For SAS AOC use
S-SGPIO1	Serial Link General Purpose Header
SW1	Power Switch/LED Indicator
USB0/1	Back panel USB 2.0 ports via KVM
USB2	USB 3.0 Type-A Header

Motherboard Features

Motherboard Features	
CPU	
• Intel® Xeon® D, Pentium processor in a System-on-a-Chip (SoC).	
Memory	
Four (4) 288-pin DIMM slots support up to: • 128GB of ECC 2400/2133/1866/1600/1333MHz RDIMM memory. • 64GB of ECC 2133/1866/1600/1333MHz UDIMM memory. **Note**: 2400MHz is only supported when using DDR4 RDIMM ECC memory, with both DIMMs populated (Both A1/A2 or B1/B2).	
DIMM Size	
• 32GB, 16GB, 8GB, and 4GB, up to 128GB for RDIMM memory or up to 64GB for UDIMM memory at 1.2V	
Note 1: Memory speed support depends on the processors used in the system.	
Note 2: For the latest CPU/memory updates, please refer to our website at http://www.supermicro.com/products/motherboard.	
Expansion Slots	
• One (1) Micro-LP PCI Express 3.0 x8 slot (CPU1 Slot1) (JPCIE1) • One (1) PCI Express 3.0 x8 slot (JPCIE2) • Two (2) M.2 M-key PCI Express 3.0 x 4 connectors (JMD1 & JMD2) • One (1) M.2 M-key PCI Express 2.0 x 4 connector (JMD3) • One (1) M.2 M-key PCI Express 2.0 x 2 connector (JMD4)	
Network	
• One (1) IPMI 2.0 dedicated LAN located on the rear I/O panel	
BaseBoard Management Controller (BMC)	
• ASpeed AST 2400 Baseboard Controller (BMC) supports IPMI 2.0	
Graphics	
• Graphics controller via ASpeed AST2400 BMC	
I/O Devices	
• Serial (COM) Port	• One (1) serial port on the rear I/O panel (COM1) via KVM
• SATA 3.0	• Two (2) SATA 3.0 connectors to HDD via backplane; four (4) SATA 3.0 connectors via M.2
• SATA/SAS	• SATA from add-on card (AOC) (S-SATA/SAS0 & S-SATA/SAS1)

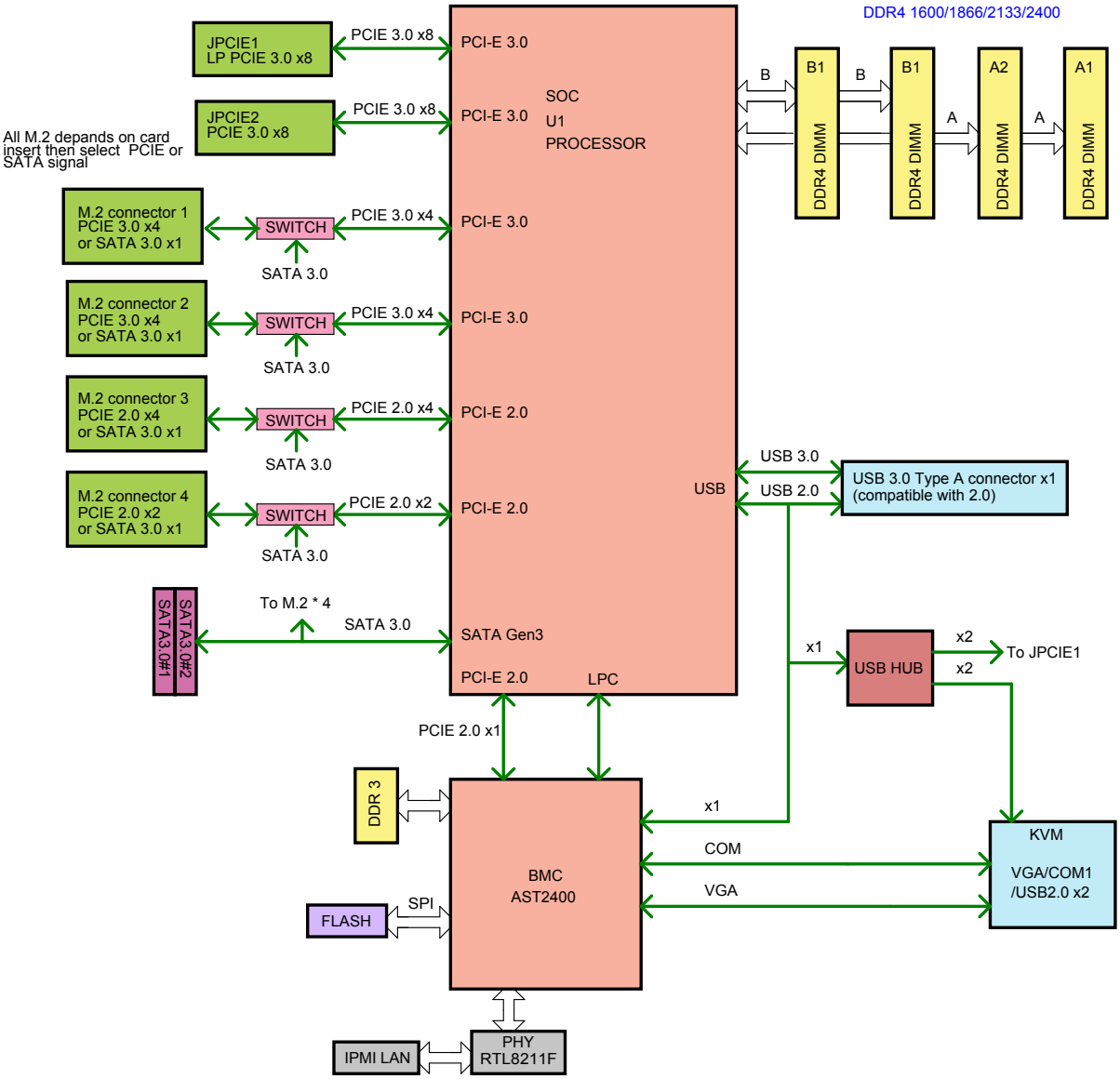
Note: The table above is continued on the next page.

Motherboard Features
Peripheral Devices
• Two (2) USB 2.0 ports (through KVM connector) (USB 0/1) • One (1) USB 3.0 Type-A header (USB 2)
BIOS
• 128Mb AMI BIOS® SPI Flash BIOS • ACPI 3.0 or later, Plug-and-Play (PnP), BIOS rescue hot-key, Riser Card auto detection support, Real Time Clock wakeup, Dual-Boot Block support, and SMBIOS 2.7 or later
Power Management
• ACPI power management • Power button override mechanism • Wake-On-LAN • Power-on mode for AC power recovery
System Health Monitoring
• Onboard voltage monitoring for: Vcpu, VDIMM, +12V, +5V, +3.3V, VBAT, +5V Stdby, +1.5V PCH, +1.2V BMC, +1.05V PCH, CPU temperature, system temperature, and memory temperature • CPU thermal trip support • PECI (Platform Environment Control Interface)/TSI
Fan Control
• Low noise fan speed control
System Management
• IPMIView, SMCIPMITOOL, IPMICFG • SCM, SPM, SUM In-Band, SUM-OOB • UID (Unit Identification)/Remote UID • SuperDoctor® 5 • Server Platform Service • Silicon enabling
LED Indicators
• CPU/Overheating • Power/suspend-state indicator • UID/remote UID • LAN activity
Dimensions
• 4.75" x 18.50"

Note 1: The CPU maximum thermal design power (TDP) is subject to chassis and heatsink cooling restrictions. For proper thermal management, please check the chassis and heatsink specifications for proper CPU TDP sizing.

Note 2: For IPMI configuration instructions, please refer to the Embedded IPMI Configuration User's Guide available at http://www.supermicro.com/support/manuals/.

Figure 1-3.
System Block Diagram



Note: This is a general block diagram and may not exactly represent the features on your motherboard. See the previous pages for the actual specifications of your motherboard.

1.2 Processor and Chipset Overview

The Intel® Xeon® D-1500 product family is the third generation, low-powered 64-bit SoC (System-on-a-Chip) processor that is optimized to deliver high performance, energy effiency and enhanced total cost of ownership solutions. The low-power consumption of the processor makes it suitable for dense servers that can effectively take on hyperscale workloads, intelligent edge network or ultra-dense embedded devices.

Based on the 14nm microarchitecture, the Xeon D-1541 SoC processor utilizes 8 processor cores on 45W of power.

Intel Xeon D-1500 Processor Features

- Low Power Consumption
- Server-Class Reliability, Availability and Serviceability (RAS)
- Intel Hyper-Threading
- Intel Turbo Boost and AVX2
- Intel Virtualization Technology (VT-x, VT-d)
- 10 Gigabit Ethernet Controller Built In
- DDR4 ECC Memory up to 128GB ECC RDIMM or 64GB ECC/Non-ECC UDIMM
- AES-NI, OS Guard, TXT Security Features
- SoC Built-in USB 3.0, USB 2.0 and 6 SATA 3.0 with SGPIO support

1.3 Special Features

Recovery from AC Power Loss

The Basic I/O System (BIOS) provides a setting that determines how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off (in which case you must press the power switch to turn it back on), or for it to automatically return to the power-on state. See the Advanced BIOS Setup section for this setting. The default setting is **Last State**.

1.4 System Health Monitoring

Onboard Voltage Monitors

The onboard voltage monitor will continuously scan crucial voltage levels. Once a voltage becomes unstable, it will give a warning or send an error message to the screen. Users can adjust the voltage thresholds to define the sensitivity of the voltage monitor. Real time readings of these voltage levels are all displayed in BIOS.

Fan Status Monitor with Firmware Control

The system health monitor embedded in the BMC chip can check the RPM status of the cooling fan. The CPU and chassis fans are controlled via IPMI

Environmental Temperature Control

System Health sensors in the BMC monitor the temperatures and voltage settings of onboard processors and the system in real time via the IPMI interface. Whenever the temperature of the CPU or the system exceeds a user-defined threshold, system/CPU cooling fans will be turned on to prevent the CPU or the system from overheating.

Note: To avoid possible system overheating, please be sure to provide adequate air-flow to your system.

System Resource Alert

This feature is available when used with SuperDoctor 5®. SuperDoctor 5 is used to notify the user of certain system events. For example, you can configure SuperDoctor 5 to provide you with warnings when the system temperature, CPU temperatures, voltages and fan speeds go beyond a predefined range.

1.5 ACPI Features

ACPI stands for Advanced Configuration and Power Interface. The ACPI specification defines a flexible and abstract hardware interface that provides a standard way to integrate power management features throughout a computer system including its hardware, operating system and application software. This enables the system to automatically turn on and off peripherals such as network cards, hard disk drives and printers.

In addition to enabling operating system-directed power management, ACPI also provides a generic system event mechanism for Plug and Play and an operating system-independent interface for configuration control. ACPI leverages the Plug and Play BIOS data structures while providing a processor architecture-independent implementation that is compatible with Windows 8/R2, and Windows 2012/R2 operating systems.

1.6 Power Supply

As with all computer products, a stable power source is necessary for proper and reliable operation. It is even more important for processors that have high CPU clock rates. In areas where noisy power transmission is present, you may choose to install a line filter to shield the computer from noise. It is recommended that you also install a power surge protector to help avoid problems caused by power surges.

1.7 Serial Port

The X10SDD-F motherboard supports one serial communication connection via KVM. COM port 1 can be used for input/output. The UART provides legacy speeds with a baud rate of up to 115.2 Kbps as well as an advanced speed with baud rates of 250 K, 500 K, or 1 Mb/s, which support high-speed serial communication devices.

Chapter 2

Installation

2.1 Static-Sensitive Devices

Electrostatic Discharge (ESD) can damage electronic components. To prevent damage to your motherboard, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing the board from the antistatic bag.
- Handle the motherboard by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure that your chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.
- Use only the correct type of CMOS onboard battery as specified by the manufacturer. Do not install the CMOS battery upside down, which may result in a possible explosion.

Unpacking

The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the motherboard, make sure that the person handling it is static protected.

2.2 Motherboard Installation

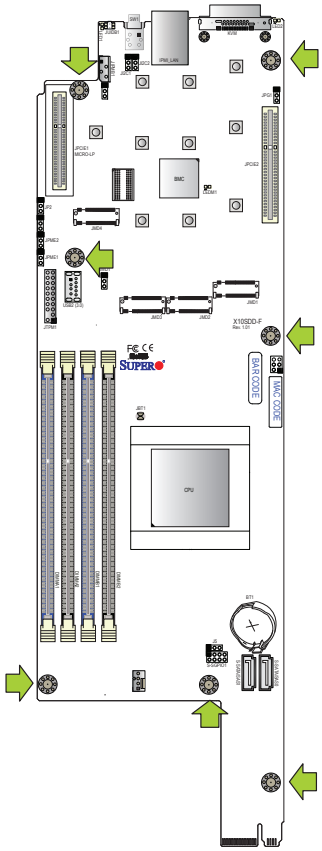
All motherboards have standard mounting holes to fit different types of chassis. Make sure that the locations of all the mounting holes for both the motherboard and the chassis match. Although a chassis may have both plastic and metal mounting fasteners, metal ones are highly recommended because they ground the motherboard to the chassis. Make sure that the metal standoffs click in or are screwed in tightly. Then use a screwdriver to secure the motherboard onto the motherboard tray.

Philips Screwdriver (1)

Philips Screws (7)

Standoffs (7) Only if Needed

Tools Needed

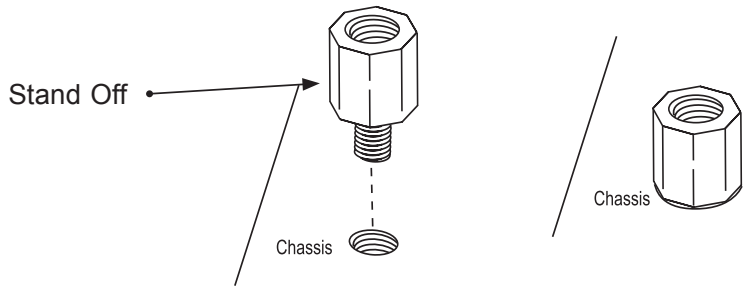


Location of Mounting Holes

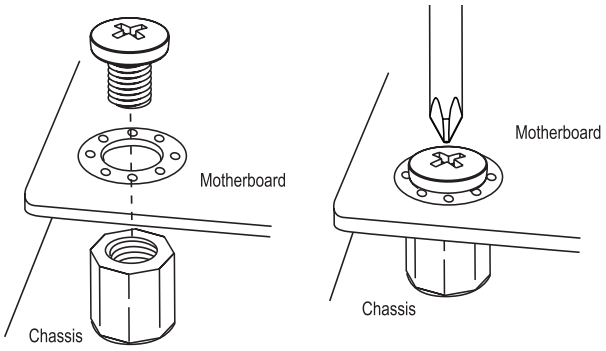
Note: 1) To avoid damaging the motherboard and its components, please do not use a force greater than 8 lb/inch on each mounting screw during motherboard installation. 2) Some components are very close to the mounting holes. Please take precautionary measures to avoid damaging these components when installing the motherboard to the chassis.

Installing the Motherboard

1. Locate the mounting holes on the motherboard. See the previous page for the location.
2. Locate the matching mounting holes on the motherboard mounting tray. Install standoffs needed. Align the mounting holes on the mothreboard against the mounting holes on the motherboard tray.



3. Install the motherboard carefully to avoid damaging motherboard components.
4. Insert a Pan head #6 screw into a mounting hole on the motherboard and its matching mounting hole on the chassis, using the Philips screwdriver.



5. Repeat Step 4 to insert #6 screws to all mounting holes. Make sure that the motherboard is securely placed on the motherboard tray. Insert the tray containing the motherboard in the chassis and follow you chassis manufacturer's installation instructions.

Note: Images displayed are for illustration only. Your chassis or components might look different from those shown in this manual.

2.3 Memory Support and Installation

Note: Check the Supermicro website for recommended memory modules.

Important: Exercise extreme care when installing or removing DIMM modules to prevent any possible damage.

Memory Support

The X10SDD-F supports DDR4 ECC memory; up to 64GB of Unbuffered (UDIMM) memory or up to 128GB of Registered (RDIMM) memory in four memory slots. Populating these DIMM modules with a pair of memory modules of the same type and size will result in interleaved memory, which will improve memory performance.

DIMM Module Population Configuration

For optimal memory performance, follow the table below when populating memory.

Processors and their Corresponding Memory Modules				
CPU#	**Corresponding DIMM Modules**			
CPU	DIMMA1	DIMMA2	DIMMB1	DIMMB2

Memory Module Population for Optimal Performance	
Number of DIMMs	**Memory Population Configuration Table (For memory to work properly, please follow the instructions below.)**
2 DIMMs	DIMMA1/DIMMB1
4 DIMMs	DIMMA1/DIMMB1, DIMMA2/DIMMB2

Memory Module Population						
DIMM Slots per Channel	**DIMM Type**	**POR Speeds (MHz)**	**Ranks per DIMM**	**Layer Count**	**FW Base**	**Supported Voltage**
2	DDR4 ECC	2400, 2133,1866, 1600, 1333	SR, DR	6	SPS	1.2V1

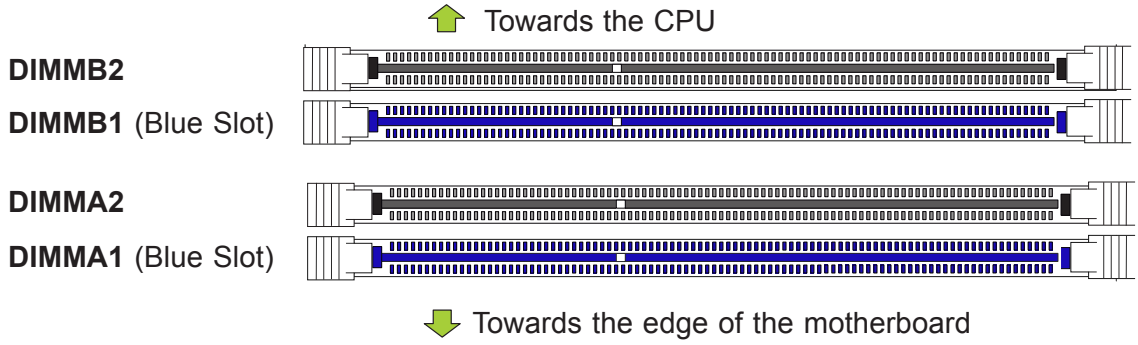
Note: 2400MHz memory speed is only supported when using DDR4 RDIMM ECC memory, with both DIMMs populated (both A1/A2 or B1/B2).

Memory Module Population		
Max Memory Possible	**4GB DRAM Technology**	**8GB DRAM Technology**
Single Rank UDIMM	16GB (4x 4GB DIMMs)	32GB (4x 8GB DIMMs)
Dual Rank UDIMMs	32GB (4x 8GB DIMMs)	64GB (4x 16GB DIMMs)

Type	Ranks Per DIMM and Data Width	DIMM Capacity (GB)		Speed (MT/s); Voltage (V); Slot Per Channel (SPC) and DIMM Per Channel (DPC)	
				2 Slots Per Channel	
				1DPC	2DPC
		4Gb	8Gb	1.2V	1.2V
RDIMM	SRx4	8GB	16GB	2133	2400
RDIMM	SRx8	4GB	8GB	2133	2400
RDIMM	DRx8	8GB	16GB	2133	2400
RDIMM	DRx4	16GB	32GB	2133	2400

DIMM Module Population Sequence

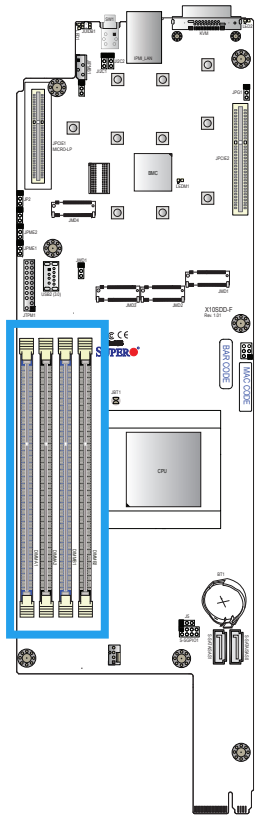
When installing memory modules, the DIMM slots must be populated in the following order: DIMMA1, DIMMB1, then DIMMA2, DIMMB2. The blue slots must be populated first.

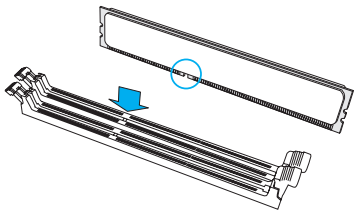


Note: Be sure to use memory modules of the same type and speed on the motherboard. Mixing of memory modules of different types and speeds is not allowed.

DIMM Installation

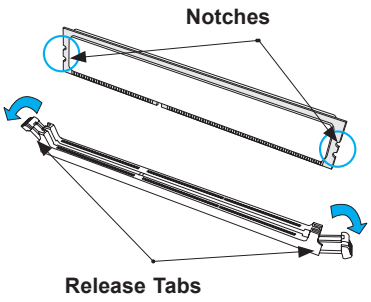
1. Insert DIMM modules in the following order: DIMMA1, DIMMB1, then DIMMA2, DIMMB2. For the system to work properly, please use memory modules of the same type and speed on the motherboard.
2. Push the release tabs outwards on both ends of the DIMM slot to unlock it.
3. Align the key of the DIMM module with the receptive point on the memory slot.
4. Align the notches on both ends of the module against the receptive points on the ends of the slot.
5. Use two thumbs together to press the notches on both ends of the module straight down into the slot until the module snaps into place.
6. Press the release tabs to the lock positions to secure the DIMM module into the slot.

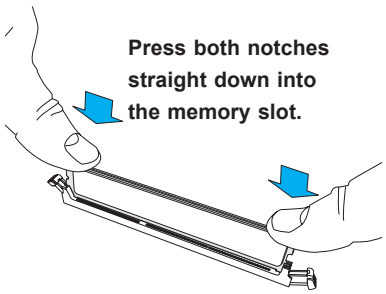




DIMM Removal

Reverse the steps above to remove the DIMM modules from the motherboard.





2.4 Rear I/O Ports

See Figure 2-2 below for the locations and descriptions of the various I/O ports on the rear of the motherboard.

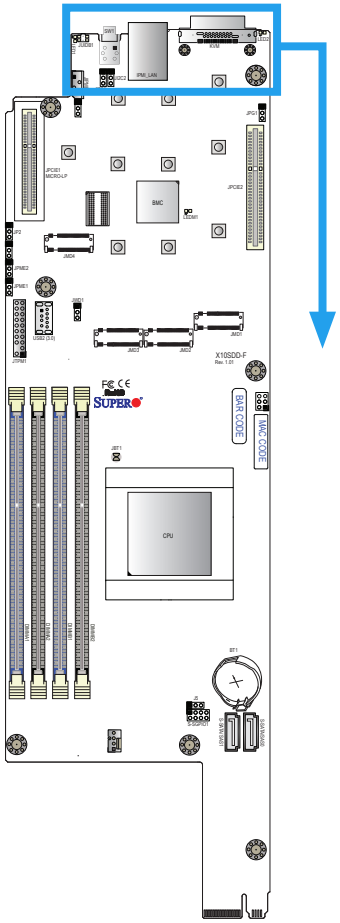
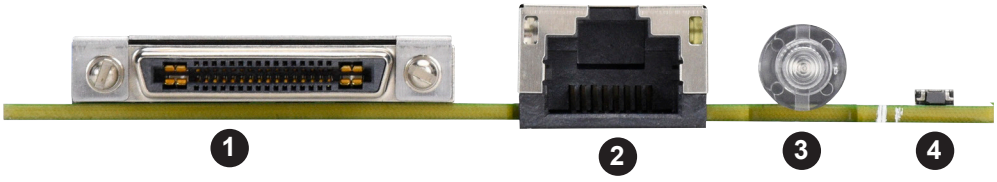


Figure 2-2. I/O Port Locations and Definitions



Rear I/O Ports			
#	**Description**	**#**	**Description**
1.	KVM Port	3.	Power Switch and LED Indicator
3.	IPMI LAN	4.	UID Switch

KVM Port

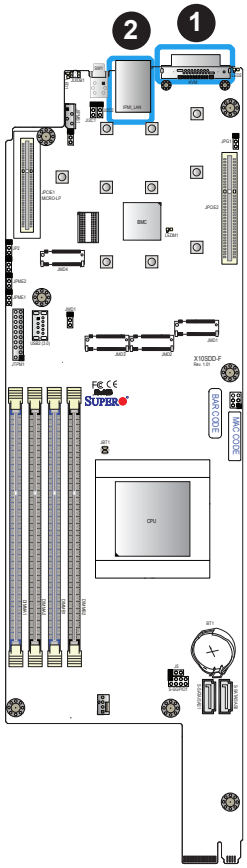
The KVM port supports two USB connections, a VGA, and UART interface. Please attach a compatible KVM connector/switch to this port.

LAN Ports

There is a dedicated IPMI LAN port on the I/O back panel. This port accepts RJ45 type cables. See the table below for the pin definitions.

IPMI_LAN Pin Definition

Pin#	Definition	Pin#	Definition
9		19	GND
10	TD0+	20	Act LED (Yellow)
11	TD0-	21	Link 100 LED (Green)
12	TD1+	22	Link 1000 LED (Amber)
13	TD1-	23	SGND
14	TD2+	24	SGND
15	TD2-	25	SGND
16	TD3+	26	SGND
17	TD3-		
18	GND		



1. KVM Port
2. IPMI_LAN

Universal Serial Bus (USB) Ports

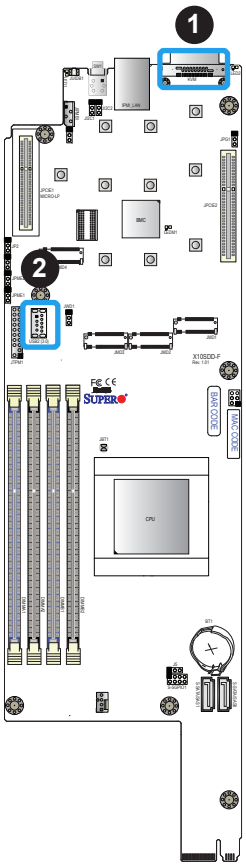
There are two Universal Serial Bus 2.0 ports (USB0/1) supported by the KVM port on the I/O back panel and can be used to provide rear USB access. An additional USB 3.0 type "A" port (USB2) is located on the motherboard. Cables are not included.

Back Panel USB0/1 (2.0) Pin Definitions

Pin#	Definition	Pin#	Definition
1	+5V	5	+5V
2	USB_N	6	USB_N
3	USB_P	7	USB_P
4	Ground	8	Ground

Type A USB2 3.0 Header Pin Definitions

Pin#	Definition	Pin#	Definition
1	VBUS	5	SSRX-
2	USB_N	6	SSRX+
3	USB_P	7	GND
4	Ground	8	SSTX-
		9	SSTX+



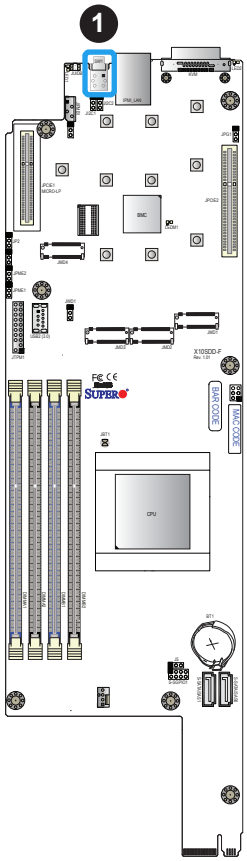
1. USB0/1
2. USB2

2.5 Connectors

Power Connections

Power Switch

A power switch (SW1) is located next to the UID button/LED on the backplane of the motherboard. Use this switch to power on or power off the system.



1. PWR Switch/LED

Headers

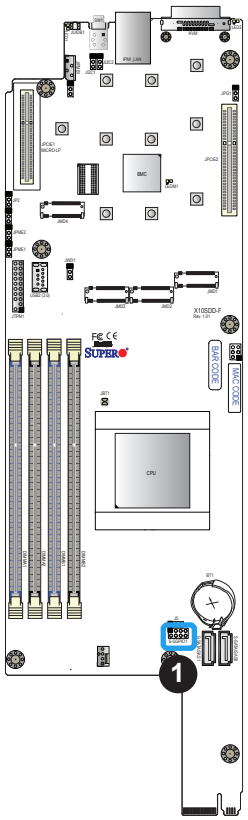
SGPIO Header

One SGPIO (Serial General Purpose Input/Output) header is located on the motherboard. S-SGPIO1 supports Serial_Link (SATA/SAS) interface. See the tables below for pin definitions.

S-SGPIO Header Pin Definitions			
Pin#	**Definition**	**Pin#**	**Definition**
1	NC	2	NC
3	Ground	4	Data
5	Load	6	Ground
7	Clock	8	NC

Note: NC = No Connection

S-SGPIO Support	
S-SGPIO1	S-SATA/SAS Ports 0 & 1 Supported



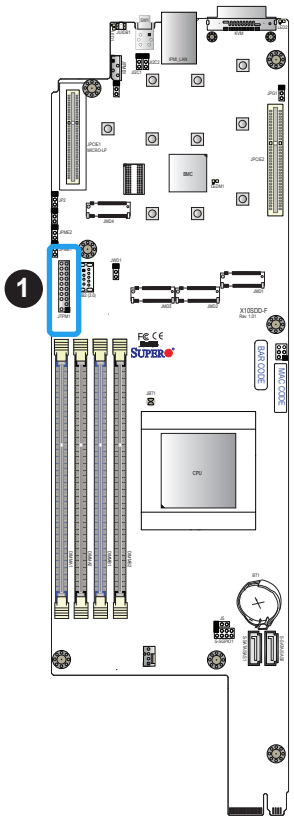
1. S-SGPIO1

TPM Header

A Trusted Platform Module (TPM) header is located at JTPM1 to provide TPM support. Use this header to enhance system performance and data security. See the table below for pin definitions.

Trusted Platform Module Header Pin Definitions

Pin#	Definition	Pin#	Definition
1	LCLK	2	GND
3	LFRAME#	4	<(KEY)>
5	LRESET#	6	+5V
7	LAD3	8	LAD2
9	+3.3V	10	LAD1
11	LAD0	12	GND
13	SMB_CLK	14	SMB_DAT
15	+3V Stdby	16	SERIRQ
17	GND	18	CLKRUN#
19	LPCPD#	20	LDRQ#

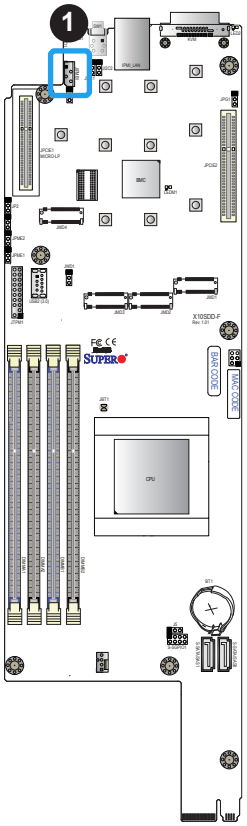


1. TPM Header

4-pin BMC External I²C Header

A System Management Bus header for IPMI 2.0 is located at JIPMB1. Connect the appropriate cable here to use the IPMB I²C connection on your system. See the table below for pin definitions.

External I²C Header Pin Definitions	
Pin#	**Definition**
1	Data
2	GND
3	Clock
4	NC



1. BMC External Header

SATA and SAS Ports

Two Serial ATA (SATA)/Serial Attached SCSI (SAS) 3.0 connectors (I-SATA0/SAS0, I-SATA1/SAS1) are located on the motherboard. These connectors are for add-on card (storage card) use.

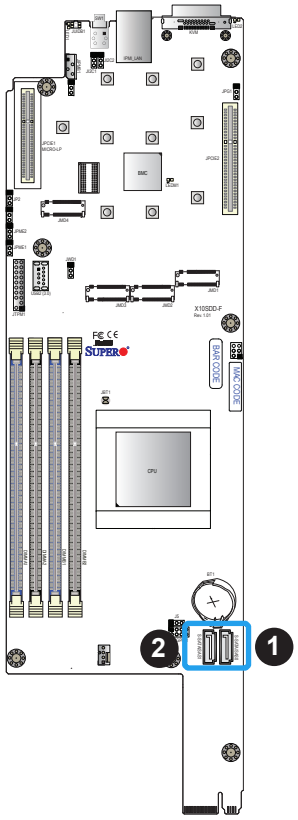
Your system will have two hard drives connected through the backplane of the motherboard. The default setting for the SATA/SAS connectors is for SATA drives, as controlled by the Intel Intel® Xeon® D, Pentium SoC. For SAS drives, an additional SAS storage card must be connected to the riser card on JPCIE2 and a cable must be connected from the storage card to the SATA/SAS connectors.

See the table below for pin definitions.

Note: Please remember to close the appropriate pins on the J5 jumper.

SATA 3.0 Port Pin Definitions

Pin#	Signal
1	Ground
2	SATA_TXP
3	SATA_TXN
4	Ground
5	SATA_RXN
6	SATA_RXP
7	Ground

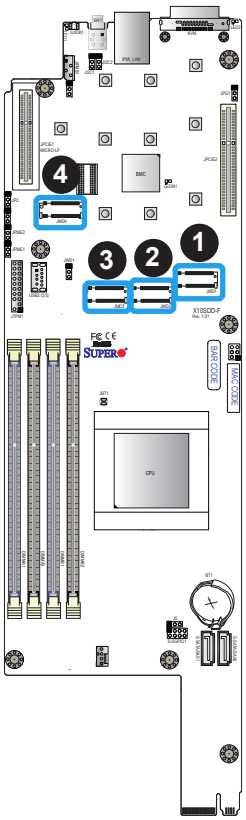


1. I-SATA0/SAS0
2. I-SATA1/SAS1

M.2/I-SATA Slots

The X10SDD-F motherboard contains four M.2 NGFF M-key/I-SATA connectors (JMD1-JMD4). M.2 was formerly Next Generation Form Factor (NGFF) and serves to replace mini PCI-E and mSATA. M.2 allows for a greater variety of card sizes, increased functionality, and spatial efficiency.

Each connector has a pin that detects whether a SATA card or an M.2 card is connected. The JMD1 and JMD2 connectors both support PCI-E 3.0 x4. The JMD3 connector supports PCI-E 2.0 x4. Lastly, the JMD4 connector supports PCI-E 2.0 x2.



1. JMD1 (M.2/I-SATA2 supports 2242, 2280 and 22110 module)
2. JMD2 (M.2/I-SATA3 supports 2242, 2280 and 22110 module)
3. JMD3 (M.2/I-SATA4 supports 2242, 2280 and 22110 module)
4. JMD4 (M.2/I-SATA2 supports 2242 module only)

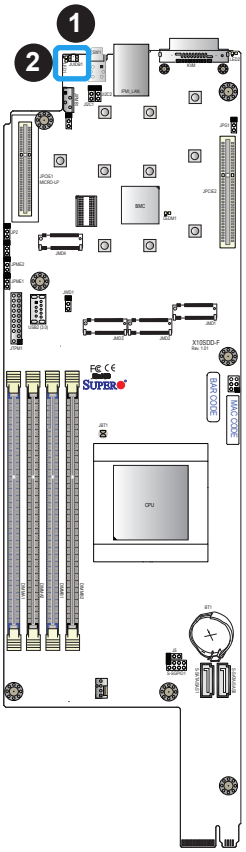
Unit Identifier Switch/UID LED Indicator

A Unit Identifier (UID) switch and an LED Indicator are located on the motherboard. The UID switch is located at JUIDB1, which is next to the SW1 connector on the back panel. The UID LED (LED1) is located next to the UID switch. When you press the UID switch, the UID LED will be turned on. Press the UID switch again to turn off the LED indicator. The UID Indicator provides easy identification of a system unit that may be in need of service.

Note: UID can also be triggered via IPMI on the motherboard. For more information on IPMI, please refer to the IPMI User's Guide posted on our website at http://www.supermicro.com.

UID Switch Pin Definitions	
Pin#	**Definition**
1	Ground
2	Ground
3	Button In
4	Button In

UID LED Pin Definitions	
Color	**Status**
Blue: On	Unit Identified



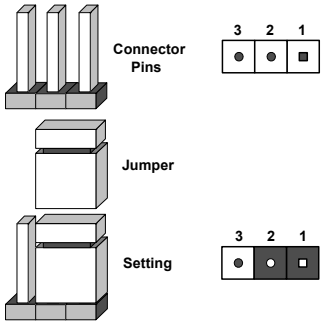
1. UID Switch
2. UID LED

2.6 Jumper Settings

How Jumpers Work

To modify the operation of the motherboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. See the diagram below for an example of jumping pins 1 and 2. Refer to the motherboard layout page for jumper locations.

Note: On two-pin jumpers, "Closed" means the jumper is on and "Open" means the jumper is off the pins.



VGA Enable/Disable

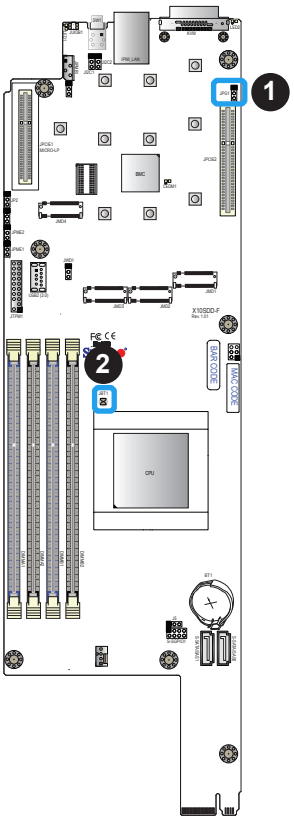
Jumper JPG1 allows the user to enable the onboard VGA connector. Refer to the table below for jumper settings.The default setting is Enabled.

VGA Enable/Disable Jumper Settings	
Jumper Setting	**Definition**
Pins 1-2	Enabled
Pins 2-3	Disabled

CMOS Clear

JBT1 is used to clear the CMOS. Instead of pins, this "jumper" consists of contact pads to prevent accidental clearing of the CMOS. To clear the CMOS, use a metal object such as a small screwdriver to touch both pads at the same time to short the connection.

Note: Be sure to completely shut down the system, and then short JBT1 to clear the CMOS.



1. VGA Enable/Disable
2. CMOS Clear

Watch Dog

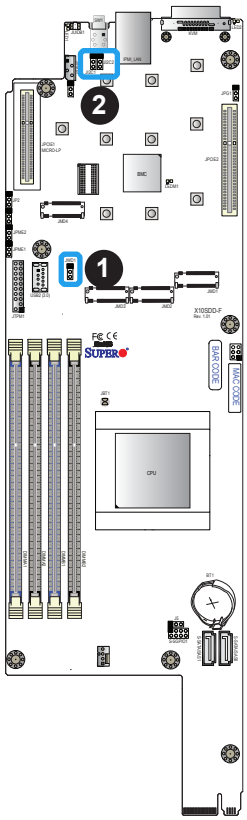
Watch Dog (JWD1) is a system monitor that can reboot the system when a software application hangs. Close pins 1-2 to reset the system if an application hangs. Close pins 2-3 to generate a non-maskable interrupt (NMI) signal for the application that hangs. Refer to the table below for jumper settings. The Watch Dog must also be enabled in the BIOS.

Watch Dog Jumper Settings	
Jumper Setting	**Definition**
Pins 1-2	Reset
Pins 2-3	NMI
Open	Disabled

SMBus to PCI Slots

Use jumpers JI²C1 and JI²C2 to connect the System Management Bus (I²C) to PCI-Express slots to improve PCI performance. These two jumpers should be set at the same time. Refer to the table below for jumper settings. The default setting is Disabled.

I²C to PCI-E Slots Jumper Settings	
Jumper Setting	**Definition**
Pins 1-2	Enabled
Pins 2-3	Disabled



1. Watch Dog
2. SMBus to PCI Slots

Management Engine (ME) Recovery

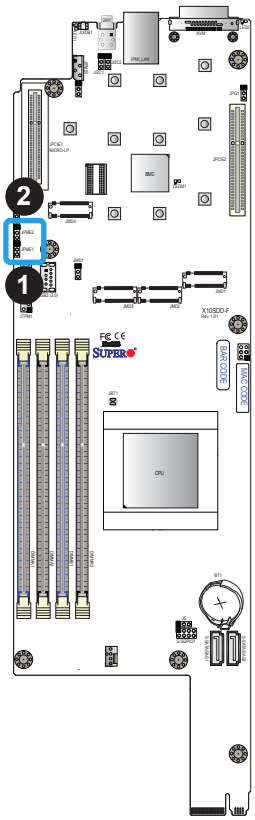
Use jumper JPME1 to select ME Firmware Recovery mode, which will limit resource allocation for essential system operation only in order to maintain normal power operation and management. In the single operation mode, online upgrade will be available via Recovery mode. See the table below for jumper settings.

Management Engine Recovery Pin Definitions	
Jumper Setting	**Definition**
Pins 1-2	Normal
Pins 2-3	ME Recovery

Manufacturer Mode Select

Close pin 2 and pin 3 of jumper JPME2 to bypass SPI flash security and force the system to operate in the manufacturer mode, which will allow the user to flash the system firmware from a host server for system setting modifications. Refer to the table below for jumper settings. The default setting is Normal.

Manufacturer Mode Jumper Settings	
Jumper Setting	**Definition**
Pins 1-2	Normal
Pins 2-3	Manufacturer Mode



1. ME Recovery
2. Manufacturer Mode

M.2 I²C Header

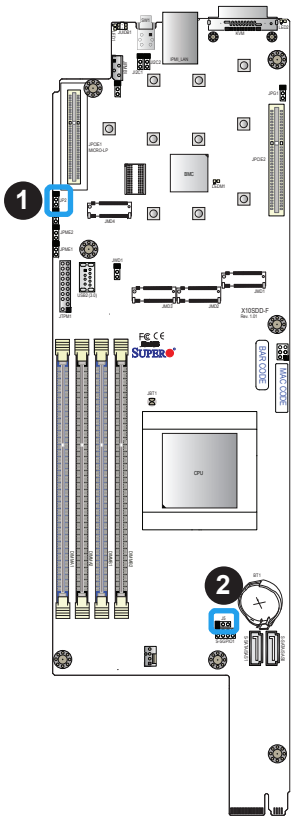
A System Management Bus header for the M.2 sockets is located at JP2. Connect the appropriate cable here to use the M.2 I²C connection on your system. Refer to the table below for pin definitions.

SMBus to M.2 Pin Definitions	
Jumper Setting	**Definition**
Pins 1-2	Enable
Pins 2-3	Disable

Hard Drive Backplane Selection

Use jumper J5 to set whether a SATA hard drive or a SAS hard drive is being used with your system. Refer to the table below for pin definitions.

Hard Drive Backplane Selection Pin Definitions	
Jumper Setting	**Definition**
Pins 1-2	SATA
Pins 2-3	SAS



1. SMBus to M.2
2. Hard Drive Backplane Selection

2.7 LED Indicators

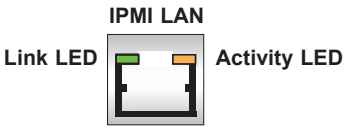
PWR Fail/Fan Fail/OH LED

A Power Fail/Fan Fail/Overheat LED is located LED2 on the motherboard. Refer to the table below for more information.

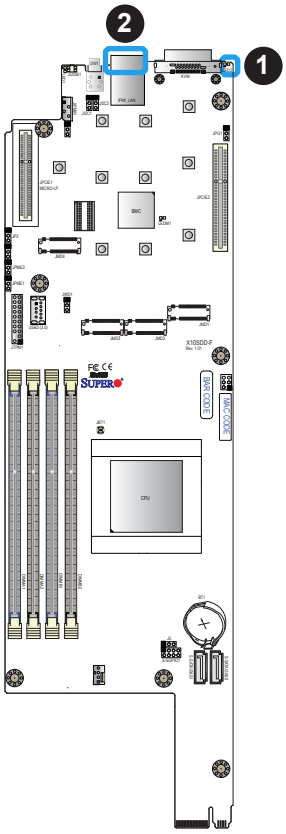
PWR Fail/Fan Fail/OH LED	
Color/State	**Definition**
Red: On	Power fail, fan fail, or overheating

IPMI-Dedicated LAN LEDs

An IPMI LAN is located on the I/O back panel. The amber LED on the right indicates activity, while the green LED on the left indicates the speed of the connection. Refer to the table below for more information.



IPMI LAN LEDs		
	Color/State	**Definition**
Link (left)	Green: Solid Amber: Solid	100 Mbps 1 Gbps
Activity (right)	Amber: Blinking	Active

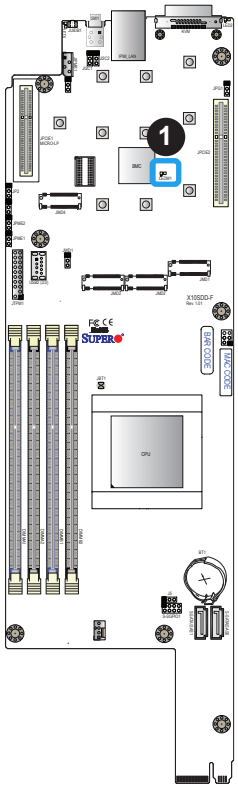


1. PWR Fail/Fan Fail/Overheat LED
2. IPMI-Dedicated LAN LED

BMC Heartbeat LED

A BMC Heartbeat LED is located at LEDM1 on the motherboard. When LEDM1 is blinking, the BMC is functioning normally. Refer to the table below for more information.

BMC LED Indicator	
LED Color	**Definition**
Green: Blinking	BMC: Normal



1. BMC Heartbeat LED

Chapter 3

Troubleshooting

3.1 Troubleshooting Procedures

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the 'Technical Support Procedures' and/or 'Returning Merchandise for Service' section(s) in this chapter. Always disconnect the AC power cord before adding, changing or installing any non hot-swap hardware components.

Before Power On

1. Make sure that the Standby is not on. (**Note:** If it is on, the onboard power is on. Be sure to unplug the power cable before installing or removing the components.)
2. Make sure that there are no short circuits between the motherboard and chassis.
3. Disconnect all ribbon/wire cables from the motherboard, including those for the keyboard and mouse. Also, be sure to remove all add-on cards.
4. Install a CPU and heatsink (be sure that it is fully seated) and then connect the chassis speaker and the power LED to the motherboard. Check all jumper settings as well.

No Power

1. Make sure that there are no short circuits between the motherboard and chassis.
2. Make sure that all jumpers are set to their default positions.
3. Check if the 115V/230V switch on the power supply is properly set.
4. Turn the power switch on and off to test the system.
5. The battery on your motherboard may be old. Check to make sure that it still supplies ~3VDC. If it does not, replace it with a new one.

No Video

1. If the power is on, but you have no video--in this case, you will need to remove all the add-on cards and cables first.

2. Remove all memory modules and turn on the system. (If the alarm is on, check the specs of memory modules, reset the memory or try a different one.)

Memory Errors

1. Make sure that the DIMM modules are properly installed and fully seated in the slots.
2. You should be using memory recommended by Supermicro (see Section 2-4). Also, it is recommended that you use the memory modules of the same type and speed for all DIMMs in the system. Do not use memory modules of different sizes, different speeds and different types on the same motherboard.
3. Check for bad DIMM modules or slots by swapping modules between slots to see if you can locate the faulty ones.
4. Check the switch of 115V/230V power supply.

Losing the System's Setup Configuration

1. Please be sure to use a high quality power supply. A poor quality power supply may cause the system to lose CMOS setup information. Refer to Section 1-5 for details on recommended power supplies.
2. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.
3. If the above steps do not fix the Setup Configuration problem, contact your vendor for repairs.

3.2 Technical Support Procedures

Before contacting Technical Support, please take the following steps. Also, note that as a motherboard manufacturer, we do not sell directly to end-users, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problem(s) with the specific system configuration that was sold to you.

1. Please review the 'Troubleshooting Procedures' and 'Frequently Asked Questions' (FAQs) sections in this chapter or see the FAQs on our website before contacting Technical Support.

2. BIOS upgrades can be downloaded from our website.

   Note: Not all BIOS can be flashed depending on the modifications to the boot block code.

3. If you still cannot resolve the problem, include the following information when contacting us for technical support:

- Motherboard model and PCB revision number
- BIOS release date/version (this can be seen on the initial display when your system first boots up)
- System configuration

An example of a Technical Support form is posted on our website.

Distributors: For immediate assistance, please have your account number ready when contacting our technical support department by e-mail.

3.3 Frequently Asked Questions

Question: What type of memory does my motherboard support?

Answer: The X10SDD-F motherboard supports DDR4 2133MHz ECC memory; up to 64GB of UDIMM memory or up to 128GB of RDIMM memory. See Section 2.4 for details on installing memory.

Question: How do I update my BIOS?

Answer: It is recommended that you **do not** upgrade your BIOS if you are not experiencing any problems with your system. Updated BIOS files are located on our website at http://www.supermicro.com/support/bios/. Please check our BIOS warning message and the information on how to update your BIOS on our web site. Select your motherboard model and download the BIOS ROM file to your computer. Also, check the current BIOS revision to make sure that it is newer than your BIOS before downloading. Please unzip the BIOS file onto a bootable device or a USB pen/thumb drive. To flash the BIOS, run the batch file named "flash.bat" with the new BIOS ROM file from your bootable device or USB pen/thumb drive. Use the following format:

F:\> flash.bat BIOS-ROM-filename.xxx <Enter>

Note: Always use the file named “flash.bat” to update the BIOS, and insert a space between "flash.bat" and the filename. The BIOS-ROM-filename will bear the motherboard name (i.e., X10SDD-F) and build version as the extension. For example, "X10SDD1.218". When completed, your system will automatically reboot.

When the BIOS flashing screen is completed, the system will reboot and will show “Press F1 or F2”. At this point, you will need to load the BIOS defaults. Press <F1> to go to the BIOS setup screen, and press <F3> to load the default settings. Next, press <F4> to save and exit. The system will then reboot.

Warning: Do not shut down or reset the system while updating the BIOS to prevent possible system boot failure!

Note: The SPI BIOS chip installed on this motherboard is not removable. To repair or replace a damaged BIOS chip, please send your motherboard to RMA at Supermicro for service.

3.4 Battery Removal and Installation

Battery Removal

To remove the onboard battery, follow the steps below:

1. Power off your system and unplug your power cable.
2. Locate the onboard battery as shown below.
3. Using a tool such as a pen or a small screwdriver, push the battery lock outwards to unlock it. Once unlocked, the battery will pop out from the holder.
4. Remove the battery.

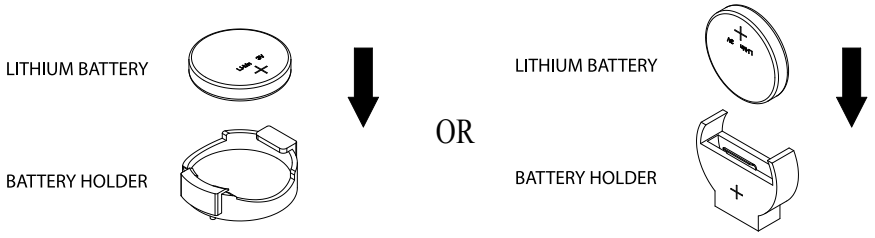
Proper Battery Disposal

Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

Battery Installation

1. To install an onboard battery, follow the steps 1 & 2 above and continue below:
2. Identify the battery's polarity. The positive (+) side should be facing up.
3. Insert the battery into the battery holder and push it down until you hear a click to ensure that the battery is securely locked.

Important: When replacing a battery, be sure to only replace it with the same type.



3.5 Returning Merchandise for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton and mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

For faster service, RMA authorizations may be requested online (http://www.supermicro.com/support/rma/).

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alteration, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.

Chapter 4

BIOS

4.1 Introduction

This chapter describes the AMIBIOS™ Setup utility for the X10SDD-F motherboard. The BIOS is stored on a chip and can be easily upgraded using a flash program.

Note: Due to periodic changes to the BIOS, some settings may have been added or deleted and might not yet be recorded in this manual. Please refer to the Manual Download area of our website for any changes to BIOS that may not be reflected in this manual.

Starting the Setup Utility

To enter the BIOS Setup Utility, hit the <Delete> key while the system is booting-up. (In most cases, the <Delete> key is used to invoke the BIOS setup screen. There are a few cases when other keys are used, such as <F1>, <F2>, etc.) Each main BIOS menu option is described in this manual.

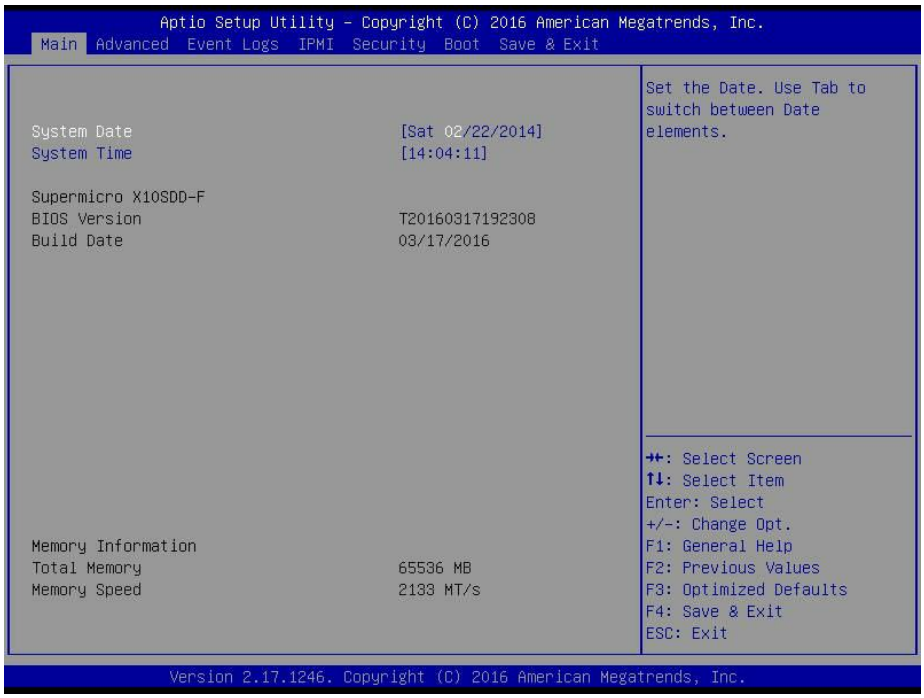
The Main BIOS screen has two main frames. The left frame displays all the options that can be configured. “Grayed-out” options cannot be configured. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it. (Note that BIOS has default text messages built in. We retain the option to include, omit, or change any of these text messages.) Settings printed in **Bold** are the default values.

A " ▶" indicates a submenu. Highlighting such an item and pressing the <Enter> key will open the list of settings within that submenu.

The BIOS setup utility uses a key-based navigation system called hot keys. Most of these hot keys (<F1>, <F2>, <F3>, <Enter>, <ESC>, <Arrow> keys, etc.) can be used at any time during the setup navigation process.

4.2 Main Setup

When you first enter the AMI BIOS setup utility, you will enter the Main setup screen. You can always return to the Main setup screen by selecting the Main tab on the top of the screen. The Main BIOS setup screen is shown below.The following Main menu items will be displayed:



System Date/System Time

Use this option to change the system date and time. Highlight *System Date* or *System Time* using the arrow keys. Enter new values using the keyboard. Press the <Tab> key or the arrow keys to move between fields. The date must be entered in MM/DD/YYYY format. The time is entered in HH:MM:SS format.

Note: The time is in the 24-hour format. For example, 5:30 P.M. appears as 17:30:00. The date's default value is 03/17/2016 after RTC reset.

Supermicro X10SDD-F

BIOS Version

This item displays the version of the BIOS ROM used in the system.

Build Date

This item displays the date when the version of the BIOS ROM used in the system was built.

Memory Information

Total Memory

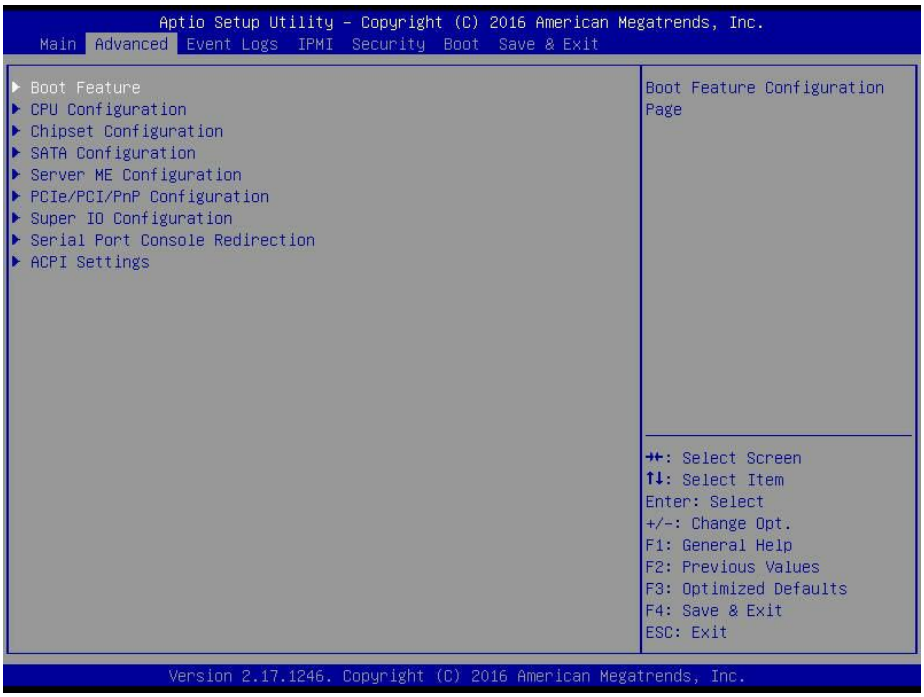
This item displays the total size of memory available in the system.

Memory Speed

This item displays the memory speed.

4.3 Advanced Setup Configurations

Use the arrow keys to select Boot Setup and press <Enter> to access the submenu items.



Warning: Take caution when changing the Advanced settings. An incorrect value, a very high DRAM frequency, or an incorrect DRAM timing setting may make the system unstable. When this occurs, revert to the manufacture default settings.

▶Boot Feature

Quiet Boot

Use this feature to select the screen display between the POST messages and the OEM logo upon bootup. Select Disabled to display the POST messages. Select Enabled to display the OEM logo instead of the normal POST messages. The options are Disabled and **Enabled**.

AddOn ROM Display Mode

Use this feature to set the display mode for the Option ROM. Select Keep Current to display the current AddOn ROM setting. Select Force BIOS to use the Option ROM display set by the system BIOS. The options are **Force BIOS** and Keep Current.

Bootup NumLock State

Use this feature to set the Power-on state for the <Numlock> key. The options are Off and **On**.

Wait For 'F1' If Error

Use this feature to force the system to wait until the 'F1' key is pressed if an error occurs. The options are Disabled and **Enabled**.

INT19 (Interrupt 19) Trap Response

Interrupt 19 is the software interrupt that handles the boot disk function. When this item is set to Immediate, the ROM BIOS of the host adaptors will "capture" Interrupt 19 at bootup immediately and allow the drives that are attached to these host adaptors to function as bootable disks. If this item is set to Postponed, the ROM BIOS of the host adaptors will not capture Interrupt 19 immediately and allow the drives attached to these adaptors to function as bootable devices at bootup. The options are **Immediate** and Postponed.

Re-try Boot

If this item is enabled, the BIOS will automatically reboot the system from a specified boot device after its initial boot failure. The options are **Disabled**, Legacy Boot, and EFI Boot.

▶Power Configuration

Watch Dog Function

If enabled, the Watch Dog Timer will allow the system to reset or generate NMI based on jumper settings when it is expired for more than 5 minutes. The options are **Disabled** and Enabled**.**

Power Button Function

This feature controls how the system shuts down when the power button is pressed. Select 4 Seconds Override for the user to power off the system after pressing and holding the power button for 4 seconds or longer. Select Instant Off to instantly power off the system as soon as the user presses the power button. The options are 4 Seconds Override and **Instant Off.**

Restore on AC Power Loss

Use this feature to set the power state after a power outage. Select Stay Off for the system power to remain off after a power loss. Select Power On for the system power to be turned on after a power loss. Select Last State to allow the system to resume its last power state before a power loss. The options are Power On, Stay Off, and **Last State**.

▶CPU Configuration

The following CPU information will display:

- Processor ID

- Processor Frequency
- Processor Max Ratio
- Processor Min Ratio
- Microcode Revision
- L1 Cache RAM
- L2 Cache RAM
- L3 Cache RAM
- CPU Version

Clock Spread Spectrum

If this feature is set to Enable, the BIOS utility will monitor the level of Electromagnetic Interference caused by the components and will attempt to reduce the interference whenever needed. The options are **Disable** and Enable.

Hyper-Threading (ALL)

Select Enabled to support Intel Hyper-threading Technology to enhance CPU performance. The options are **Disable** and Enable.

Cores Enabled

Set a numeric value to enable the number of cores. (Please refer to the Intel website for more information.) Enter **0** to enable all cores.

Monitor/Mwait

Select Enabled to enable the Monitor/MWait instructions. The Monitor instruction monitors a region of memory for writes, and MWait instructions instruct the CPU to stop until the monitored region begins to write. The options are Disable and **Enable**.

Execute Disable Bit (Available if supported by the OS & the CPU)

Select Enabled to enable the Execute-Disable Bit which will allow the processor to designate areas in the system memory where an application code can execute and where it cannot, thus preventing a worm or a virus from flooding illegal codes to overwhelm the processor or damage the system during an attack. The default is **Enable**. (Refer to the Intel® and Microsoft® websites for more information.)

PPIN Control

Select Unlock/Enable to use the Protected-Processor Inventory Number (PPIN) in the system. The options are **Unlock/Enable** and Unlock/Disable.

Hardware Prefetcher (Available when supported by the CPU)

If set to Enabled, the hardware prefetcher will prefetch streams of data and instructions from the main memory to the L2 cache to improve CPU performance. The options are Disable and **Enable**.

Adjacent Cache Line Prefetch (Available when supported by the CPU)

The CPU prefetches the cache line for 64 bytes if this feature is set to Disabled. The CPU prefetches both cache lines for 128 bytes as comprised if this feature is set to **Enable**.

DCU Streamer Prefetcher (Available when supported by the CPU)

Select Enabled to enable the DCU (Data Cache Unit) Streamer Prefetcher which will stream and prefetch data and send it to the Level 1 data cache to improve data processing and system performance. The options are Disable and **Enable**.

DCU IP Prefetcher (Available when supported by the CPU)

Select Enabled for DCU (Data Cache Unit) IP Prefetcher support, which will prefetch IP addresses to improve network connectivity and system performance. The options are Disable and **Enable**.

Direct Cache Access (DCA)

Select Enabled to use Intel's DCA (Direct Cache Access) Technology to improve data transfer efficiency. The options are Disable and **Enable**.

Intel® Virtualization Technology (Available when supported by the CPU)

Select Enabled to support Intel® Virtualization Technology, which will allow one platform to run multiple operating systems and applications in independent partitions, creating multiple "virtual" systems in one physical computer. The options are Disable and **Enable**.

Note: If a change is made to this setting, you will need to reboot the system for the change to take effect. Refer to Intel's website for detailed information.

▶Advanced Power Management Configuration

EIST (P-States)

EIST (Enhanced Intel SpeedStep Technology) allows the system to automatically adjust processor voltage and core frequency to reduce power consumption and heat dissipation. The options are Disable and **Enable.**

▶CPU P State Control

P State Domain

This feature allows the user to indicate the P-State domain for each logical process in the system. All processes indicate the same domain in the same package. The options are **ALL** and ONE.

P-State Coordination

This feature allows the user to change the P-State (Power-Performance State) coordination type. P-State is also known as "SpeedStep" for Intel processors. Select HW_ALL to change the P-State coordination type for hardware components only. Select SW_ALL to change the P-State coordination type for all software installed in the system. Select SW_ANY to change the P-State coordination type for a software program in the system. The options are **HW_All,** SW_ALL, and SW_ANY.

Energy Efficient P-State

Select Enable to support power-saving mode for P-State. The options are Disable and **Enable.**

Boot Performance Mode

This feature allows the user to select the performance state that the BIOS will set before the operating system handoff. The options are **Max Performance** and Max Efficient.

Turbo Mode

Select Enable for processor cores to run faster than the frequency specified by the manufacturer. The options are Disable and **Enable**.

▶CPU HWPM State Control

Enable CPU HWPM

Select Enable for better CPU energy performance. The options are **Disable**, HWPM NATIVE MODE, and HWPM OOB MODE.

Enable CPU Autonomous Cstate

Use this feature to enable CPU Autonomous C State, which converts HALT instructions to Mwait. The options are **Disable** and Enable.

▶CPU C State Control

CPU C State

Use this feature to enable the enhanced C State of the CPU. The options are Disable and **Enable**.

Package C State Limit

This feature allows the user to set the limit on the C State package register. The options are C0/C1 State, C2 State, C6 (Non Retention) State**,** and **C6 (Rentention) state.**

CPU C3 Report

Select Enabled to allow the BIOS to report the CPU C3 State (ACPI C2) to the operating system. During the CPU C3 State, the CPU clock generator is turned off. The options are **Disable** and Enable**.**

CPU C6 Report

Select Enabled to allow the BIOS to report the CPU C6 State (ACPI C3) to the operating system. During the CPU C6 State, the power to all cache is turned off. The options are Disable and **Enable**.

Enhanced Halt State (C1E)

Select Enabled to use Enhanced Halt-State technology, which will significantly reduce the CPU's power consumption by reducing the CPU's clock cycle and voltage during a Halt-state. The options are Disable and **Enable**.

▶CPU T State Control

ACPI (Advanced Configuration Power Interface) T-States

Select Enable to support CPU throttling by the operating system to reduce power consumption. The options are **Disable** and Enable.

▶CPU Advanced PM Turning

▶Energy Perf BIAS

Energy Performance Tuning

When enabled, this item selects whether the BIOS or Operating System can turn on the energy performance bias tuning. The options are Disable and **Enable**.

If the above is set to Disable, Energy Performance BIAS Setting will display:

Energy Performance BIAS Setting

This feature allows balancing Power Efficiency vs Performance. This will override whatever setting is in the Operating System. The options are Performance, **Balanced Performance**, Balanced Power, and Power.

Power/Performance Switch

This feature allows dynamic switching between Power and Performance power efficiency. The options are Disable and **Enable**.

Workload Configuration

This feature allows for optimization of workload. Balanced is recommended. The options are **Balanced** and I/O Sensitive.

▶Program PowerCTL_MSR

PKG C-state Lat. Neg.

Use this feature to indicate whether latency should be negotiated with PCH for packaging C-States. The options are Disable and **Enable**.

SAPM Control

This feature indicates whether the PCU should control the System Agent PM using its power-performance tuning algorithm. The options are Disable and **Enable**.

Energy Efficient Turbo

Use this feature to enable energy efficient turbo mode. The options are Disable and **Enable**.

▶DRAM RAPL Configuration

Override BW_LIMIT_TF

Use this feature to allow custom tuning of BW_LIMIT_TF when DRAM RAPL is enabled. Press the "+" or "-" key to change the value. The default value is **1**.

DRAM RAPL Extended Range

Use this feature to set the DRAM Running Average Power Limit (RAPL) Extended Range. The options are Disable and **Enable**.

▶Chipset Configuration

Warning: Setting the wrong values in the following features may cause the system to malfunction.

▶North Bridge

This feature allows the user to configure the following North Bridge settings.

▶IIO Configuration

EV DFX (Device Function On-Hide) Features

When this feature is set to Enable, the EV_DFX Lock Bits that are located on a processor will always remain clear during electric tuning. The options are **Disable** and Enable.

▶IIO1 Configuration

CPU1 MICRO-LP PCI-E 3.0 X8

This feature allows the user to select PCI-E support for the device installed on the Micro LP slot. The options are Auto, Gen 1 (2.5 GT/s), Gen 2 (5 GT/s), and **Gen 3 (8 GT/s)**.

CPU1 SLOT1 PCI-E 3.0 X8

This feature allows the user to select PCI-E support for the device installed on SLOT1. The options are Auto, Gen 1 (2.5 GT/s), Gen 2 (5 GT/s), and **Gen 3 (8 GT/s)**.

▶IOAT (Intel® IO Acceleration) Configuration

Enable IOAT

Select Enable to enable Intel I/OAT (I/O Acceleration Technology) support, which significantly reduces CPU overhead by leveraging CPU architectural improvements and freeing the system resource for other tasks. The options are Disable and **Enable**.

No Snoop

Select Enable to support no-snoop mode for each CB device. The options are **Disable** and Enable.

▶Intel® VT for Directed I/O (VT-d)

Intel® VT for Directed I/O (VT-d)

Select Enable to use Intel® Virtualization Technology support for Direct I/O VT-d support by reporting the I/O device assignments to the VMM (Virtual Machine Monitor) through the DMAR ACPI Tables. This feature offers fully-protected I/O resource sharing across Intel platforms, providing greater reliability, security and availability in networking and data-sharing. The options are **Enable** and Disable.

ACS Control

Use this feature to program Access Control Services (ACS) to the PCI-E Root Port Bridges. The options are **Enable** and Disable.

Interrupt Remapping

Select Enable for Interrupt Remapping support to enhance system performance. The options are **Enable** and Disable.

▶Memory Configuration

Enforce POR

Select Enable to enforce POR restrictions on DDR4 frequency and voltage programming. The options are **Enabled** and Disabled.

Memory Frequency

Use this feature to set the maximum memory frequency for onboard memory modules. The options are **Auto**, 1333, 1400, 1600, 1800, 1867, 2000, 2133, 2200, 2400, 2600, 2667, 2800, 2993, 3000, 3200, and Reserved (Do not select Reserved).

Data Scrambling

Select Enabled to enable data scrambling to enhance system performance and data integrity. The options are **Auto**, Disabled, and Enabled.

DRAM RAPL Baseline

Use this feature to set the run-time power-limit baseline for DRAM modules. The options are Disable, DRAM RAPL Mode 0, and **DRAM RAPL Mode 1**.

Set Throttling Mode

Throttling improves reliability and reduces power consumption in the processor via automatic voltage control during processor idle states. The options are Disabled and **CLTT** (Closed Loop Thermal Throttling).

A7 Mode

Select Enabled to support the A7 (Addressing) mode to improve memory performance. The options are Disable and **Enable**.

▶DIMM Information

This item displays the status of a DIMM module specified by the user.

- DIMMA1
- DIMMB1
- DIMMA2
- DIMMB2

▶Memory RAS (Reliability Availability Serviceability) Configuration

Use this submenu to configure the following Memory RAS settings.

Patrol Scrub

Patrol Scrubbing is a process that allows the CPU to correct correctable memory errors detected on a memory module and send the correction to the requestor (the original source). When this item is set to Enabled, the IO hub will read and write back one cache line every 16K cycles, if there is no delay caused by internal processing. By using this method, roughly 64 GB of memory behind the IO hub will be scrubbed every day. The options are Disable and **Enable**.

Patrol Scrub Interval

This feature allows you to decide how many hours the system should wait before the next complete patrol scrub is performed. Use the keyboard to enter a value from 0-24. The default setting is **24**.

Demand Scrub

Demand Scrubbing is a process that allows the CPU to correct correctable memory errors found on a memory module. When the CPU or I/O issues a demand-read command, and the read data from memory turns out to be a correctable error, the error is corrected and sent to the requestor (the original source). Memory is updated as well. Select Enable to use Demand Scrubbing for ECC memory correction. The options are Disable and **Enable**.

Device Tagging

Select Enable to support device tagging that generates stuck bits or hard errors. The options are **Disable** and Enable.

▶South Bridge

The following South Bridge information will display:

- USB Configuration
- USB Module Version
- USB Devices

Legacy USB Support

This feature enables support for legacy USB devices. Select Auto to disable legacy support if USB devices are not present. Select Disable to have USB devices available only for EFI applications. The options are **Enabled,** Disabled, and Auto.

XHCI Hand-Off

This item is a work-around solution for operating systems that do not support XHCI (Extensible Host Controller Interface) hand-off. The XHCI ownership change should be claimed by the XHCI driver. The options are **Enabled** and Disabled.

EHCI Hand-Off

This item is for the Operating Systems that do not support Enhanced Host Controller Interface (EHCI) hand-off. When this item is enabled, EHCI ownership change will be claimed by the EHCI driver. The options are **Disabled** and Enabled.

Port 60/64 Emulation

This feature enables or disables I/O port 60h/64h emulation support. This should be enabled for complete USB keyboard legacy support for non-USB-aware Operating Systems. The options are Disabled and **Enabled**.

USB 3.0 Support

Select Enabled for onboard USB 3.0 support. The options are **Enabled** and Disabled.

EHCI1

Select Enabled to enable EHCI (Enhanced Host Controller Interface) support on USB 2.0 connector #1 (at least one USB 2.0 connector should be enabled for EHCI support). The options are Disabled and **Enabled**.

EHCI2

Select Enabled to enable EHCI (Enhanced Host Controller Interface) support on USB 2.0 connector #2 (at least one USB 2.0 connector should be enabled for EHCI support). The options are Disabled and **Enabled**.

XHCI Pre-Boot Driver

Select Enabled to enable XHCI (Extensible Host Controller Interface) support on a pre-boot drive specified by the user. The options are Enabled and **Disabled**.

▶SATA Configuration

When this submenu is selected, the AMI BIOS automatically detects the presence of the SATA devices that are supported by the Intel PCH chip and displays the following items:

SATA Controller

This item enables or disables the onboard SATA controller supported by the Intel PCH chip. The options are **Enabled** and Disabled.

Configure SATA as

Select IDE to configure a SATA drive specified by the user as an IDE drive. Select AHCI to configure a SATA drive specified by the user as an AHCI drive. Select RAID to configure a SATA drive specified by the user as a RAID drive. The options are IDE, **AHCI**, and RAID.

If the item above "Configure SATA as" is set to AHCI, the following items will display:

SATA Frozen

Use this item to enable the HDD Security Frozen Mode. The options are **Disabled** and Enabled.

SATA AHCI LPM

Use this feature to enable the Link Power Management for SATA AHCI. The options are Disabled and **Enabled**.

Support Aggressive Link Power Mgmt

When this item is set to Enabled, the SATA AHCI controller manages the power usage of the SATA link. The controller will put the link in a low power mode during extended periods of I/O inactivity, and will return the link to an active state when I/O activity resumes. The options are **Enabled** and Disabled.

SATA Port 0 ~ Port 5

This item displays the information detected on the installed SATA drive on the particular SATA port.

- Model number of drive and capacity
- Software Preserve Support

Port 0 ~ Port 5 Hot Plug

Set this item to Enabled for hot-plugging support, which will allow the user to replace a SATA drive without shutting down the system. The options are **Enabled** and Disabled.

Port 0 ~ Port 5 Spin Up Device

On an edge detect from 0 to 1, set this item to allow the PCH to initialize the device. The options are Enabled and **Disabled**.

Port 0 ~ Port 5 SATA Device Type

Use this item to specify if the SATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are **Hard Disk Drive** and Solid State Drive.

▶Server ME Configuration

The following Server ME Configuration information will display:

- Operational Firmware Version
- ME Firmware Type
- Recovery Firmware Version
- ME Firmware Features
- ME Firmware Status #1
- ME Firmware Status #2
  - Current state
  - Error Code

▶PCIe/PCI/PnP Configuration

The following information will display:

- PCI Bus Driver Version
- PCI Devices Common Settings:

PCI PERR/SERR Support

Select Enabled to allow a PCI device to generate a PERR/SERR number for a PCI Bus Signal Error Event. The options are **Disabled** and Enabled.

Above 4G Decoding (Available if the system supports 64-bit PCI decoding)

Select Enabled to decode a PCI device that supports 64-bit in the space above 4G Address. The options are **Disabled** and Enabled.

SR-IOV Support

Use this feature to enable or disable Single Root IO Virtualization Support. The options are **Disabled** and Enabled.

Maximum Payload

Use this feature to select the setting for the PCI Express maximum payload size. The options are **Auto**, 128 Bytes, 256 Bytes, 512 Bytes, 1024 Bytes, 2048 Bytes, and 4096 Bytes.

Relaxed Ordering

Select Enable to enable Relaxed Ordering support which will allow certain transactions to violate the strict-ordering rules of PCI bus for a transaction to be completed prior to other transactions that have already been enqueued. The options are **Disabled** and Enabled.

Extended Tag

Use this item to allow a device to use the 8-bit tag field as a requester. The options are **Disabled** and Enabled.

ARI Forwarding

When this feature is enabled, the Downstream Port disables its traditional device number to 0 when turning Type1 Configuration Request into a Type0 Configuration Request. The default value is **Disabled**.

CPU1 MICRO-LP PCI-E 3.0 X8 OPROM

Use this feature to select which firmware type to be loaded for the add-on card in this slot. The options are Disabled, **Legacy**, and EFI.

CPU1 SLOT1 PCI-E 3.0 X8 OPROM

Use this feature to select which firmware type to be loaded for the add-on card in this slot. The options are Disabled, **Legacy**, and EFI.

JMD1 M.2 PCI-E 3.0 X4 OPROM

Use this feature to select which firmware type to be loaded for the add-on card in this slot. The options are Disabled, **Legacy**, and EFI.

JMD2 M.2 PCI-E 3.0 X4 OPROM

Use this feature to select which firmware type to be loaded for the add-on card in this slot. The options are Disabled, **Legacy**, and EFI.

JMD3 M.2 PCI-E 2.0 X4 OPROM

Use this feature to select which firmware type to be loaded for the add-on card in this slot. The options are Disabled, **Legacy**, and EFI.

JMD4 M.2 PCI-E 2.0 X2 OPROM

Use this feature to select which firmware type to be loaded for the add-on card in this slot. The options are Disabled, **Legacy**, and EFI.

Onboard Video Option ROM

Use this item to select the Onboard Video Option ROM type. The options are Disabled, **Legacy,** and EFI.

VGA Priority

This feature allows the user to select the graphics adapter to be used as the primary boot device. The options are **Onboard** and Offboard.

Network Stack

Select Enabled to enable PXE (Preboot Execution Environment) or UEFI (Unified Extensible Firmware Interface) for network stack support. The options are **Disabled** and Enabled.

If the item above set to Enabled, the four items below will become available for configuration:

IPv4 PXE Support

Select Enabled to enable IPv4 PXE boot support. The options are Disabled and **Enabled**.

IPv6 PXE Support

Select Enabled to enable IPv6 PXE boot support. The options are **Disabled** and Enabled.

PXE boot wait time

Use this option to specify the wait time to press the ESC key to abort the PXE boot. Press "+" or "-" on your keyboard to change the value. The default setting is **0**.

Media detect count

Use this option to specify the number of times media will be checked. Press "+" or "-" on your keyboard to change the value. The default setting is **1**.

▶Super IO Configuration

The following Super IO information will display:

- Super IO Chip 2400

Super IO Chip Logical Device(s) Configuration

▶Serial Port 1

Serial Port 1 Configuration

This submenu allows the user the configure settings of Serial Port 1.

Serial Port 1

Select Enabled to enable the selected onboard serial port. The options are Disabled and **Enabled**.

Device Settings

This item displays the status of a serial part specified by the user.

Change Port 1 Settings

This feature specifies the base I/O port address and the Interrupt Request address of a serial port specified by the user. Select Auto to allow the BIOS to automatically assign the base I/O and IRQ address.

The options for Serial Port 1 are **Auto**, (IO=3F8h; IRQ=4; DMA), (IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; DMA), (IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; DMA), (IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; DMA), and (IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12; DMA).

▶Serial Port Console Redirection

COM1 Console Redirection

Select Enabled to enable console redirection support for a serial port specified by the user. The options are **Disabled** and Enabled.

If the item above set to Enabled, the following items will become available for user's configuration:

▶COM1 Console Redirection Settings

This feature allows the user to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

Terminal Type

This feature allows the user to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, **VT100+**, VT-UTF8, and ANSI.

Bits Per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and **115200** (bits per second).

Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 Bits and **8 Bits**.

Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark, and Space.

Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and 2.

Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are Disabled and **Enabled.**

Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

Legacy OS Redirection Resolution

Use this feature to select the number of rows and columns used in Console Redirection for legacy OS support. The options are 80x24 and **80x25**.

Putty KeyPad

This feature selects the settings for Function Keys and KeyPad used for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SC0, ESCN, and VT400.

Redirection After BIOS POST

Use this feature to enable or disable legacy console redirection after BIOS POST. When set to Bootloader, legacy console redirection is disabled before booting the OS. When set to Always Enable, legacy console redirection remains enabled when booting the OS. The options are **Always Enable** and Bootloader.

SOL Console Redirection

Select Enabled to use the SOL port for Console Redirection. The options are Disabled and **Enabled.**

If the item above set to Enabled, the following items will become available for configuration:

▶SOL Console Redirection Settings

Use this feature to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are ANSI, VT100, **VT100+**, and VT-UTF8.

Bits Per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and **115200** (bits per second).

Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 Bits and **8 Bits**.

Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark and Space.

Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and 2.

Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are Disabled and **Enabled.**

Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

Legacy OS Redirection Resolution

Use this feature to select the number of rows and columns used in Console Redirection for legacy OS support. The options are 80x24 and **80x25**.

Putty KeyPad

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SCO, ESCN, and VT400.

Redirection After BIOS POST

Use this feature to enable or disable legacy Console Redirection after BIOS POST. When set to Bootloader, legacy Console Redirection is disabled before booting the OS. When set to Always Enable, legacy Console Redirection remains enabled when booting the OS. The options are **Always Enable** and Bootloader.

EMS (Emergency Management Services) Console Redirection

Select Enabled to use a COM port selected by the user for EMS Console Redirection. The options are Enabled and **Disabled.**

If the item above set to Enabled, the following items will become available for configuration:

▶EMS Console Redirection Settings

This feature allows the user to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

Out-of-Band Mgmt Port

The feature selects a serial port in a client server to be used by the Microsoft Windows Emergency Management Services (EMS) to communicate with a remote host server. The options are **COM1** and SOL/COM2.

Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII character set. Select VT100+ to add color and function key support. Select ANSI to use the extended ASCII character set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, VT100+, **VT-UTF8**, and ANSI.

Bits Per Second

This item sets the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 57600, and **115200** (bits per second).

Flow Control

Use this item to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None,** Hardware RTS/CTS, and Software Xon/Xoff.

Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 Bits and **8 Bits**.

Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark and Space.

Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and 2.

▶ACPI Settings

WHEA Support

Select Enabled to support the Windows Hardware Error Architecture (WHEA) platform and provide a common infrastructure for the system to handle hardware errors within the Windows OS environment to reduce system crashes and to enhance system recovery and health monitoring. The options are Disabled and **Enabled**.

High Precision Event Timer

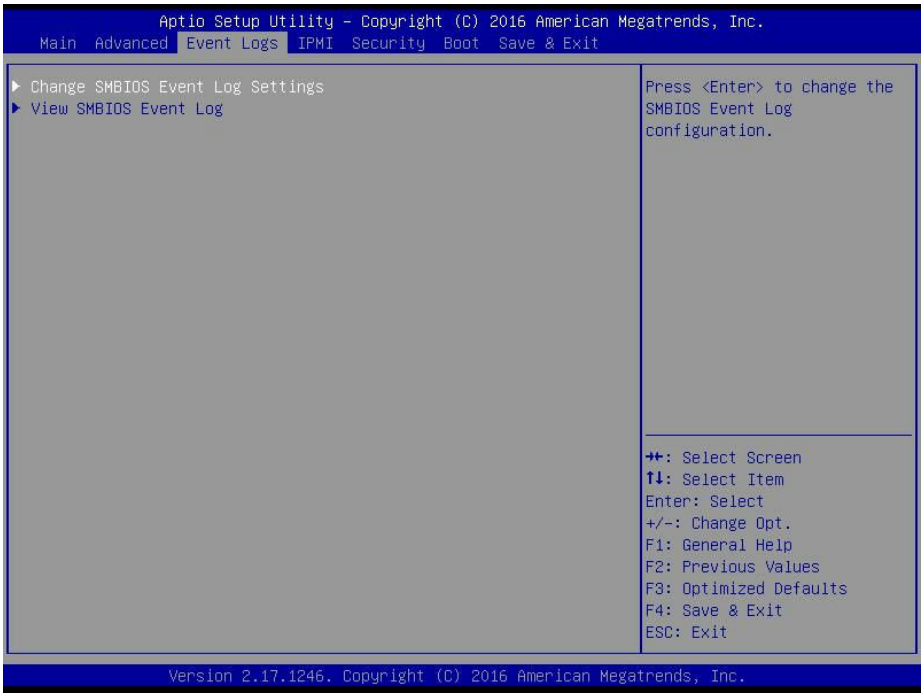
Select Enabled to activate the High Performance Event Timer (HPET) that produces periodic interrupts at a much higher frequency than a Real-time Clock (RTC) does in synchronizing multimedia streams, providing smooth playback and reducing the dependency on other timestamp calculation devices, such as an x86 RDTSC Instruction embedded in the CPU. The High Performance Event Timer is used to replace the 8254 Programmable Interval Timer. The options are Disabled and **Enabled**.

PCI AER Support

Select Enabed to enable the ACPI OS to manage PCI Advanced Error Reporting. The options are **Disabled** and Enabled.

4.4 Event Logs

Use this feature to configure Event Log settings.



▶Change SMBIOS Event Log Settings

Enabling/Disabling Options

SMBIOS Event Log

Change this item to enable or disable all features of the SMBIOS Event Logging during system boot. The options are **Enabled** and Disabled.

Runtime Error Logging Support

Select Enable to support Runtime Error Logging. The options are Enable and **Disable**. If this item is set to Enable, the following item will be available for configuration:

Memory Corrected Error Enabling

Select Enable for the BIOS to correct a memory error if it is correctable. The options are Disable and **Enable**.

Memory Corr. Error Threshold

Use this item to enter the threshold value for correctable memory errors. The default setting is **10**.

PCI-Ex (PCI-Express) Error Enable

Select Yes for the BIOS to correct errors occurred in the PCI-E slots. The options are Yes and **No**.

Erasing Settings

Erase Event Log

If No is selected, data stored in the event log will not be erased. Select Yes, Next Reset, data in the event log will be erased upon next system reboot. Select Yes, Every Reset, data in the event log will be erased upon every system reboot. The options are **No**, Yes, Next reset, and Yes, Every reset.

When Log is Full

Select Erase Immediately for all messages to be automatically erased from the event log when the event log memory is full. The options are **Do Nothing** and Erase Immediately.

SMBIOS Event Long Standard Settings

Log System Boot Event

This option toggles the System Boot Event logging to enabled or disabled. The options are **Disabled** and Enabled.

MECI

The Multiple Event Count Increment (MECI) counter counts the number of occurences that a duplicate event must happen before the MECI counter is incremented. This is a numeric value. The default value is **1**.

METW

The Multiple Event Time Window (METW) defines number of minutes must pass between duplicate log events before MECI is incremented. This is in minutes, from 0 to 99. The default value is **60**.

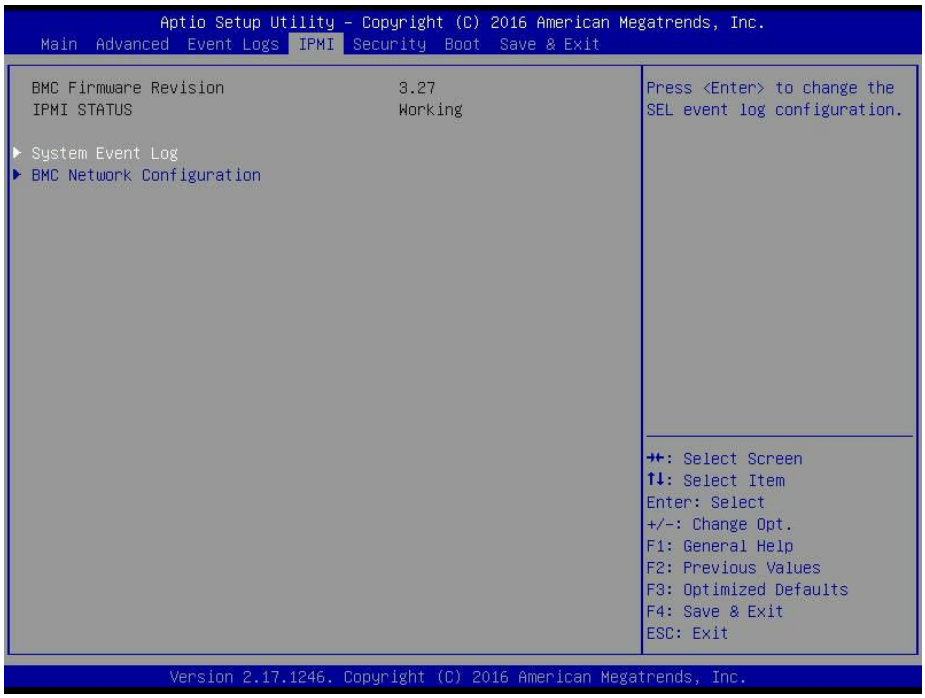
Note: After making changes on a setting, be sure to reboot the system for the changes to take effect.

▶View SMBIOS Event Log

This section displays the contents of the SMBIOS Event Log.

4.5 IPMI

Use this feature to configure Intelligent Platform Management Interface (IPMI) settings.



BMC Firmware Revision

This item indicates the IPMI firmware revision used in your system.

IPMI Status (Baseboard Management Controller)

This item indicates the status of the IPMI firmware installed in your system.

▶System Event Log

Enabling/Disabling Options

SEL Components

Select Enabled for all system event logging at bootup. The options are Disabled and **Enabled**.

Erasing Settings

Erase SEL

Select Yes, On next reset to erase all system event logs upon next system reboot. Select Yes, On every reset to erase all system event logs upon each system reboot. Select No to keep all system event logs after each system reboot. The options are **No,** Yes, On next reset, and Yes, On every reset.

When SEL is Full

This feature allows the user to decide what the BIOS should do when the system event log is full. Select Erase Immediately to erase all events in the log when the system event log is full. The options are **Do Nothing** and Erase Immediately.

Note: After making changes on a setting, be sure to reboot the system for the changes to take effect.

▶BMC Network Configuration

BMC Network Configuration

IPMI LAN Selection

This item displays the IPMI LAN setting. The default setting is **Failover**.

IPMI Network Link Status

This item displays the IPMI Network Link status. The default setting is **Dedicated LAN**.

Update IPMI LAN Configuration

Select Yes for the BIOS to implement all IP/MAC address changes at the next system boot. The options are **No** and Yes.

If the item above set to Yes, the following item will become available for user's configuration:

Configuration Address Source

This feature allows the user to select the source of the IP address for this computer. If Static is selected, you will need to know the IP address of this computer and enter it to the system manually in the field. If DHCP is selected, the BIOS will search for a DHCP (Dynamic Host Configuration Protocol) server in the network that is attached to and request the next available IP address for this computer. The options are **DHCP** and Static.

Configuration Address Source

This item displays the current configuration address for this computer.

Station IP Address

This item displays the Station IP address for this computer. This should be in decimal and in dotted quad form (i.e., 192.168.10.253).

Subnet Mask

This item displays the sub-network that this computer belongs to. The value of each three-digit number separated by dots should not exceed 255.

Station MAC Address

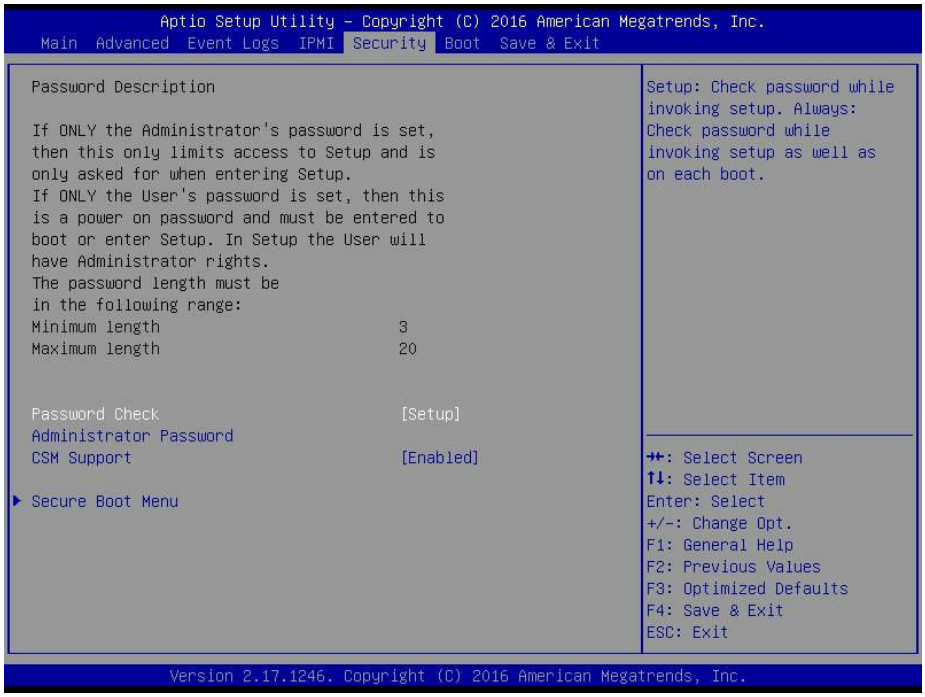
This item displays the Station MAC address for this computer. Mac addresses are 6 two-digit hexadecimal numbers.

Gateway IP Address

This item displays the Gateway IP address for this computer. This should be in decimal and in dotted quad form (i.e., 172.31.0.1).

4.6 Security

This menu allows the user to configure the following security settings for the system.



Password Check

Select Setup for the system to check for a password at Setup. Select Always for the system to check for a password at bootup or upon entering the BIOS Setup utility. The options are **Setup** and Always.

Administrator Password

Press Enter to create a new, or change an existing Administrator password.

CSM Support

Select Enabled to support the EFI Compatibility Support Module (CSM), which provides compatibility support for traditional legacy BIOS for system boot. The options are **Enabled** and Disabled.

▶Secure Boot Menu

This section displays the contents of the following secure boot features:

- System Mode
- Secure Boot
- Vendor Keys

Secure Boot

Use this item to enable secure boot. The options are **Disabled** and Enabled.

Secure Boot Mode

Use this item to select the secure boot mode. The options are Standard and **Custom**.

▶Key Management

This submenu allows the user to configure the following Key Management settings.

Provision Factory Default Keys

Select Enabled to install the default Secure-Boot keys set by the manufacturer. The options are **Disabled** and Enabled.

▶Enroll All Factory Default Keys

Select Yes to install all default secure keys set by the manufacturer. The options are **Yes** and No.

Save All Secure Boot Variables

This feature allows the user to decide if all secure boot variables should be saved.

▶Platform Key (PK)

This feature allows the user to configure the settings of the platform keys.

Set New Key

Select Yes to load the new platform keys (PK) from the manufacturer's defaults. Select No to load the platform keys from a file. The options are **Yes** and No.

▶Key Exchange Key

Set New Key

Select Yes to load the KEK from the manufacturer's defaults. Select No to load the KEK from a file. The options are Yes and No.

Append Key

Select Yes to add the KEK from the manufacturer's defaults list to the existing KEK. Select No to load the KEK from a file. The options are Yes and No.

▶Authorized Signatures

Set New Key

Select Yes to load the database from the manufacturer's defaults. Select No to load the DB from a file. The options are Yes and No.

Append Key

Select Yes to add the database from the manufacturer's defaults to the existing DB. Select No to load the DB from a file. The options are Yes and No.

▶Forbiden Signatures

Set New Key

Select Yes to load the DBX from the manufacturer's defaults. Select No to load the DBX from a file. The options are Yes and No.

Append Key

Select Yes to add the DBX from the manufacturer's defaults to the existing DBX. Select No to load the DBX from a file. The options are Yes and No.

▶Authorized TimeStamps

Set New Key

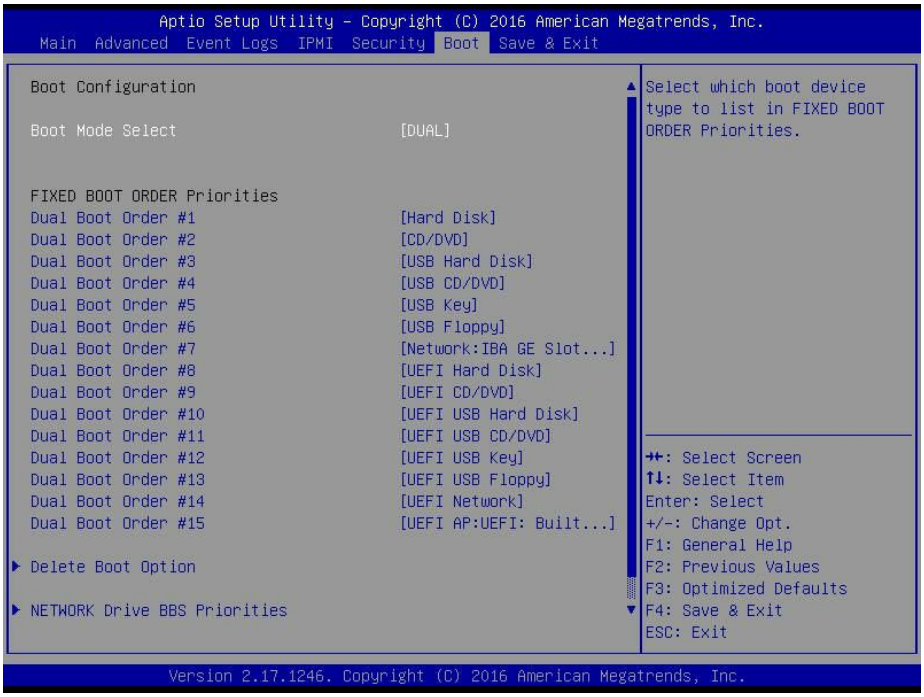
Select Yes to load the DBT from the manufacturer's defaults. Select No to load the DBT from a file. The options are Yes and No.

Append Key

Select Yes to add the DBT from the manufacturer's defaults list to the existing DBT. Select No to load the DBT from a file. The options are Yes and No.

4.7 Boot

Use this feature to configure Boot Settings:



Boot Mode Select

Use this item to select the type of device that the system is going to boot from. The options are Legacy, UEFI, and **Dual.** The default setting is **Dual**.

Fixed Boot Order Priorities

This option prioritizes the order of bootable devices that the system to boot from. Press <Enter> on each entry from top to bottom to select devices.

If the item above set to Legacy, UEFI/Dual the following items will be displayed:

- Legacy/UEFI/Dual Boot Order #1
- Legacy/UEFI/Dual Boot Order #2
- Legacy/UEFI/Dual Boot Order #3
- Legacy/UEFI/Dual Boot Order #4
- Legacy/UEFI/Dual Boot Order #5
- Legacy/UEFI/Dual Boot Order #6
- Legacy/UEFI/Dual Boot Order #7

- Legacy/UEFI/Dual Boot Order #8
- Legacy/UEFI/Dual Boot Order #9
- Legacy/UEFI/Dual Boot Order #10
- Legacy/UEFI/Dual Boot Order #11
- Legacy/UEFI/Dual Boot Order #12
- Legacy/UEFI/Dual Boot Order #13
- Legacy/UEFI/Dual Boot Order #14
- Legacy/UEFI/Dual Boot Order #15

▶Delete Boot Option

Use this feature to remove a pre-defined boot device from which the system will boot during startup.

The settings are [any pre-defined boot device].

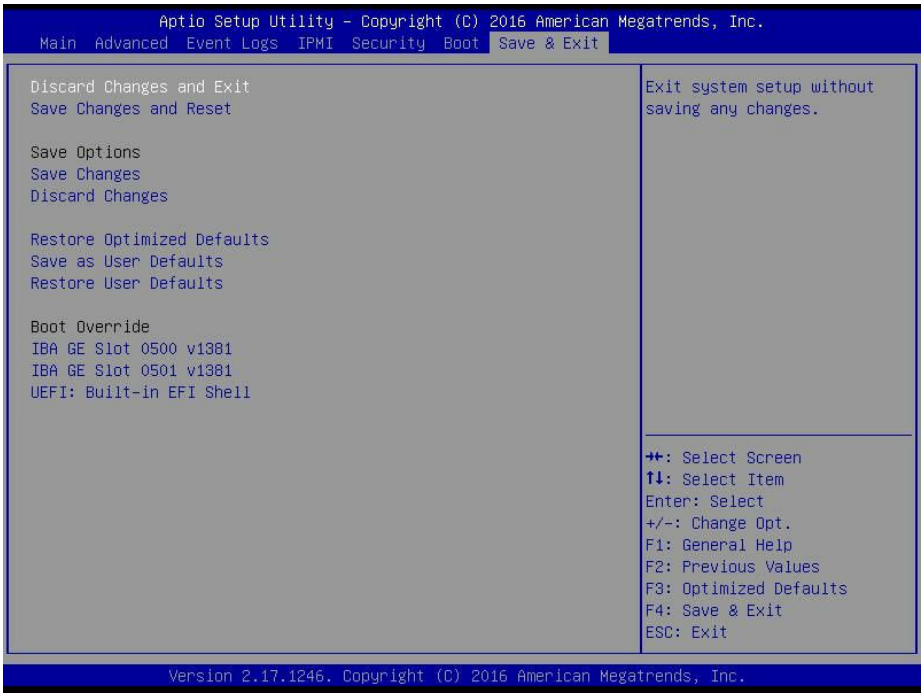
▶UEFI Application Boot Priorities

This feature allows the user to specify which UEFI devices are boot devices.

- UEFI Boot Order #1

4.8 Save & Exit

Select the Exit tab from the BIOS setup utility screen to enter the Exit BIOS Setup screen.



Discard Changes and Exit

Select this option to quit the BIOS Setup without making any permanent changes to the system configuration, and reboot the computer. Select Discard Changes and Exit from the Exit menu and press <Enter>.

Save Changes and Reset

When you have completed the system configuration changes, select this option to leave the BIOS setup utility and reboot the computer, so the new system configuration parameters can take effect. Select Save Changes and Exit from the Exit menu and press <Enter>.

Save Changes

After completing the system configuration changes, select this option to save the changes you have made. This will not reset (reboot) the system.

Discard Changes

Select this option and press <Enter> to discard all the changes and return to the AMI BIOS utility Program.

Default Options

Restore Optimized Defaults

To set this feature, select Restore Optimized Defaults from the Save & Exit menu and press <Enter>. These are factory settings designed for maximum system stability, but not for maximum performance.

Save As User Defaults

To set this feature, select Save as User Defaults from the Exit menu and press <Enter>. This enables the user to save any changes to the BIOS setup for future use.

Restore User Defaults

To set this feature, select Restore User Defaults from the Exit menu and press <Enter>. Use this feature to retrieve user-defined settings that were saved previously.

Boot Override

Listed on this section are other boot options for the system (i.e., Built-in EFI shell). Select an option and press <Enter>. Your system will boot to the selected boot option.

Appendix A

Software Installation

A.1 Installing Software Programs

The Supermicro FTP site contains drivers and utilities for your system at ftp://ftp.supermicro.com. Some of these must be installed, such as the chipset driver.

After accessing the FTP site, go into the CDR_Images directory and locate the ISO file for your motherboard. Download this file to create a CD/DVD of the drivers and utilities it contains. (You may also use a utility to extract the ISO file if preferred.)

Another option is to go to the Supermicro website at http://www.supermicro.com/products/. Find the product page for your motherboard here, where you may download individual drivers and utilities.

After creating a CD/DVD with the ISO files, insert the disk into the CD/DVD drive on your system, and the following screen should appear.

Note 1: Click the icons showing a hand writing on paper to view the readme files for each item. Click the computer icons to the right of these items to install each item (from top to the bottom) one at a time. **After installing each item, you must reboot the system before moving on to the next item on the list.** The bottom icon with a CD on it allows you to view the entire contents.

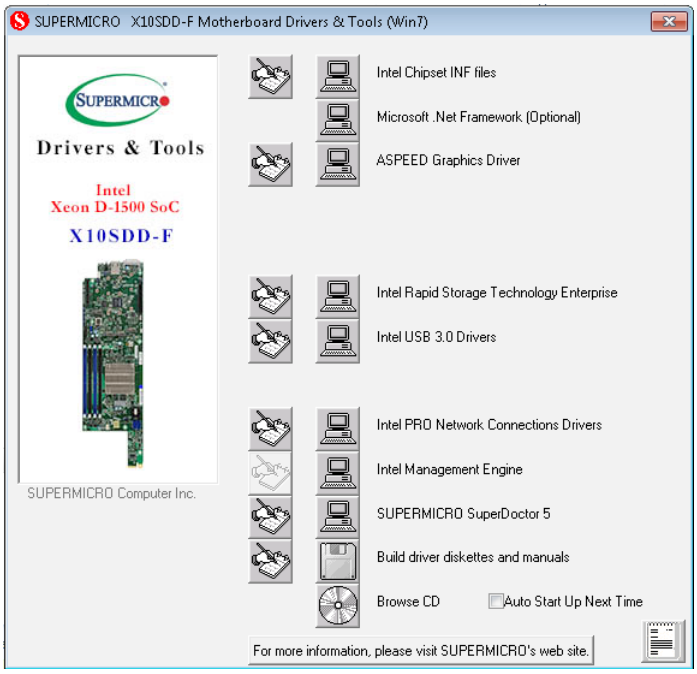


Figure B-1. Driver/Tool Installation Display Screen

Note 2: When making a storage driver diskette by booting into a driver CD, please set the SATA configuration to *Compatible Mode,* and configure the SATA as IDE in the BIOS setup. After making the driver diskette, be sure to change the SATA settings back to your original settings.

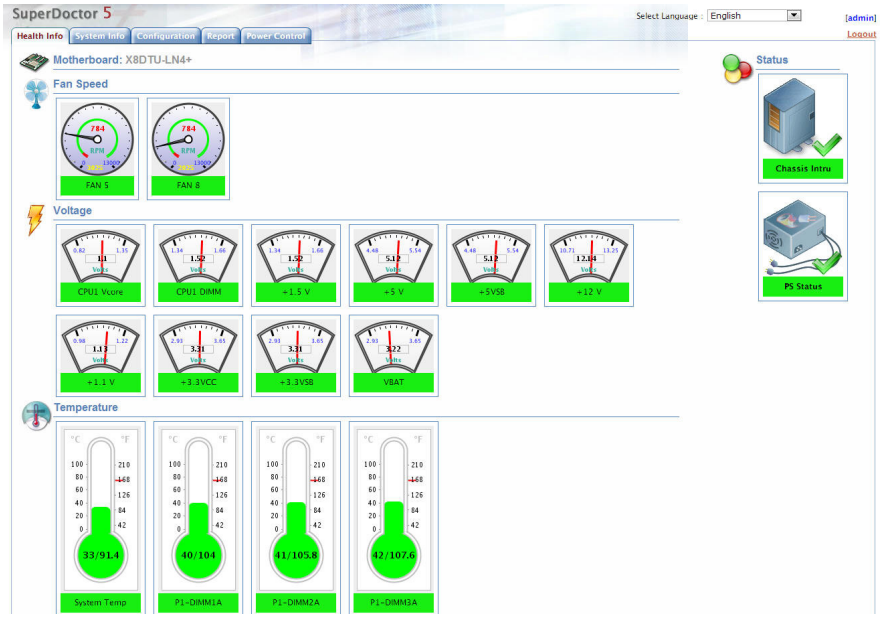
A.2 SuperDoctor® 5

The Supermicro SuperDoctor 5 is a hardware monitoring program that functions in a command-line or web-based interface in Windows and Linux operating systems. The program monitors system health information, such as CPU temperature, system voltages, system power consumption, and fan speed, and provides alerts via email or the Simple Network Management Protocol (SNMP).

SuperDoctor 5 comes in local and remote management versions and can be used with Nagios to maximize your system monitoring needs. With the SuperDoctor 5 Management Server (SSM Server), you can remotely control the power status and reset chassis intrusion for multiple systems with SuperDoctor 5 or IPMI. SD5 Management Server monitors HTTP, FTP, and SMTP services to optimize the efficiency of your operation.

Note: The default username and password for SuperDoctor 5 is admin/admin.

Figure B-2. SuperDoctor 5 Interface Display Screen (Health Information)



Note: The SuperDoctor 5 program and user's manual can be downloaded from the Supermicro website at http://www.supermicro.com/products/nfo/sms_sd5.cfm.

Appendix B

Standardized Warning Statements

The following statements are industry standard warnings, provided to warn the user of situations which have the potential for bodily injury. Should you have questions or experience difficulty, contact Supermicro's Technical Support department for assistance. Only certified technicians should attempt to install or configure components.

Read this section in its entirety before installing or configuring components.

These warnings may also be found on our website at http://www.supermicro.com/about/policies/safety_information.cfm.

Battery Handling

Warning! There is the danger of explosion if the battery is replaced incorrectly. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions

電池の取り扱い

電池交換が正しく行われなかった場合、破裂の危険性があります。交換する電池はメーカーが推奨する型、または同等のものを使用下さい。使用済電池は製造元の指示に従って処分して下さい。

警告

电池更换不当会有爆炸危险。请只使用同类电池或制造商推荐的功能相当的电池更换原有电池。请按制造商的说明处理废旧电池。

警告

電池更換不當會有爆炸危險。請使用製造商建議之相同或功能相當的電池更換原有電池。請按照製造商的說明指示處理廢棄舊電池。

Warnung

Bei Einsetzen einer falschen Batterie besteht Explosionsgefahr. Ersetzen Sie die Batterie nur durch den gleichen oder vom Hersteller empfohlenen Batterietyp. Entsorgen Sie die benutzten Batterien nach den Anweisungen des Herstellers.

Attention

Danger d'explosion si la pile n'est pas remplacée correctement. Ne la remplacer que par une pile de type semblable ou équivalent, recommandée par le fabricant. Jeter les piles usagées conformément aux instructions du fabricant.

¡Advertencia!

Existe peligro de explosión si la batería se reemplaza de manera incorrecta. Reemplazar la batería exclusivamente con el mismo tipo o el equivalente recomendado por el fabricante. Desechar las baterías gastadas según las instrucciones del fabricante.

!אזהרה

קיימת סכנה פיצוץ של הסוללה במידה והחלפה בדרך לא הנכונה. יש להחליף
את הסוללה בסוג דומה או מומלצת ע"ידן היצרן בלבד.

יש לסלק את הסוללות המשומשות על פי הוראות היצרן.

هناك خطر من انفجار في حالة استبدال البطارية بطريقة غير صحيحة فعليك
استبدال البطارية
فقط بنفس النوع أو ما يعادلها كما أوصت به الشركة المصنعة
تخلص من البطاريات المستعملة وفقا لتعليمات الشركة الصانعة

경고!

배터리가 올바르게 교체되지 않으면 폭발의 위험이 있습니다. 기존 배터리와 동일하거나 제조사에서 권장하는 동등한 종류의 배터리로만 교체해야 합니다. 제조사의 안내에 따라 사용된 배터리를 처리하여 주십시오.

Waarschuwing

Er is ontploffingsgevaar indien de batterij verkeerd vervangen wordt. Vervang de batterij slechts met hetzelfde of een equivalent type die door de fabrikant aanbevolen wordt. Gebruikte batterijen dienen overeenkomstig fabrieksvoorschriften afgevoerd te worden.

Product Disposal

Warning! Ultimate disposal of this product should be handled according to all national laws and regulations.

製品の廃棄

この製品を廃棄処分する場合、国の関係する全ての法律・条例に従い処理する必要があります。

警告

本产品的废弃处理应根据所有国家的法律和规章进行。

警告

本產品的廢棄處理應根據所有國家的法律和規章進行。

Warnung

Die Entsorgung dieses Produkts sollte gemäß allen Bestimmungen und Gesetzen des Landes erfolgen.

¡Advertencia!

Al deshacerse por completo de este producto debe seguir todas las leyes y reglamentos nacionales.

Attention

La mise au rebut ou le recyclage de ce produit sont généralement soumis à des lois et/ou directives de respect de l'environnement. Renseignez-vous auprès de l'organisme compétent.

경고!

이 제품은 해당 국가의 관련 법규 및 규정에 따라 폐기되어야 합니다.

Waarschuwing

De uiteindelijke verwijdering van dit product dient te geschieden in overeenstemming met alle nationale wetten en reglementen.

Appendix C

UEFI BIOS Recovery

Warning: Do not upgrade the BIOS unless your system has a BIOS-related issue. Flashing the wrong BIOS can cause irreparable damage to the system. In no event shall Supermicro be liable for direct, indirect, special, incidental, or consequential damages arising from a BIOS update. If you need to update the BIOS, do not shut down or reset the system while the BIOS is updating. Doing so may cause a boot failure.

C.1 Overview

The Unified Extensible Firmware Interface (UEFI) specification provides a software-based interface between the operating system and the platform firmware in the pre-boot environment. The UEFI specification supports an architecture-independent mechanism for add-on card initialization to allow the UEFI OS loader, which is stored in the add-on card, to boot up the system. UEFI offers a clean, hand-off control to a computer system at bootup.

C.2 Recovering the UEFI BIOS Image

An AMIBIOS flash chip consists of a boot sector block and a main BIOS code block (a main BIOS image). The boot sector block contains critical BIOS codes, including memory detection and recovery codes for the user to flash a new BIOS image if the original BIOS image is corrupted. When the system power is on, the boot sector codes execute first. Once it is completed, the main BIOS code will continue with system initialization and bootup.

Note: Follow the BIOS Recovery instructions below for BIOS recovery when the main BIOS block crashes. However, when the BIOS Boot sector crashes, you will need to send the motherboard back to Supermicro for RMA repair.

C.3 Recovering the BIOS Block with a USB Device

This feature allows the user to recover a BIOS image using a USB-attached device without additional utilities used. A USB flash device such as a USB Flash Drive, or a USB CD/DVD ROM/RW device can be used for this purpose. However, a USB Hard Disk drive cannot be used for BIOS recovery at this time.

To perform UEFI BIOS recovery using a USB-attached device, follow the instructions on the next page.

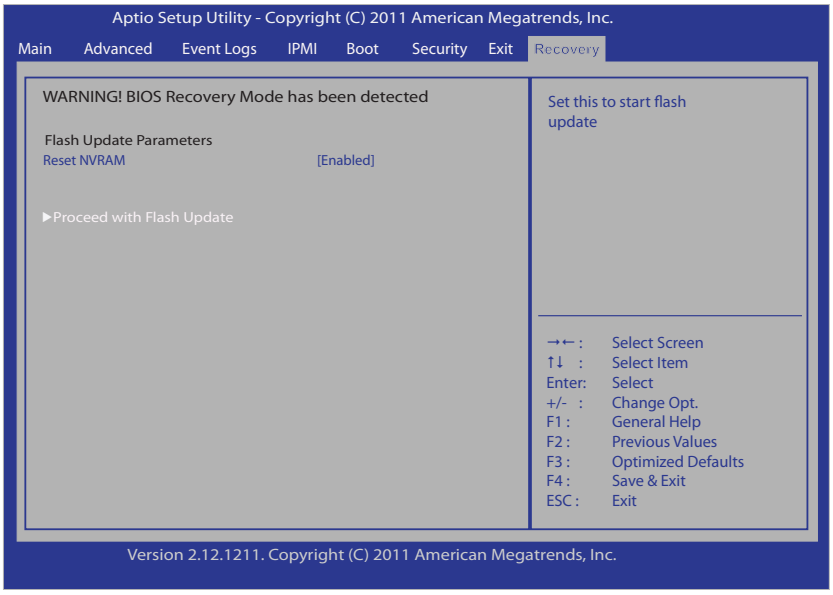
1. Using a different machine, copy the "Super.ROM" binary image file into the disc Root "\" Directory of a USB device or a writeable CD/DVD.

   Note: If you cannot locate the "Super.ROM" file in your driver disk, visit our website at www.supermicro.com to download the BIOS image into a USB flash device and rename it to "Super.ROM" for BIOS recovery use.

2. Insert the USB device that contains the new BIOS image ("Super.Rom") into your USB drive and power on the system

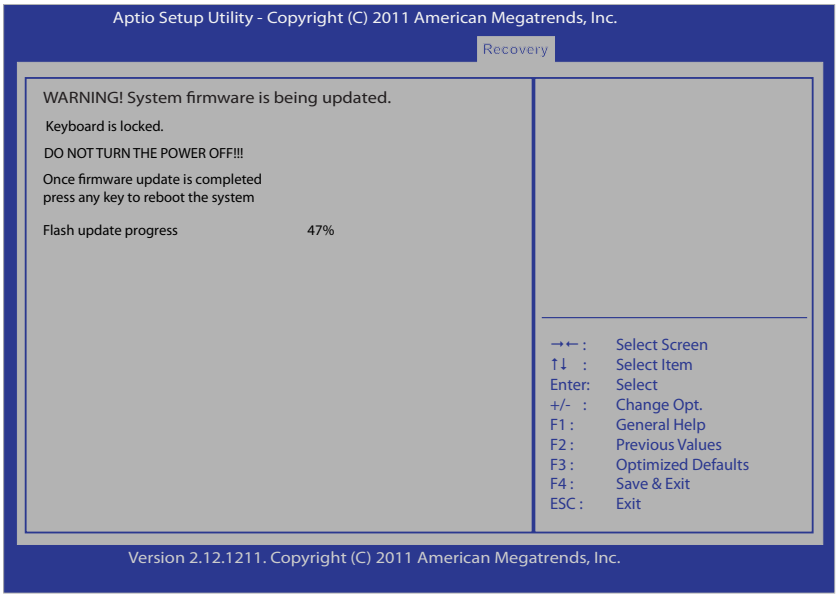
3. While powering on the system, keep pressing <Ctrl> and <Home> simultaneously on your PS2 or USB keyboard until your hear two short beeps. This may take from a few seconds to one minute.

4. After locating the new BIOS binary image, the system will enter the BIOS Recovery page as shown below.

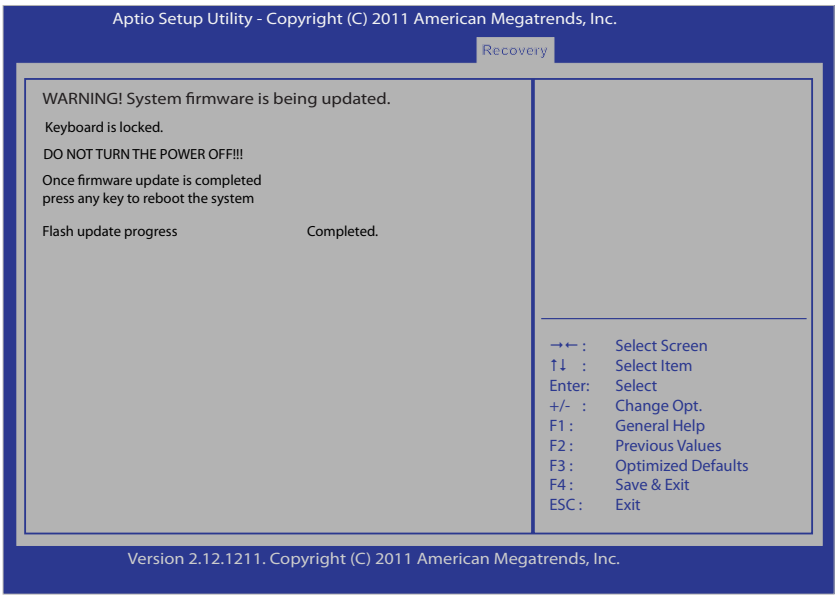


Note: At this point, you may decide if you want to start with the BIOS Recovery. If you decide to proceed wtih BIOS Recovery, follow the procedures below.

5. When the screen as shown above displays, use the arrow keys to select the item "Proceed with flash update" and press the <Enter> key. You will see the BIOS recovery progress as shown in the screen below.



Note: *Do not interrupt the BIOS flashing process until it has completed*.



6. After the BIOS recovery process has completed, press any key to reboot the system.
7. Using a different system, extract the BIOS package into a bootable USB flash drive.
8. When a DOS prompt appears, enter FLASH.BAT BIOSname.### at the prompt.

Note: *Do not interrupt this process* until the BIOS flashing is complete.

9. After seeing the message that BIOS update has completed, unplug the AC power cable from the power supply, clear CMOS, then plug the AC power cable in the power supply again to power on the system.

10. Press <Del> continuously to enter the BIOS Setup utility.

11. Press <F3> to load the default settings.

12. After loading the default settings, press <F4> to save the settings and exit the BIOS Setup utility.