

Addendum-X5DPL-8GM/X5DPL-iGM (Rev. 1.1)

Keylock

The keyboard lock connection is designated on JL1. Utilizing this header allows you to inhibit any actions made on the keyboard, effectively "locking" it.

Wake-On-LAN

The Wake-On-LAN header is designated WOL. See the table on the right for pin definitions. You must enable the LAN Wake-Up setting in BIOS to use this feature. You must also have a LAN card with a Wake-on-LAN connector and cable.

Wake-On-LAN Pin Definitions (WOL)

Pin Number	Definition
1	+5V Standby
2	Ground
3	Wake-up

Wake-On-Ring

The Wake-On-Ring header is designated JWOR1. This function allows your computer to receive and "wake-up" by an incoming call to the modem when in suspend state. See the table on the right for pin definitions. You must have a Wake-On-Ring card and cable to use this feature.

Wake-on-Ring Pin Definitions (JWOR1)

Pin Number	Definition
1	Ground
2	Wake-up

SMB

A System Management Bus header is located above the IPMI connector. Connect the appropriate cable here to utilize SMB on your system. (*SMB signal is 3.3V.)

SMB Header Pin Definitions

Pin Number	Definition
1	Data
2	Ground
3	Clock
4	No Connection