

Attention!!

Important Information!!

(Read the information listed below before using this product.)

1. For the X6DHR-X8G/XiG boards:

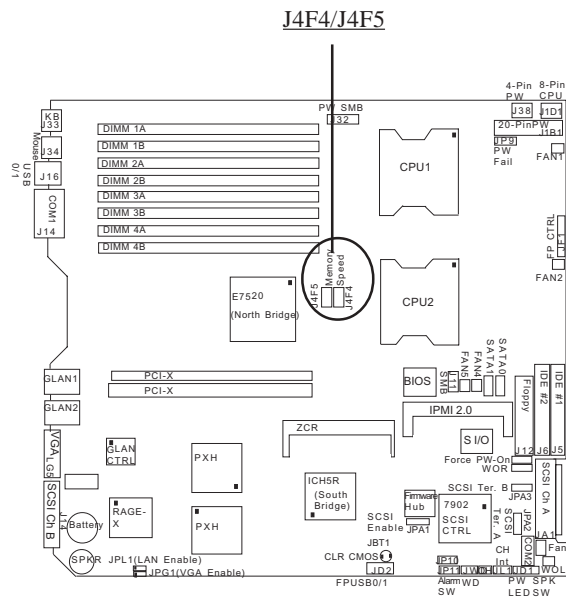
Jumpers J4F4/J4F5 (PLLSEL Select): These jumpers allow the user to select PLLSEL (Memory Speed). Make sure that the jumper settings are set correctly according to your memory speed before you power on the system.

(*Warning!! If these jumpers are not set correctly according to your memory speed, video display failure may occur!!!)

(*Notes: 1. The default setting is "Closed" for both jumpers to enable DDR Memory at 333MHz. However, if your memory speed is 266MHz, please make sure to set both jumpers to "Open". If Reg. ECC DDR 333 (-PC2700)) memory modules are used, 4 pieces of double banked memory and 6 pieces of single banked memory are supported.)

PLLSEL Select Jumper Settings (J4F4/J4F5)		
DDR	J4F4	J4F5
*333 MHz	Closed	Closed
266MHz	Open	Open

(*Default:Closed-333MHz)



J1D1 (12V 8-pin PWR) and J38 (12 V 4-pin PWR): Make sure that both PWR Connectors are used to ensure adequate power supply to the system and the CPU.