



X12DGU

USER'S MANUAL

Revision 1.0

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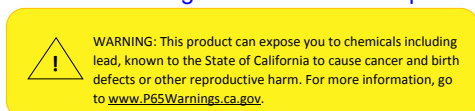
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Manual Revision 1.0

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## Preface

### About This Manual

This manual is written for system integrators, IT technicians and knowledgeable end users. It provides information for the installation and use of the X12DGU motherboard.

### About This Motherboard

The Supermicro X12DGU supports dual 3rd Gen Intel® Xeon® Scalable Processors (in Socket P LGA 4189) with up to 40 CPU cores per CPU and a thermal design power (TDP) of up to 270W. Built with the Intel C621A chipset, the X12DGU supports up to 8TB 3DS LRDIMM/LRDIMM/3DS RDIMM/RDIMM DDR4 ECC memory with speeds up to 3200/2933/2666MHz in 32 DIMM slots, and up to 8TB of Intel® Optane™ PMem 200 Series with speeds up to 3200MHz. This motherboard features superior I/O expandability, including four PCIe 4.0 slots for add-on cards, four PCIe 4.0 advanced I/O modules (AIOM), ten SATA 3.0 ports, and a dedicated BMC LAN port. It also offers the most advanced data protection capability that provides TPM (Trusted Platform Module) and hardware RoT (Root of Trust) support. The X12DGU is optimized for future PCIe specifications with flexible I/O, networking, storage, and GPU support. It is ideal for use in general purpose servers with HPC (High Performance Computing), Artificial Intelligence (AI), and deep learning applications. Please note that this motherboard is intended to be installed and serviced by professional technicians only. For processor/memory updates, please refer to our website at <http://www.supermicro.com/products/>.

 **Note 1:** The Intel® Optane™ Persistent Memory (PMem) 200 Series are supported by the 3rd Gen Intel Xeon Scalable (83xx/63xx/53xx/4314 Series) Processors.

**Note 2:** Memory speed support depends on the processors used in the system.

### Conventions Used in the Manual



**Important:** Important information given to ensure proper system installation or to relay safety precautions.



**Warning!** Indicates important information given to prevent equipment/property damage or personal injury.



**Warning!** Indicates high voltage may be encountered while performing a procedure.



**Note:** Additional Information given to differentiate between various models or to provide information for proper system setup.

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# Table of Contents

## **Chapter 1 Introduction**

|                                                                      |    |
|----------------------------------------------------------------------|----|
| 1.1 Checklist .....                                                  | 7  |
| 1.2 Processor and Chipset Support .....                              | 19 |
| 1.3 Special Features .....                                           | 20 |
| 1.4 System Health Monitoring .....                                   | 20 |
| 1.5 ACPI Features .....                                              | 21 |
| 1.6 Power Supply .....                                               | 21 |
| 1.7 Serial Port.....                                                 | 21 |
| 1.8 Intel® Optane™ Persistent Memory (PMem) 200 Series Overview..... | 21 |

## **Chapter 2 Installation**

|                                               |    |
|-----------------------------------------------|----|
| 2.1 Static-Sensitive Devices .....            | 22 |
| 2.2 Processor and Heatsink Installation ..... | 23 |
| 2.3 Motherboard Installation.....             | 39 |
| 2.4 Memory Support and Installation .....     | 41 |
| 2.5 Rear I/O Connectors/Ports .....           | 46 |
| 2.6 Front Control Panels .....                | 49 |
| 2.7 Connectors .....                          | 57 |
| 2.8 Jumper Settings .....                     | 69 |
| 2.9 LED Indicators.....                       | 73 |

## **Chapter 3 Troubleshooting**

|                                            |    |
|--------------------------------------------|----|
| 3.1 Troubleshooting Procedures .....       | 75 |
| 3.2 Technical Support Procedures .....     | 78 |
| 3.3 Frequently Asked Questions .....       | 79 |
| 3.4 Battery Removal and Installation ..... | 80 |
| 3.5 Returning Merchandise for Service..... | 81 |

## **Chapter 4 UEFI BIOS**

|                                        |     |
|----------------------------------------|-----|
| 4.1 Introduction.....                  | 82  |
| 4.2 Main Setup .....                   | 83  |
| 4.3 Advanced Setup Configurations..... | 85  |
| 4.4 Event Logs .....                   | 161 |
| 4.5 BMC .....                          | 163 |
| 4.6 Security.....                      | 166 |
| 4.7 Boot.....                          | 171 |

4.8 Save & Exit.....173

***Appendix A BIOS POST Codes***

A.1 BIOS POST Codes.....175

***Appendix B Software***

B.1 Microsoft Windows OS Installation.....176

B.2 Driver Installation.....178

B.3 SuperDoctor® 5.....179

B.4 BMC.....180

B.5 Logging into the BMC (Baseboard Management Controller).....180

***Appendix C Standardized Warning Statements***

# Chapter 1

## Introduction

Congratulations on purchasing your computer motherboard from an industry leader. Supermicro motherboards are designed to provide you with the highest standards in quality and performance.

### 1.1 Checklist

This motherboard is intended to be used in a Supermicro proprietary server as a part of an integrated solution. It will not be shipped as a standard, independent product. There will be no shipping package provided for this motherboard.

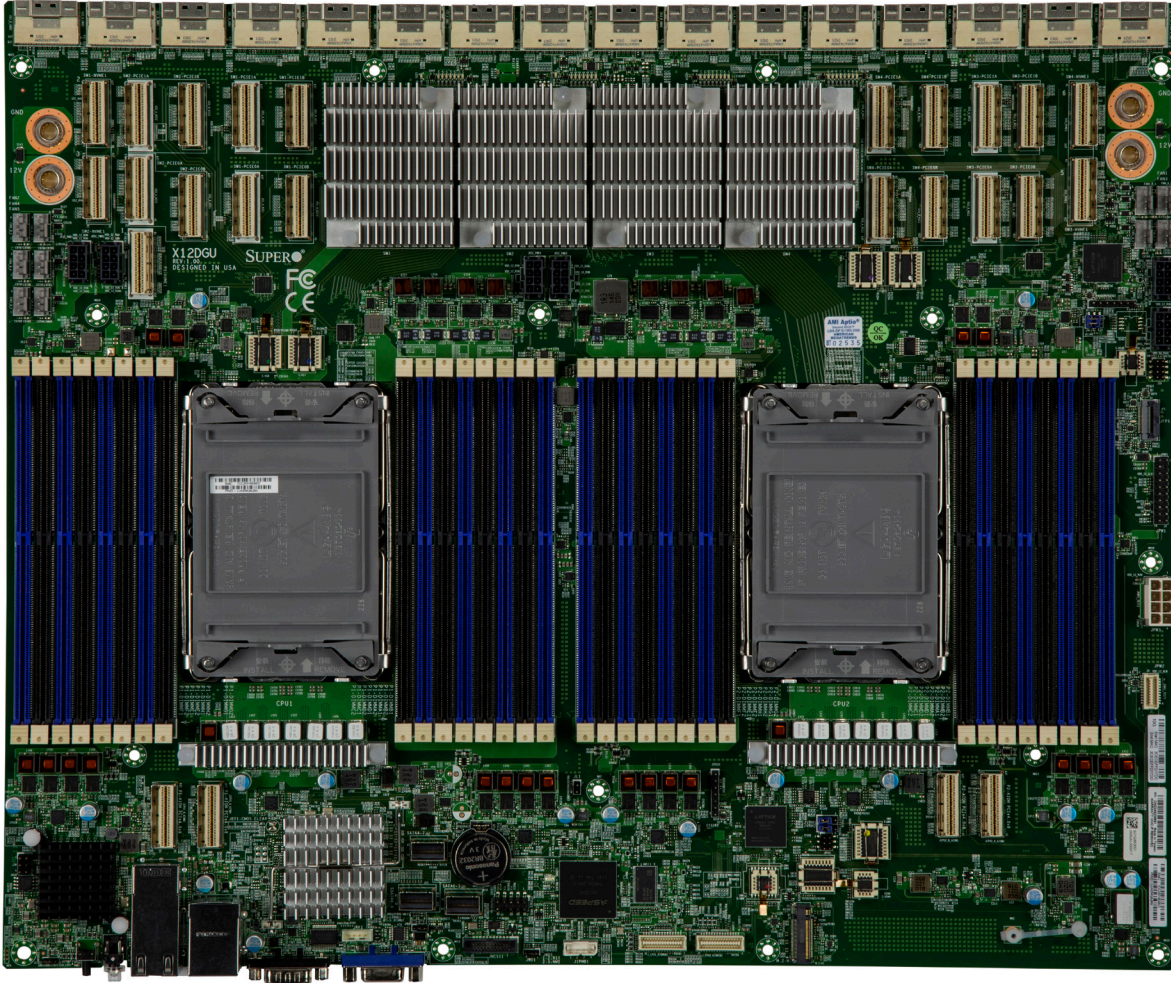
### Important Links

For your motherboard to work properly, please follow the links below to download all necessary drivers/utilities and the user's manual for your computer.

- Supermicro product manuals: <http://www.supermicro.com/support/manuals/>
- Product drivers and utilities: <https://www.supermicro.com/wdl/driver>
- Product safety info: [http://www.supermicro.com/about/policies/safety\\_information.cfm](http://www.supermicro.com/about/policies/safety_information.cfm)
- A secure data deletion tool designed to fully erase all data from storage devices can be found at our website: [https://www.supermicro.com/about/policies/disclaimer.cfm?url=/wdl/utility/Lot9\\_Secure\\_Data\\_Deletion\\_Utility/](https://www.supermicro.com/about/policies/disclaimer.cfm?url=/wdl/utility/Lot9_Secure_Data_Deletion_Utility/)
- Firmware-related and AOC user's guides: <http://www.supermicro.com/support/manuals/>
- If you have any questions, please contact our support team at: [support@supermicro.com](mailto:support@supermicro.com)

This manual may be periodically updated without notice. Please check the Supermicro website for possible updates to the manual revision level.

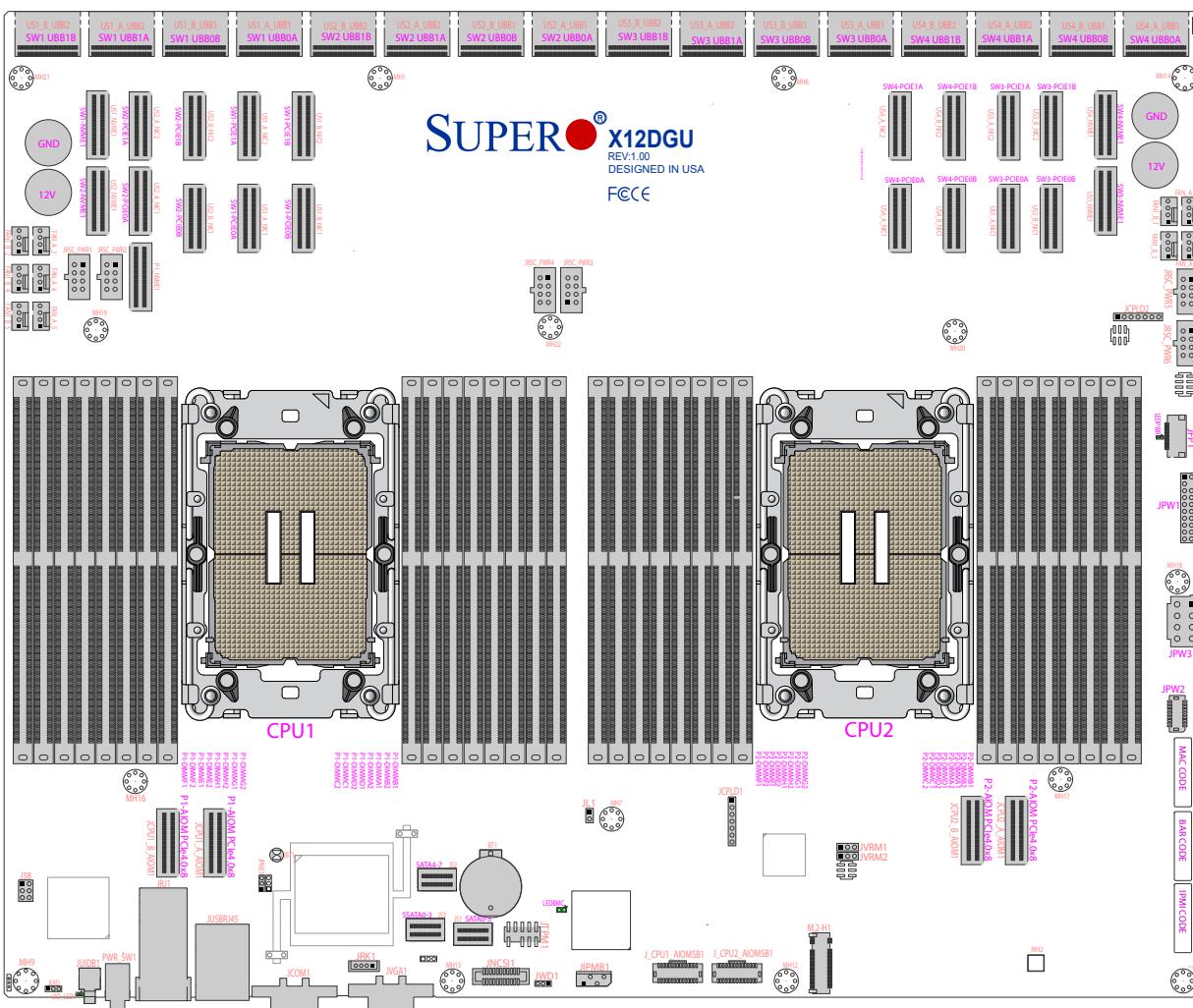
## X12DGU Motherboard Image



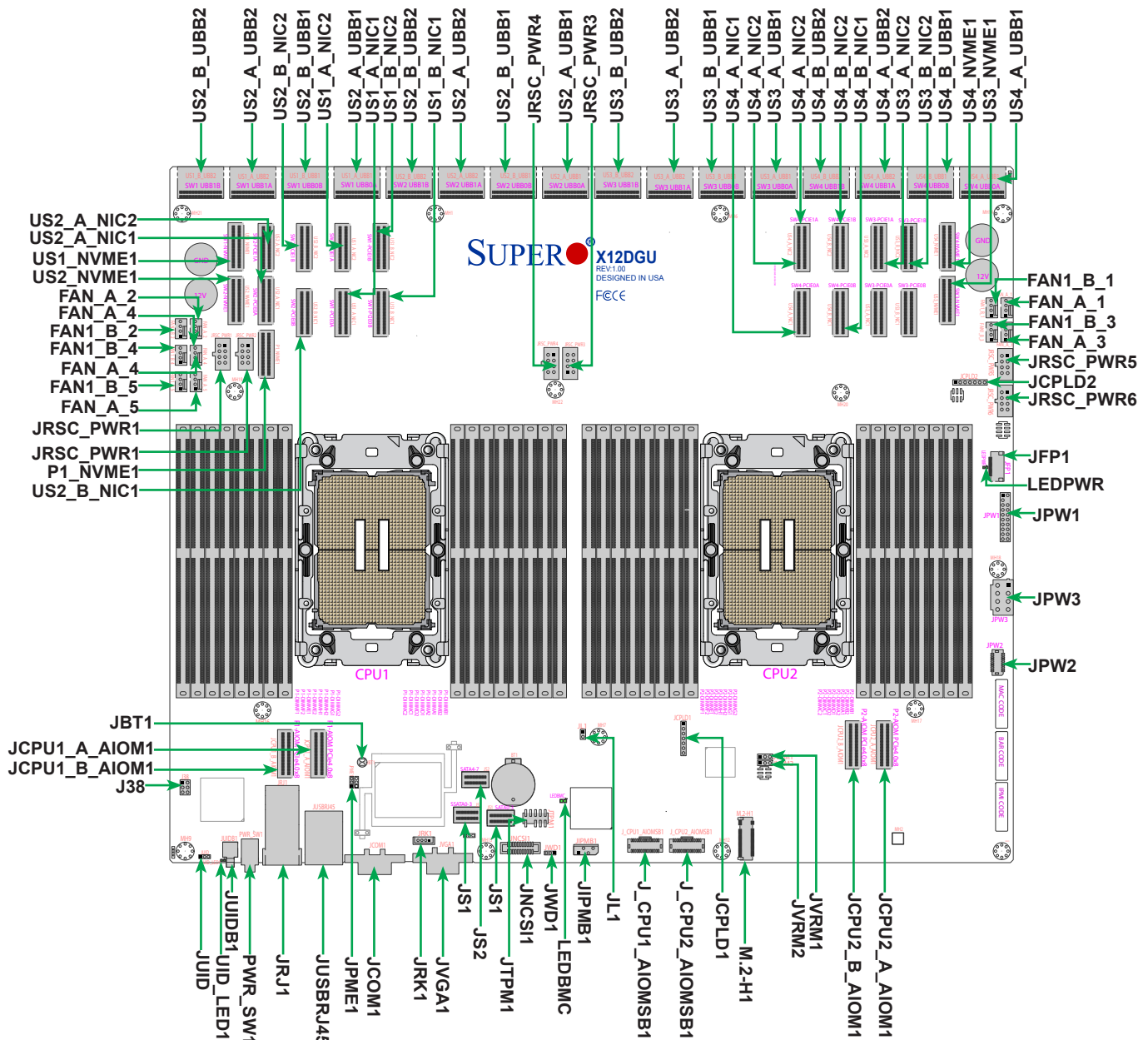
 **Note:** All graphics shown in this manual were based upon the latest PCB revision available at the time of publication of the manual. The motherboard you received may or may not look exactly the same as the graphics shown in this manual.

### X12DGU Motherboard Layout

(not drawn to scale)



**Note:** Components not documented are for internal testing only.



- See [Chapter 2](#) for detailed information on jumpers, I/O ports, and JFP1 front panel connections.
- "■" indicates the location of Pin 1.
- Jumpers/LED indicators not indicated are used for testing only.
- Use only the correct type of onboard CMOS battery as specified by the manufacturer. Do not install the onboard battery upside down to avoid a possible explosion.

## Quick Reference Table

| Jumper      | Description                                                                                   | Default Setting                           |
|-------------|-----------------------------------------------------------------------------------------------|-------------------------------------------|
| JBT1        | CMOS Clear                                                                                    | Open (Normal)                             |
| JPME1       | Manufacturing Mode Select                                                                     | Pins 1-2: normal                          |
| JUID        | UID LED/BMC Reset Jumper                                                                      | Pins 1-2: UID LED                         |
| JVRM1/JVRM2 | BMC and PCH I <sup>2</sup> C/SDA to VRM and<br>BMC and PCH I <sup>2</sup> C/SCL to VRM Select | Pins 1/3 BMC I <sup>2</sup> C/SDA for VRM |
| JWD1        | Watch Dog Jumper                                                                              | Pins 3-4: normal                          |

| LED      | Description               | Status                                                                                          |
|----------|---------------------------|-------------------------------------------------------------------------------------------------|
| UID-LED1 | Unit Identifier (UID) LED | Solid Blue: Unit Identified                                                                     |
| LEDPWR   | Power LED                 | LED On: Onboard Power On                                                                        |
| LEDBMC   | BMC Heartbeat LED         | Blinking Green: BMC Normal (Active),<br>Solid Green: (During BMC Reset or during a Cold Reboot) |

| Connector                                                                                                                           | Description                                                                         |
|-------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------|
| Battery (BT1)                                                                                                                       | Onboard battery                                                                     |
| FAN_A_1/FAN_A_2/<br>FAN_A_3/FAN_A_4/<br>FAN_A_5                                                                                     | 4-pin cooling fan headers                                                           |
| FAN1_B_1/FAN1_B_2/<br>FAN1_B_3/FAN1_B_4/<br>FAN1_B_5                                                                                | 4-pin cooling fan headers                                                           |
| JRSC_PWR1~6                                                                                                                         | 8-pin power connectors for riser cards                                              |
| JCOM1                                                                                                                               | Rear panel serial port                                                              |
| JFP1                                                                                                                                | Front Control Panel header with I <sup>2</sup> C                                    |
| JIPMB1                                                                                                                              | 6-pin BMC external I2C header                                                       |
| JL1                                                                                                                                 | Chassis Intrusion header                                                            |
| JRJ1                                                                                                                                | Rear panel LAN port                                                                 |
| M.2-H1                                                                                                                              | PCIe 4.0 x2/SATA3 Hybrid M.2 slot connected to the PCH (with support of M-Key 2280) |
| US1_A_UBB1~2/<br>US1_B_UBB1~2/<br>US2_A_UBB1~2/<br>US2_B_UBB1~2/<br>US3_A_UBB1~2/<br>US3_B_UBB1~2/<br>US4_A_UBB1~2/<br>US4_B_UBB1~2 | PCIe 4.0 x16 connectors for GPU devices.                                            |

|                                                                                                                                     |                                                                                                                                   |
|-------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------|
| JCPU1_A_AIOM1/<br>JCPU1_B_AIOM1/<br>JCPU2_A_AIOM1/<br>JCPU2_B_AIOM1                                                                 | PCIe 4.0 x16 SlimSAS connectors for AIOM Networking AOC                                                                           |
| J_CPU1_AIOMSB1/<br>J_CPU2_AIOMSB1                                                                                                   | Connectors for AIOM Networking AOC                                                                                                |
| US1_NVME1/<br>US2_NEME1/<br>US3_NVME1/<br>US4_NVME1                                                                                 | PCIe 4.0 x16 SlimSAS connectors for NVMe devices                                                                                  |
| US1_A_NIC1~2/<br>US1_B_NIC1~2/<br>US2_A_NIC1~2/<br>US2_B_NIC1~2/<br>US3_A_NIC1~2/<br>US3_B_NIC1~2/<br>US4_A_NIC1~2/<br>US4_B_NIC1~2 | PCIe 4.0 x16 SlimSAS connectors for I/O riser cards                                                                               |
| JNCSI1                                                                                                                              | NC-SI (Network Controller Sideband Interface) connector (See the note below)                                                      |
| JCPLD1/JCLPD2                                                                                                                       | Connectors reserved for manufacturer use for onboard CPLD (Complex Programmable Logic Device) firmware programming                |
| JPW1/JPW2/JPW3                                                                                                                      | System power connectors                                                                                                           |
| JPWR6                                                                                                                               | 4-pin power connector                                                                                                             |
| JRSC_PWR1~4                                                                                                                         | 4-pin power connectors for riser cards                                                                                            |
| JSTBY1                                                                                                                              | +5V AUX power header                                                                                                              |
| JTPM1                                                                                                                               | Trusted Platform Module/Port 80 connector                                                                                         |
| JVGA1                                                                                                                               | Rear panel VGA port                                                                                                               |
| JUIDB1                                                                                                                              | Rear panel unit identifier switch                                                                                                 |
| JUSBRJ45                                                                                                                            | Rear panel USB ports (USB0/USB1)                                                                                                  |
| J38                                                                                                                                 | UART header to X170 10G LAN controller                                                                                            |
| P1_NVME1                                                                                                                            | PCIe 4.0 x16 slimsas connector for NVMe devices connected to the CPU1                                                             |
| PWR_SW1                                                                                                                             | Power switch                                                                                                                      |
| SATA 0-3 (JS1)                                                                                                                      | SlimSAS LP (MCIO) connector with support of eight Intel® PCH SATA 3.0 connections (RAID 0, RAID 1, RAID 5, and RAID 10 supported) |
| SATA 4-7 (JS2)                                                                                                                      | SlimSAS LP (MCIO) connector with support of eight Intel® PCH SATA 3.0 connections (RAID 0, RAID 1, RAID 5, and RAID 10 supported) |
| SSATA 0-3 (JS3)                                                                                                                     | SlimSAS LP (MCIO) connector with support of eight Intel® PCH SATA 3.0 connections (RAID 0, RAID 1, RAID 5, and RAID 10 supported) |
| VROC RAID Key (JRK1)                                                                                                                | Intel VROC key header for NVMe RAID support (See the note below.)                                                                 |



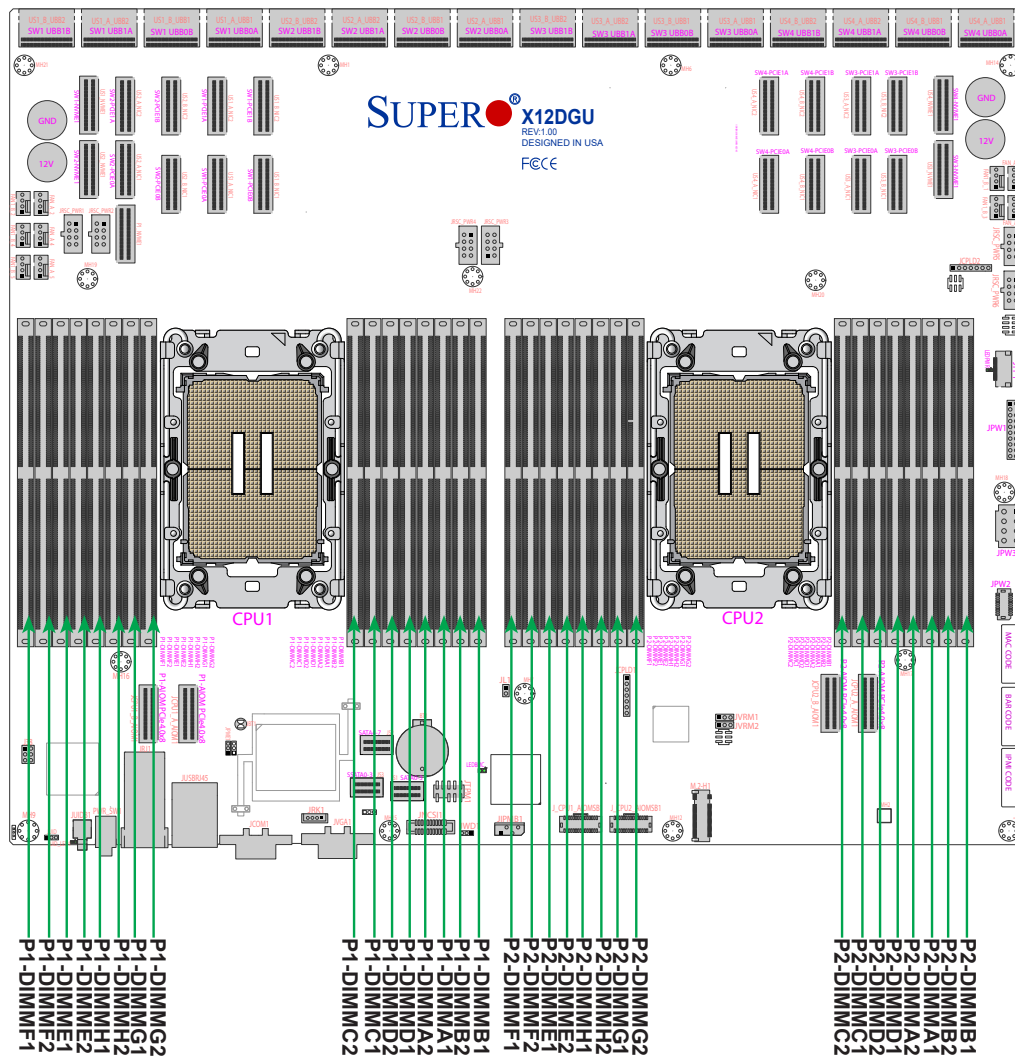
**Note 1:** For detailed instructions on how to configure VROC RAID settings, please refer to the VROC RAID Configuration User's Guide posted on the web page under the link: <http://www.supermicro.com/support/manuals/>.

**Note 2:** For details on how to configure Network Interface Card (NIC) settings, please refer to the Network Interface Card Configuration User's Guide posted on our website under the link: <http://www.supermicro.com/support/manuals/>.

## Memory Slots

This motherboard contains 32 DIMM slots which supports up to 8TB of DDR5 memory in 32 slots and up to 8TB of PMem 300 Series memory in 16 DIMM slots. Please refer to the layout drawing below for the locations of the DIMM slots:

| <b>DIMM Slots Supported by CPU1</b> | <b>DIMM Slots Supported by CPU2</b> |
|-------------------------------------|-------------------------------------|
| P1-DIMMA1                           | P2-DIMMA1                           |
| P1-DIMMA2                           | P2-DIMMA2                           |
| P1-DIMMB1                           | P2-DIMMB1                           |
| P1-DIMMB2                           | P2-DIMMB2                           |
| P1-DIMMC1                           | P2-DIMMC1                           |
| P1-DIMMC2                           | P2-DIMMC2                           |
| P1-DIMMD1                           | P2-DIMMD1                           |
| P1-DIMMD2                           | P2-DIMMD2                           |
| P1-DIMME1                           | P2-DIMME1                           |
| P1-DIMME2                           | P2-DIMME2                           |
| P1-DIMMF1                           | P2-DIMMF1                           |
| P1-DIMMF2                           | P2-DIMMF2                           |
| P1-DIMMG1                           | P2-DIMMG1                           |
| P1-DIMMG2                           | P2-DIMMG2                           |
| P1-DIMMH1                           | P2-DIMMH1                           |
| P1-DIMMH2                           | P2-DIMMH2                           |



## Motherboard Features

| Motherboard Features                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |                                                                                                                                                                                       |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>CPU</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |                                                                                                                                                                                       |
| <ul style="list-style-type: none"> <li>Supports dual 3rd Gen Intel Xeon Scalable Processors (in Socket P LGA 4189) with up to 40 CPU cores and a thermal design power (TDP) of up to 270W</li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                               |                                                                                                                                                                                       |
| <b>Memory</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |                                                                                                                                                                                       |
| <ul style="list-style-type: none"> <li>Supports up to 8TB 3DS LRDIMM/LRDIMM/3DS RDIMM/RDIMM DDR4 (288-pin) ECC memory with speeds up to 3200/2933/2666MHz in 32 DIMM slots and up to 8TB PMem 200 Series memory with speeds up to 3200MHz in 16 DIMM slots.</li> </ul> <p> <b>Note 1:</b> The Intel Optane Persistent Memory (PMem) 200 Series are supported by 3rd Gen Intel Xeon Scalable (83xx/63xx/53xx/4314 Series) Processors.</p> <p><b>Note 2:</b> Memory speed and capacity support depends on the processors used in the system.</p>    |                                                                                                                                                                                       |
| <b>DIMM Size</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |                                                                                                                                                                                       |
| <ul style="list-style-type: none"> <li>Up to 256GB at 1.2V</li> </ul> <p> <b>Note:</b> For the latest CPU/memory updates, please refer to our website at <a href="http://www.supermicro.com/products/motherboard">http://www.supermicro.com/products/motherboard</a>.</p>                                                                                                                                                                                                                                                                         |                                                                                                                                                                                       |
| <b>Chipset</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |                                                                                                                                                                                       |
| <ul style="list-style-type: none"> <li>Intel PCH C621A</li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |                                                                                                                                                                                       |
| <b>Expansion Slots</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |                                                                                                                                                                                       |
| <ul style="list-style-type: none"> <li>Eight PCIe 4.0 x16 connectors for GPUs</li> <li>Four SlimSAS PCIe 4.0 x16 NVMe connectors</li> <li>One NVMe PCIe 3.0 x2 / SATA3 Hybrid M.2 port (with M-Key 2280 and 22110 support)</li> <li>Four SlimSAS PCIe 4.0 x16 connectors for riser cards</li> <li>Four SlimSAS PCIe 4.0 x16 connectors for network interface cards</li> <li>Two MiniSAS connectors with support of eight SATA 3.0 connections (RAID 0, RAID 1, RAID 5, and RAID 10 supported)</li> <li>One MiniSAS connector with support of four SSATA 3.0 connections (RAID 0, RAID 1, RAID 5, and RAID 10 supported)</li> </ul> |                                                                                                                                                                                       |
| <b>Network</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |                                                                                                                                                                                       |
| <ul style="list-style-type: none"> <li>Two 10GBASE-T Ethernet LAN ports connected to Intel X710 Ethernet Controller for X12DGU</li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |                                                                                                                                                                                       |
| <b>Baseboard Management Controller (BMC)/Network</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |                                                                                                                                                                                       |
| <ul style="list-style-type: none"> <li>ASPEED AST2600 BMC</li> <li>One dedicated BMC LAN/VGA (via AST2600 BMC HW RoT)</li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |                                                                                                                                                                                       |
| <b>Graphics</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |                                                                                                                                                                                       |
| <ul style="list-style-type: none"> <li>Graphics controller and VGA support via ASPEED AST2600 BMC</li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |                                                                                                                                                                                       |
| <b>I/O Devices</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |                                                                                                                                                                                       |
| <ul style="list-style-type: none"> <li>VGA Port</li> <li>Serial (COM) Port</li> <li>SATA 3.0</li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | <ul style="list-style-type: none"> <li>One front-accessible VGA port (JVGA1)</li> <li>One front-accessible serial port (JCOM1)</li> <li>Three SATA 3.0 ports (JS1/JS2/JS3)</li> </ul> |

## Peripheral Devices

- Two USB 3.0 ports on rear panel

## BIOS

- AMI SPI BIOS
- EFI GUI, SPI dual/quad speed control, riser card auto detection support, RTC (Real Time Clock) wakeup, IPMIView, SMCIPMITOOL, IPMI CFG, SuperDoctor5, SD3 (for Windows OS), Redundant power supply unit detection, SPM, SUM-OOB/InBand

## Power Management

- ACPI power management
- S1, S4, S5 support
- Power button override mechanism
- Power-on mode for AC power recovery
- Wake-on-LAN
- Power supply monitoring

## System Health Monitoring

- Onboard voltage monitoring for +12V, +5V/+5V standby, +3.3V, and +3.3V standby, Vcore, Vmem
- Onboard temperature monitoring for CPU, VRM, LAN, PCH, system, and memory
- 7+1 CPU switch phase voltage regulator
- CPU thermal trip support
- Platform Environment Control Interface (PECI)

## Fan Control

- Fan status monitoring via BMC connections
- Single zone cooling
- Low-noise fan speed control
- Ten 4-pin proprietary fan headers

## System Management

- SuperDoctor® 5 and SD3 (for Windows OS)
- Chassis intrusion header and detection
- Server platform service

## Firmware Integrity/System Security

- TPM (Trusted Platform Module) support
- RoT (Root of Trust) support to protect firmware security by detecting critical data corruption, and restoring platform integrity

## LED Indicators

- Power LED
- UID/remote UID
- LAN activity LED
- BMC/CPLD firmware LED

## Dimensions

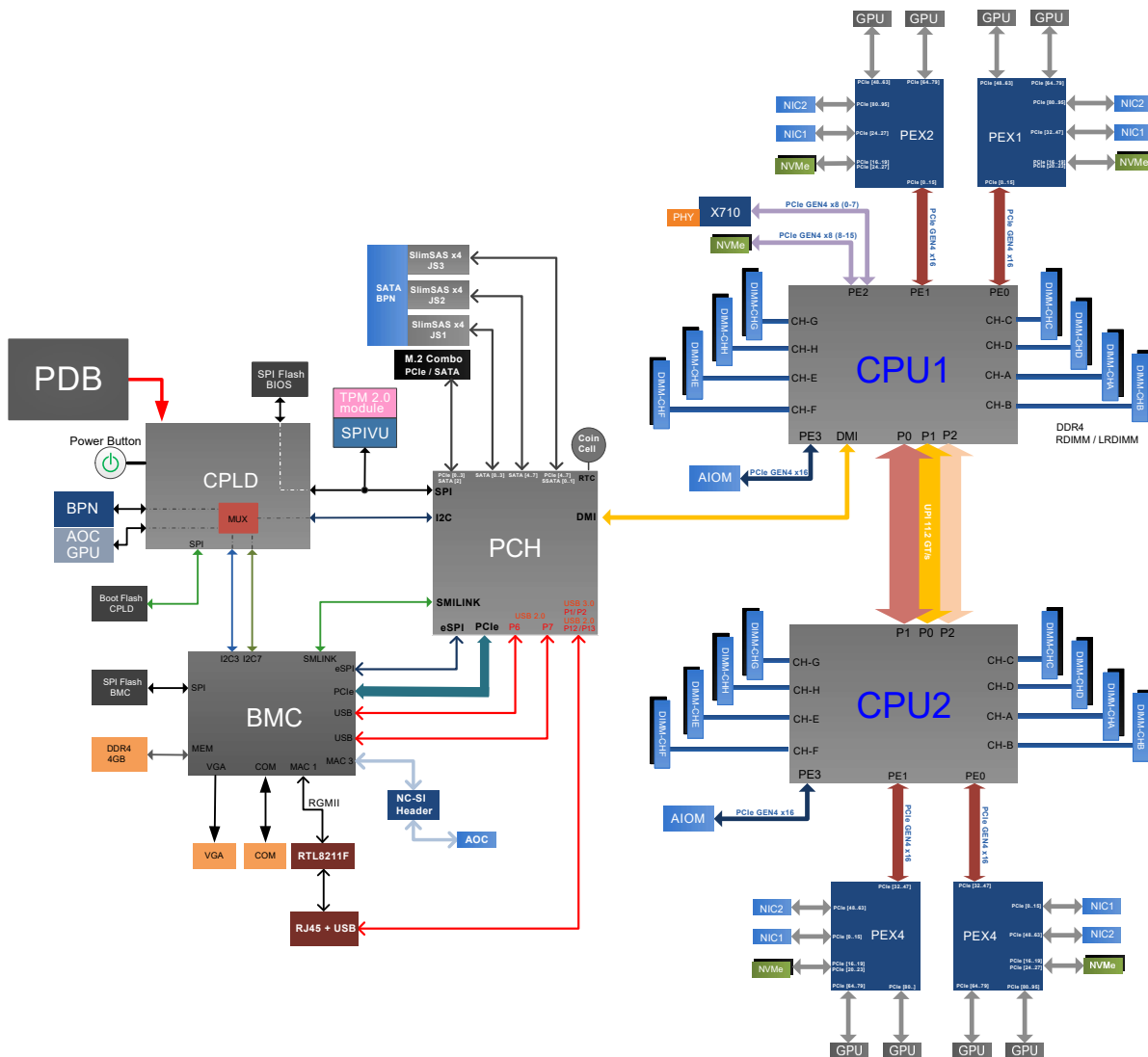
- 14.29" x 17.32" (363mm x 440mm)



**Note 1:** The CPU maximum thermal design power (TDP) is subject to chassis and heatsink cooling restrictions. For proper thermal management, please check the chassis and heatsink specifications.

**Note 2:** For BMC configuration instructions, please refer to the Embedded BMC Configuration User's Guide available at <http://www.supermicro.com/support/manuals/>.

# X12DGU



System Block Diagram

 **Note:** This is a general block diagram and may not exactly represent the features on your motherboard. See the previous pages for the actual specifications of your motherboard.

## 1.2 Processor and Chipset Support

Built upon the functionality and capability of the 3rd Gen Intel Xeon Scalable Processors (Socket P) and the Intel C621A chipset, the X12DGU motherboard offers critical, pivotal technological breakthroughs that unleash unprecedented computing capabilities and provides a scalable platform optimized for applications used in Artificial Intelligence (AI) Acceleration, Deep Learning Boost, Network Interconnectivity, and Platform Manageability technologies. The key features supported by the platform include the following:

### Features Supported

- Performance improvements with higher core counts (up to 3UPIs/socket @11.2GT/s)
- Vector Neural Network Instructions (VNNI) support to accelerate AI/deep learning training
- New hardware-enhanced security features help protect platform and data without compromising performance
- Higher performance storage (PCIe 4.0 NVMe) with double the bandwidth of PCIe 3.0

## 1.3 Special Features

### Recovery from AC Power Loss

The Basic I/O System (BIOS) provides a setting that determines how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off (in which case you must press the power switch to turn it back on), or for it to automatically return to the power-on state. See the Advanced BIOS Setup section for this setting. The default setting is **Last State**.

## 1.4 System Health Monitoring

### Onboard Voltage Monitors

An onboard voltage monitor will scan the voltages of the onboard chipset, memory, and CPU continuously. Once a voltage becomes unstable, an error message is logged in the system event log in the BMC.

### Fan Status Monitor with Firmware Control

The system health monitor embedded in the BMC chip can check the RPM status of the cooling fans. The CPU and chassis fans are controlled via IPMI.

### Environmental Temperature Control

System Health sensors monitor temperatures and voltage settings of onboard processors and the system in real time via the BMC interface. Whenever the temperature of the CPU or the system exceeds Supermicro's predefined threshold, the system and CPU cooling fan speed will increase to prevent the CPU or system from overheating.

 **Note:** To avoid possible system overheating, please be sure to provide adequate air-flow to your system.

### System Resource Alert

This feature is available when used with SuperDoctor 5® in the Windows OS environment. SuperDoctor is used to notify the user of certain system events. For example, you can configure SuperDoctor to provide you with warnings when the system temperature, CPU temperatures, voltages and fan speeds go beyond a predefined range.

## 1.5 ACPI Features

ACPI stands for Advanced Configuration and Power Interface. The ACPI specification defines a flexible and abstract hardware interface that provides a standard way to integrate power management features throughout a computer system, including its hardware, operating system and application software. This enables the system to automatically turn on and off peripherals such as network cards, hard disk drives and printers.

In addition to enabling operating system-directed power management, ACPI also provides a generic system event mechanism for Plug and Play, and an operating system-independent interface for configuration control. ACPI leverages the Plug and Play BIOS data structures, while providing a processor architecture-independent implementation that is compatible with appropriate Windows operating systems. For detailed information regarding OS support, please refer to the Supermicro website.

## 1.6 Power Supply

As with all computer products, a stable power source is necessary for proper and reliable operation. It is even more important for processors that have high CPU clock rates where noisy power transmission is present.

The X12DGU motherboard utilizes two Supermicro proprietary +12V power supply, which are connected the main power connectors of the motherboard. In addition, there are six power connectors (JRSC\_PWR1~6) provide power from X12DGU to the riser cards.

It is strongly recommended that you only use Supermicro approved redundant power supply units for the Supermicro system chassis.

## 1.7 Serial Port

The X12DGU motherboard supports one serial communication connection. COM Port 1 (JCOM1) is used for input/output. The UART provides legacy speeds with a baud rate of up to 115.2 Kbps.

## 1.8 Intel® Optane™ Persistent Memory (PMem) 200 Series Overview

The 3rd Gen Intel Xeon Scalable Processors support Intel Optane PMem 200 Series memory. Intel Optane PMem offers higher capacities than the traditional DDR4 modules. It also provides increased storage capabilities due to data persistence in a DDR4 form factor for higher performance computing platforms with flexible configuration options.

 **Note:** The Intel® Optane™ Persistent Memory (PMem) 200 Series are supported by 3rd Gen Intel Xeon Scalable (83xx/63xx/53xx/4314 Series) Processors.

## Chapter 2

# Installation

### 2.1 Static-Sensitive Devices

Electrostatic Discharge (ESD) can damage electronic components. To avoid damaging your motherboard, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

#### Precautions

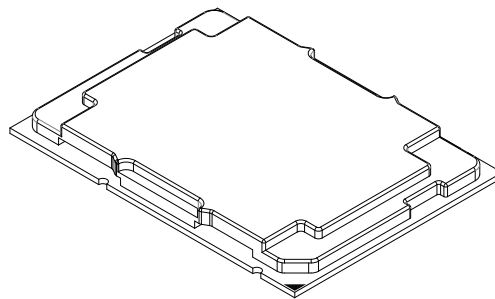
- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing the motherboard from the antistatic bag.
- Handle the motherboard by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure that your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.
- Use only the correct type of onboard CMOS battery. Do not install the onboard battery upside down to avoid a possible battery explosion.

## 2.2 Processor and Heatsink Installation

The processor (CPU) and processor carrier should be assembled together first to form the processor carrier assembly. This assembly will be then attached to the heatsink to form the processor heatsink module (PHM) before being installed into the CPU socket. Before installation, be sure to perform the following steps below:

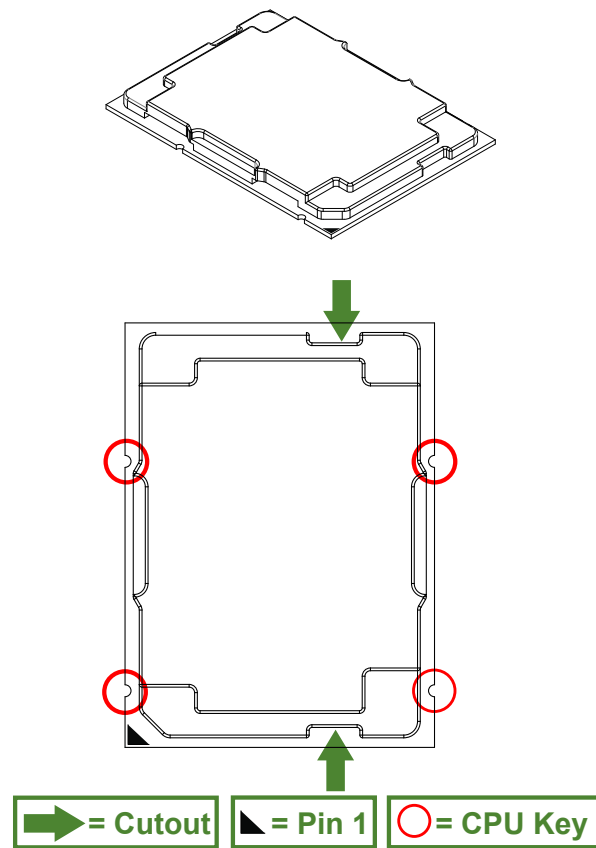
- Please carefully follow the instructions given on the previous page to avoid ESD-related damages.
- Unplug the AC power cords from all power supplies after shutting down the system.
- Check that the plastic protective cover is on the CPU socket, and none of the socket pins are bent. If they are, contact your retailer.
- When handling the processor, avoid touching or placing direct pressure on the LGA lands (gold contacts). Improper installation or socket misalignment can cause serious damage to the processor or CPU socket, which may require manufacturer repairs.
- Thermal grease is pre-applied on a new heatsink. No additional thermal grease is needed.
- Refer to the Supermicro website for updates on processor and memory support.
- All graphics in this manual are for illustrations only. Your components may look different.

### The 3rd Gen Intel Xeon Scalable Processor



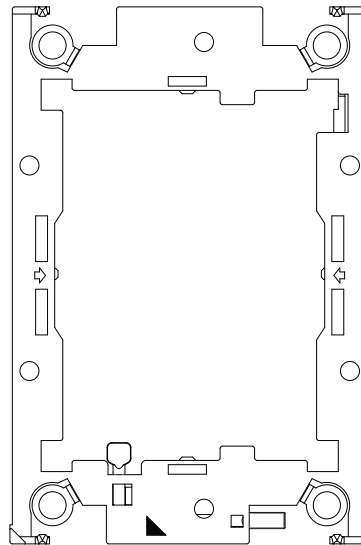
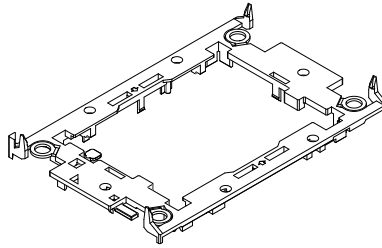
Processor Top View

### 1. The 3rd Gen Intel Xeon Scalable Processor

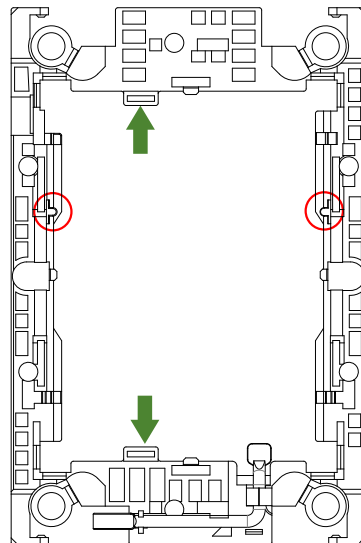


Processor Top View

## 2. The Processor Carrier

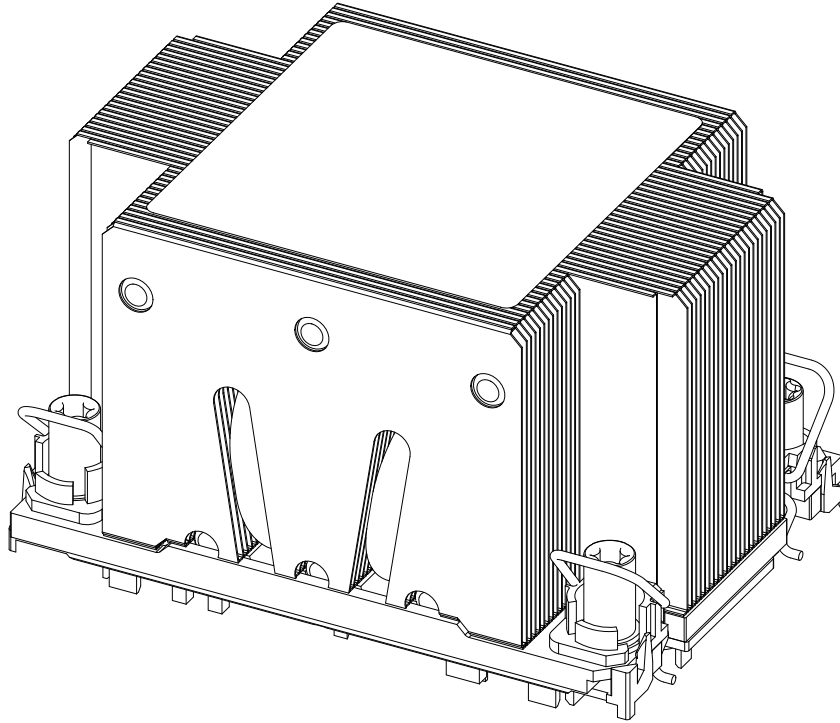


**Carrier Top View**



**Carrier Bottom View**

### 3. Heatsink

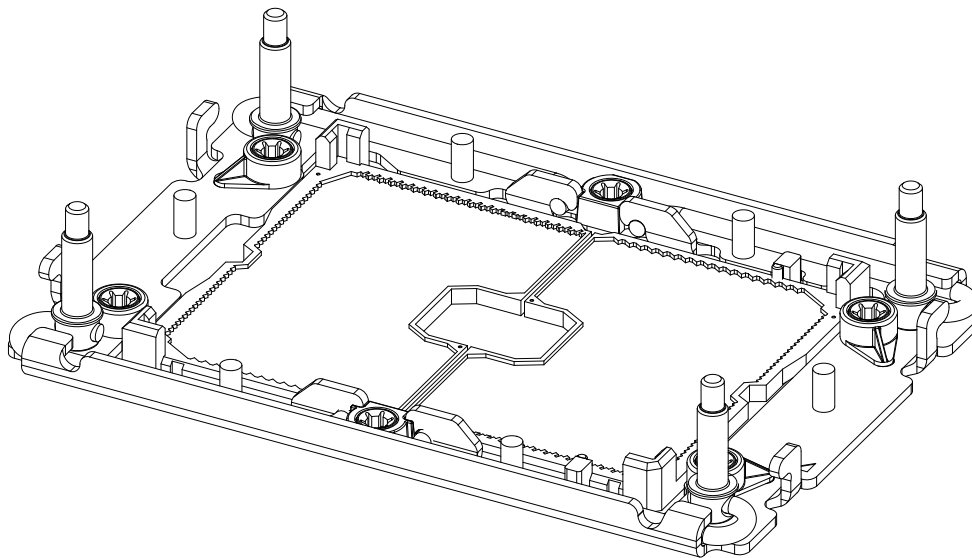
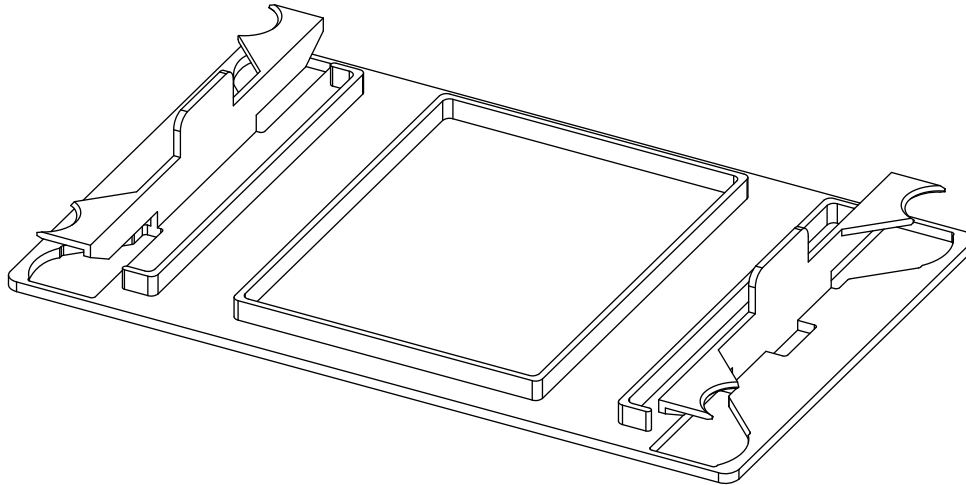


 **Note:** Exercise extreme care when handling the heatsink. Pay attention to the edges of heatsink fins, which can be sharp! To avoid damaging the heatsink, please do not apply excessive force on the fins.

## Overview of the CPU Socket

The CPU socket is protected by a plastic protective cover.

**Plastic Protective Cover**



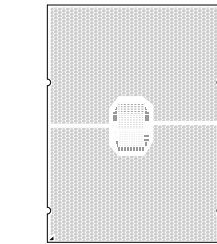
**CPU Socket**

## Overview of the Processor Carrier Assembly

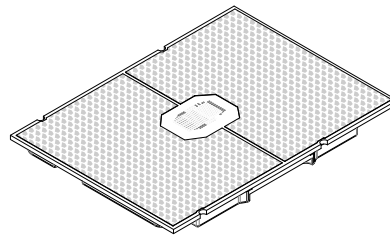
The processor carrier assembly contains a 3rd Gen Intel Xeon Scalable processor and a processor carrier. Carefully follow the instructions given in the installation section to place a processor into the carrier to create a processor carrier.

### 1. The 3rd Gen Intel Xeon Scalable Processor

Intel Processor (Component Side)



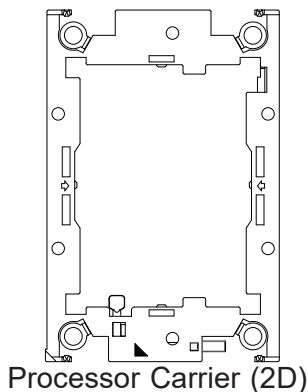
Processor (2D)



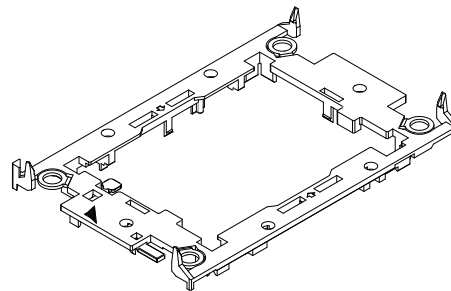
Processor (3D)

### 2. Processor Carrier

Intel Processor Carrier Top View)



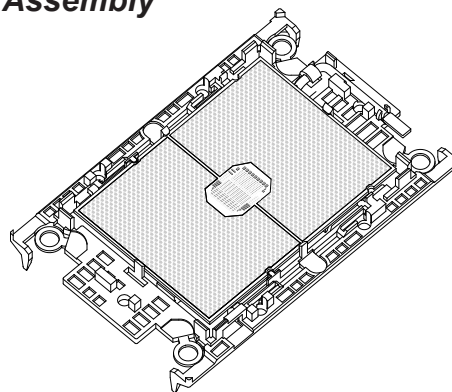
Processor Carrier (2D)



Processor Carrier (3D)



### 3. Processor Carrier Assembly

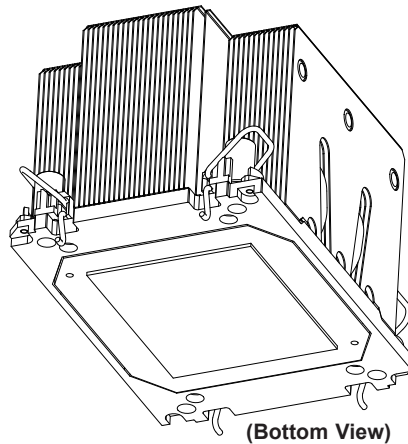


(with a Processor Seated inside the Carrier)

## Overview of the Processor Heatsink Module (PHM)

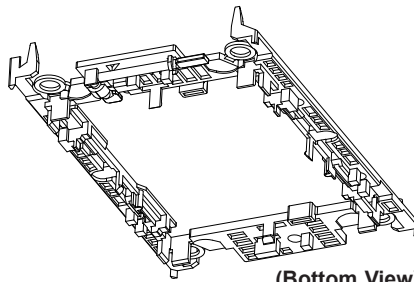
The Processor Heatsink Module (PHM) contains a heatsink, a processor carrier, and a 3rd Gen Intel Xeon Scalable processor.

### 1. Heatsink



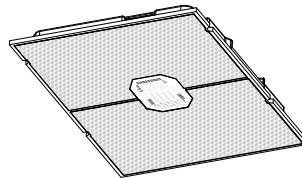
(Bottom View)

### 2. Processor Carrier E1A



(Bottom View)

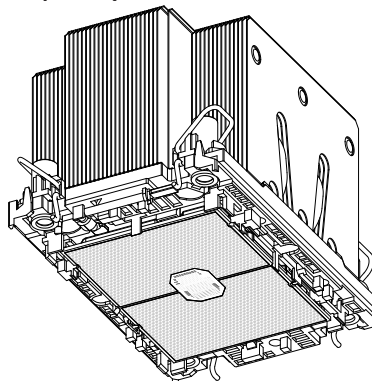
### 3. The 3rd Gen Intel Xeon Scalable Processor (SP XCC)



(Bottom View)



### 4. Processor Heatsink Module (PHM)



(Bottom View)

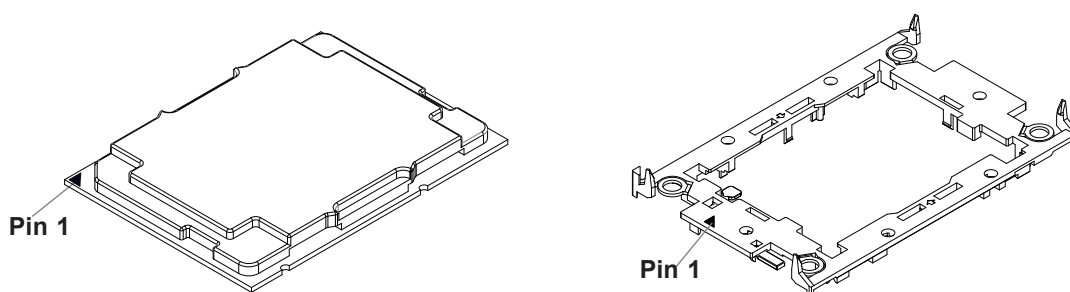
## Creating the Processor Carrier Assembly

The processor carrier assembly contains a 3rd Gen Intel Xeon Scalable processor and a processor carrier.

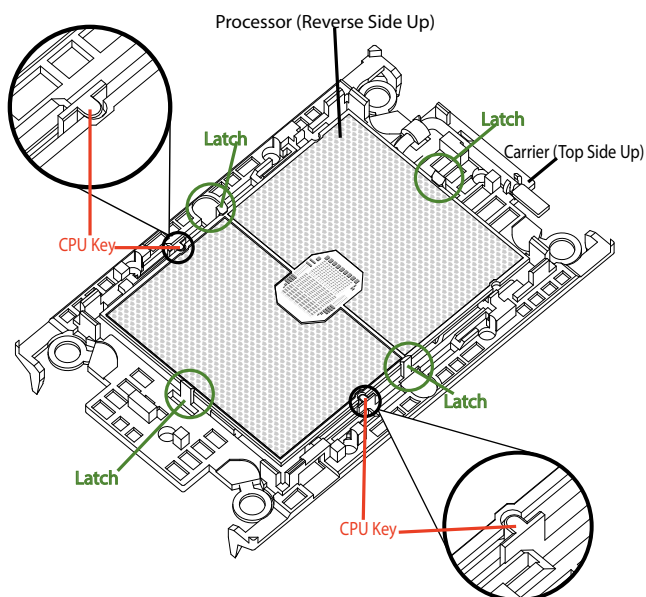
To create the processor carrier assembly, please follow the steps below:

 **Note:** Before installation, be sure to follow the instructions given on pages 1 and 2 of this chapter to properly prepare for installation.

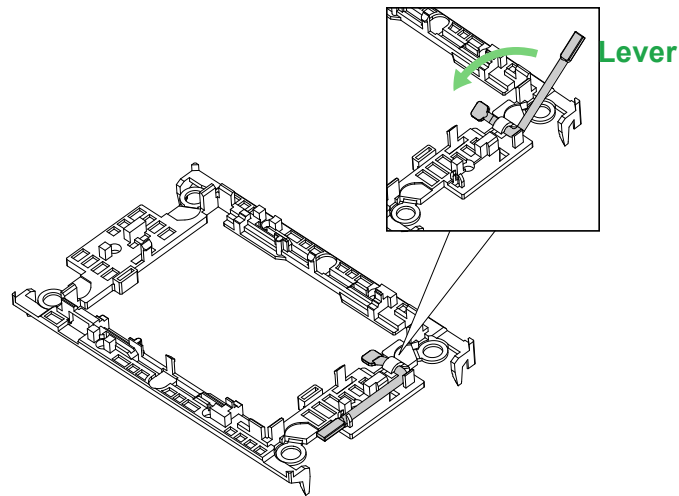
1. Hold the processor with the component side (including the gold contacts) facing down. Locate the small, gold triangle at the corner of the processor and the corresponding hollowed triangle on the processor carrier as shown in the graphics below. Please note that the triangle indicates the Pin 1 location.



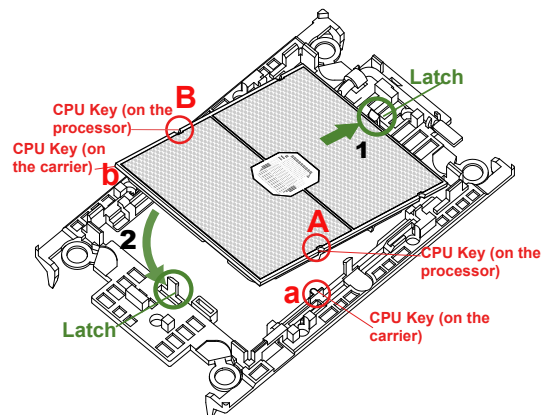
2. First, turn over the processor carrier and locate Pin 1 on the CPU and Pin 1 on the carrier. Then, turn the processor over with component side (including the gold contacts) facing up and locate CPU keys on the processor. Finally, locate the CPU keys and four latches on the carrier as shown below.



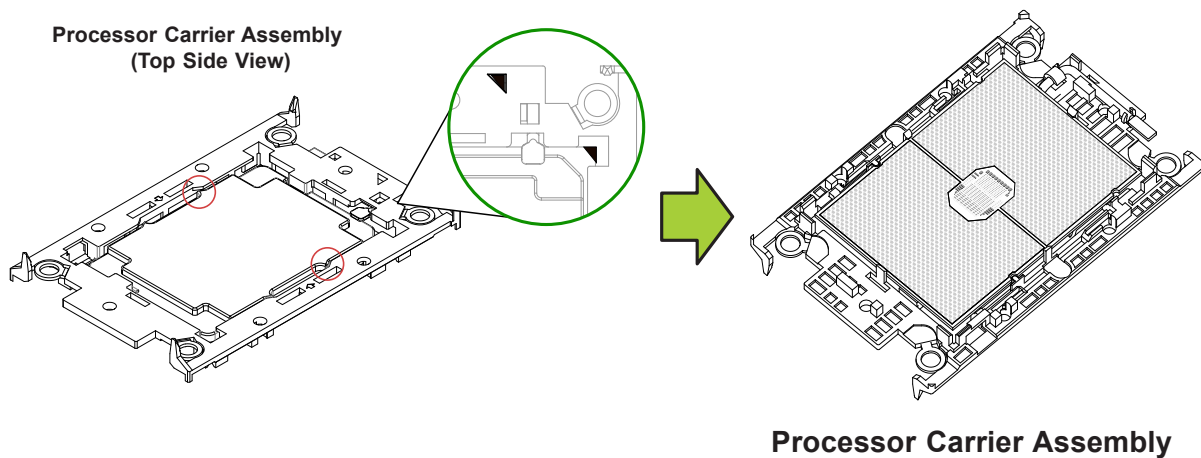
3. Locate the lever on the CPU socket and press it down as shown below.



4. Using Pin 1 as a guide, carefully align the CPU keys (A and B) on the processor against the CPU keys on the carrier (a and b) as shown in the drawing below.
5. Once they are properly aligned, carefully insert the CPU into the carrier, making sure that the CPU is properly secured by latches 1, 2, 3, and 4.



6. After the processor is placed inside the carrier, examine the four sides of the processor, making sure that the processor is properly seated on the carrier.

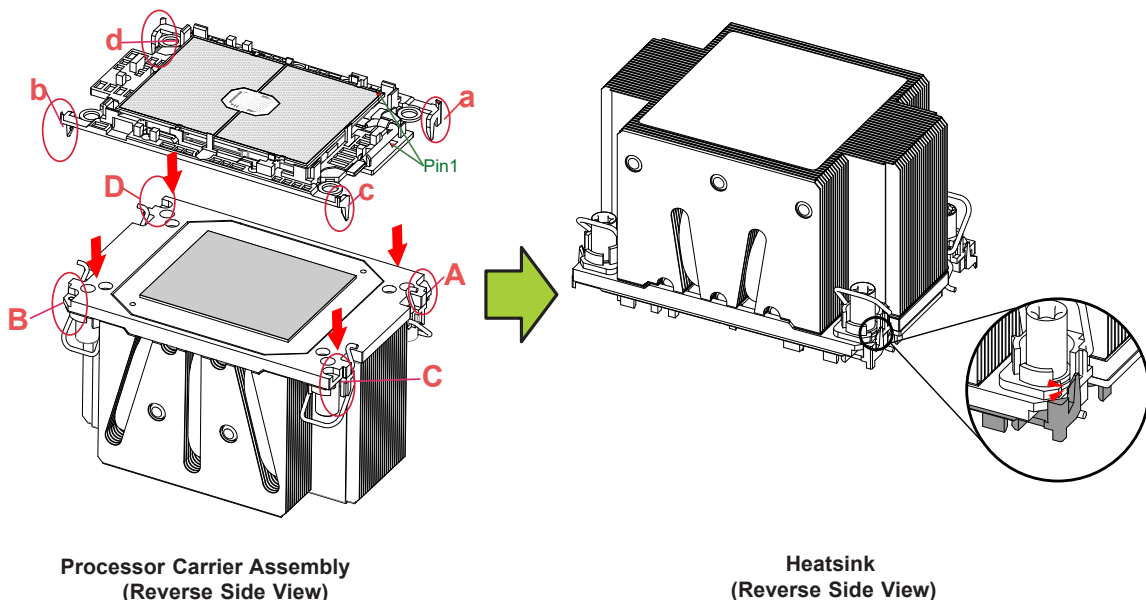


## Creating the Processor Heatsink Module (PHM)

After creating the processor carrier assembly, please follow the instructions below to mount the processor carrier into the heatsink to form the PHM.

 **Note:** If this is a new heatsink, the thermal grease has been pre-applied on the underside. Otherwise, apply the proper amount of thermal grease.

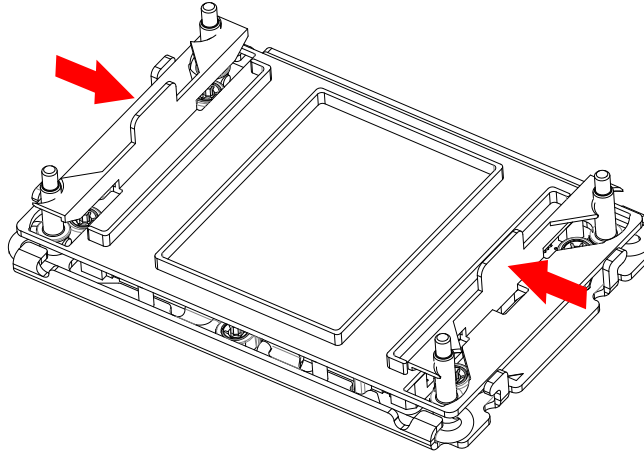
1. Turn the heatsink over with the thermal grease, which is on the reverse side of the heatsink, facing up. Pay attention to the two triangle cutouts (A, B) located at the diagonal corners of the heatsink as shown in the drawing below.
2. Hold the processor carrier component side facing up, and locate the triangle on the CPU and the triangle on the carrier. (Triangle indicates Pin 1.)
3. Using Pin 1 as a guide, turn the processor carrier assembly over with the gold contacts facing up. Locate Pin 1 (A) on the processor and Pin 1 (a) on the processor carrier assembly.
4. Align the corner marked a on the processor carrier assembly against the triangle cutout A on the heatsink, and align the corners marked b, c, and d on the processor assembly against the corners marked B, C, and D on the heatsinks.
5. Once they are properly aligned, place the corners marked a, b, c, and d on the processor carrier assembly into the corners of the heatsink marked A, B, C, and D making sure that all plastic clips are properly attached to the heatsink.



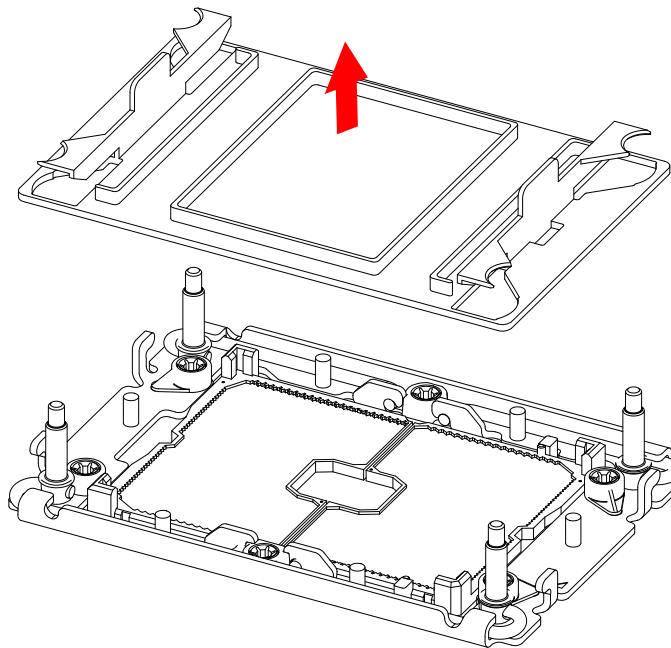
## Preparing the CPU Socket for Installation

This motherboard comes with a plastic protective cover installed on the CPU socket. Remove it from the socket by following the instructions below:

1. Press the tabs inward.



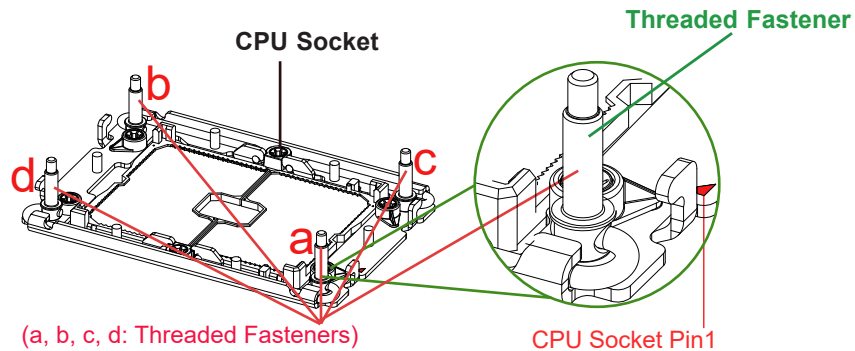
2. Pull up the protective cover from the socket.



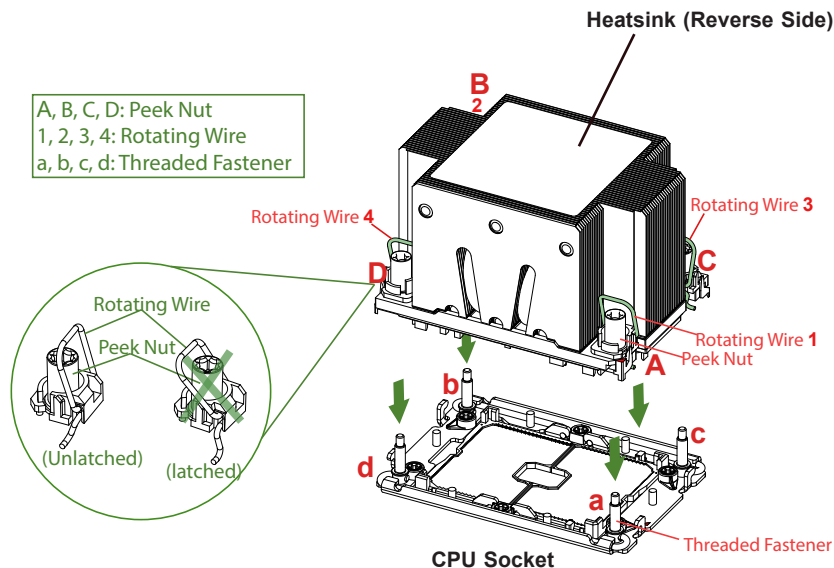
## Preparing to Install the PHM into the CPU Socket

After assembling the Processor Heatsink Module, you are ready to install it into the CPU socket. To ensure the proper installation, please follow the procedures below:

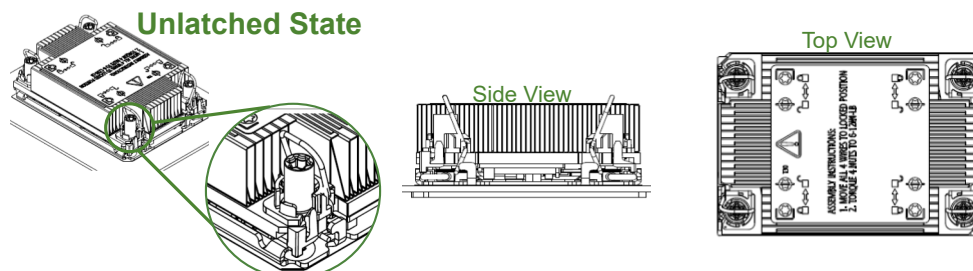
1. Locate four threaded fasteners (a, b, c, d) on the CPU socket.



2. Locate four peek nuts (A, B, C, D) and four rotating wires (1, 2, 3, 4) on the heatsink as shown in the graphics below.

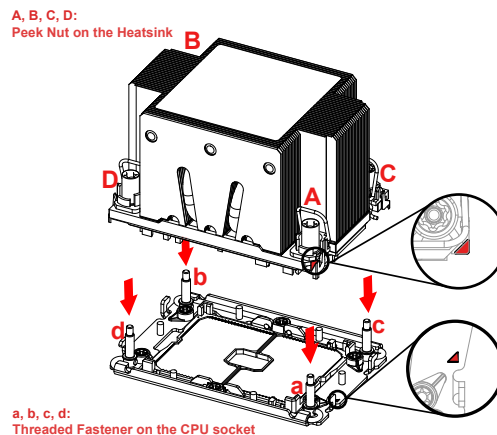


3. Check the rotating wires (1, 2, 3, 4) to make sure that they are at unlatched positions as shown in the drawing below before installing the PHM into the CPU socket.

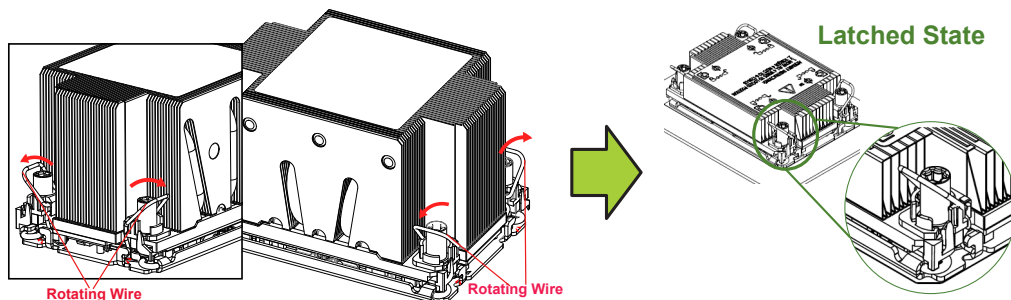


## Installing the PHM into the CPU Socket

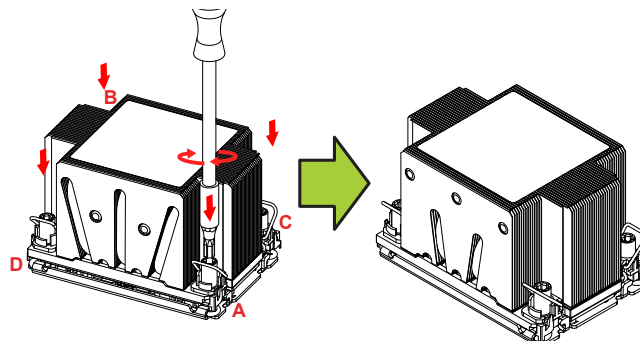
1. Align peek nut A, which is next to the triangle (Pin 1) on the heatsink, against threaded fastener a on the CPU socket. Then align peek nuts B, C, and D on the heatsink against threaded fasteners b, c, and d on the CPU socket, making sure that all peek nuts on the heatsink are properly aligned with the correspondent threaded fasteners on the CPU socket.
2. Once they are aligned, gently place the heatsink on top the CPU socket, making sure that each peek nut is properly attached to its corresponding threaded fastener.



3. Press all four rotating wires outwards and make sure that the heatsink is securely latched onto the CPU socket.



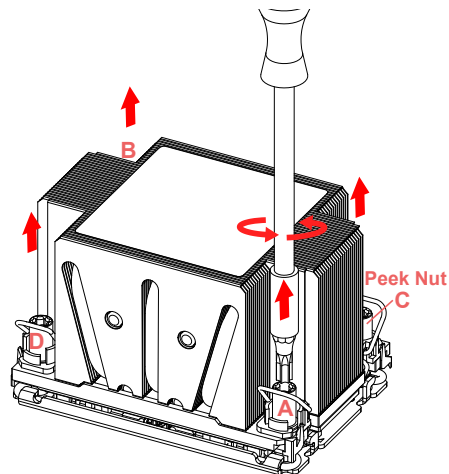
4. With a T30 screwdriver, tighten all peek nuts in the sequence of A, B, C, and D with even pressure. To avoid damaging the processor or socket, do not use excessive force when tightening the peek nuts. (For best durability, 8in-lbf torque is recommended.)
5. Examine all corners of the heatsink to ensure that the PHM is firmly attached to the CPU socket.



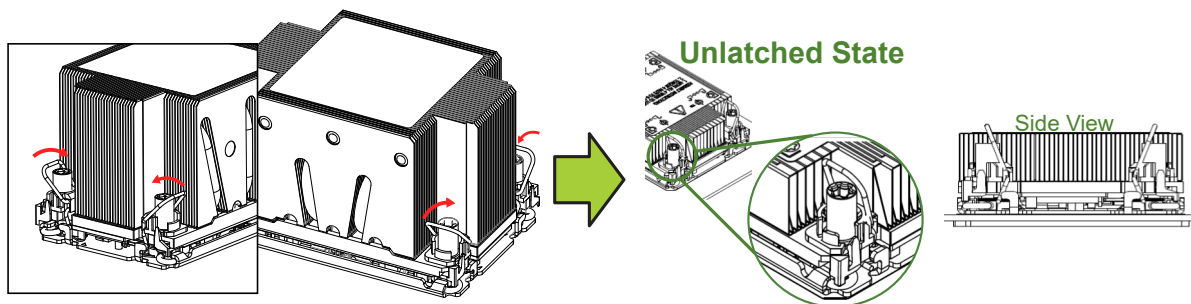
## Removing the PHM from the CPU Socket

Before removing the PHM from the motherboard, be sure to shut down the system and unplug the power cables from the power supply. Then follow the steps below:

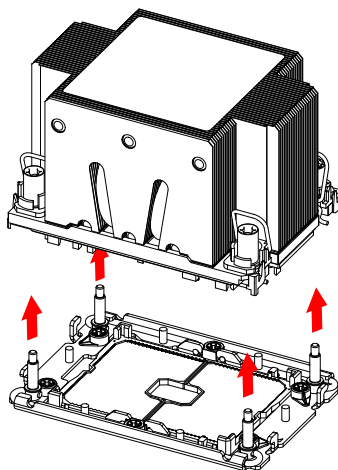
1. Use a T30 screwdriver to loosen the four peek nuts on the heatsink in the sequence of A, B, C, and D.



2. Once the peek nuts are loosened from the CPU socket, press the rotating wires inwards to unlatch the PHM from the socket as shown in the drawings below.



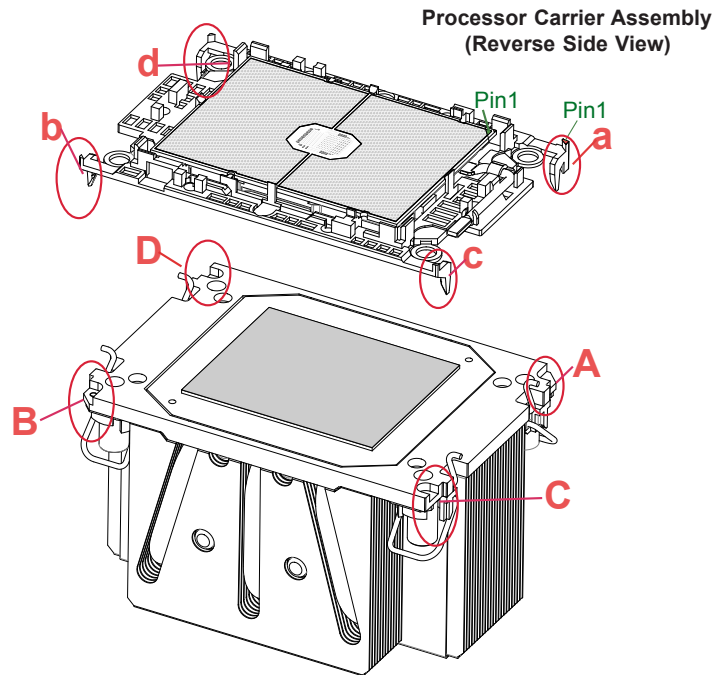
3. Gently pull the PHM upwards to remove it from the CPU socket.



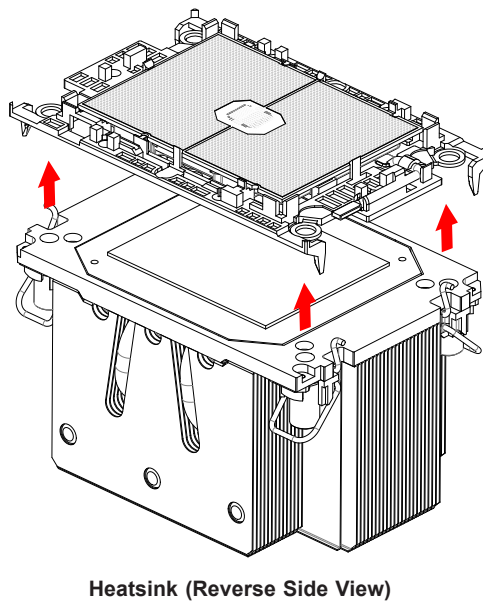
## Removing the Processor Carrier Assembly from the PHM

To remove the processor carrier assembly from the PHM, please follow the steps below:

1. Detach the four plastic clips (marked a, b, c, d) on the processor carrier assembly from the four corners of the heatsink (marked A, B, C, D) as shown in the drawings below.



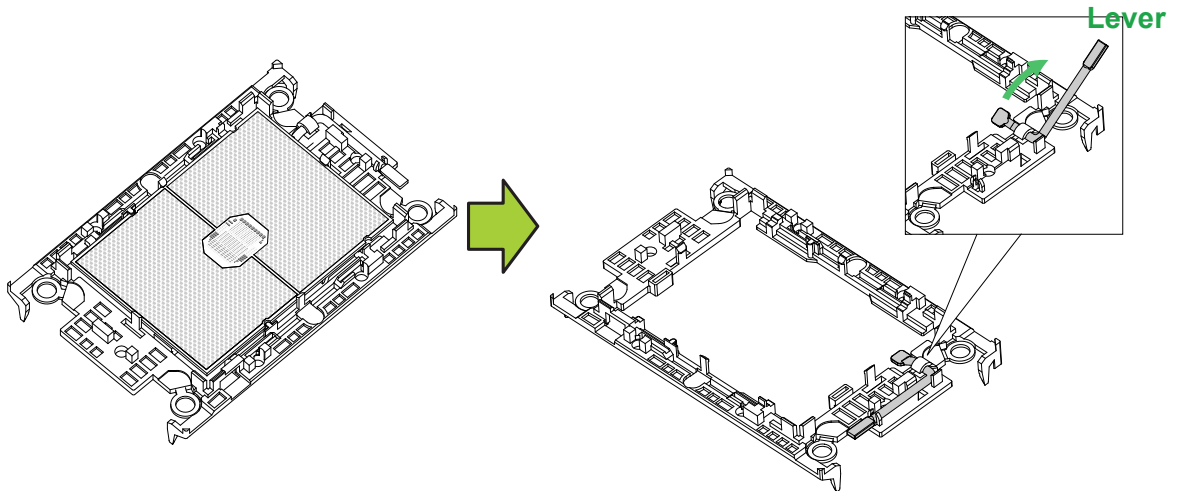
2. When all plastic clips are detached from the heatsink, remove the processor carrier assembly from the heatsink.



## Removing the Processor from the Processor Carrier Assembly

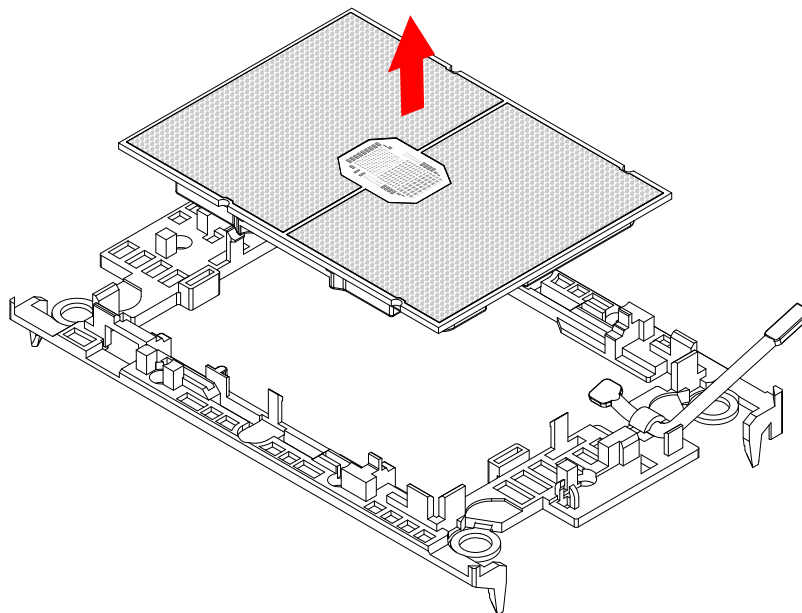
Once you have removed the processor carrier assembly from the PHM, you are ready to remove the processor from the processor carrier by following the steps below.

1. Unlock the lever from its locked position and push the lever upwards to disengage the processor from the processor carrier as shown in the drawing on the right below.



2. Once the processor is loosened from the carrier, carefully remove the processor from the processor carrier.

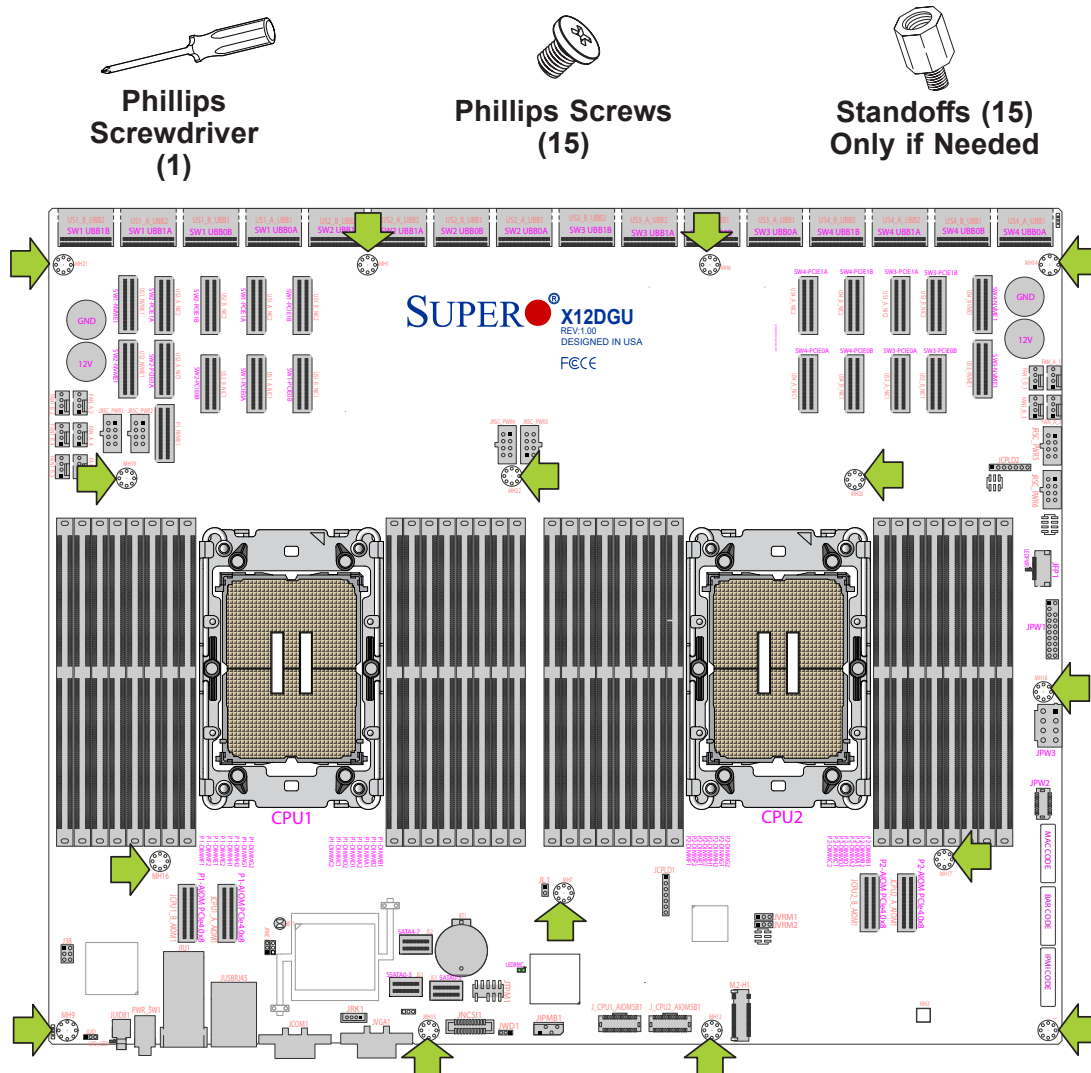
 **Note:** Please handle the processor with care to avoid damaging the processor and its pins.



## 2.3 Motherboard Installation

Make sure that the locations of all the mounting holes for both the motherboard and the chassis match. Make sure that the metal standoffs click in or are screwed in tightly.

### Tools Needed



### Location of Mounting Holes

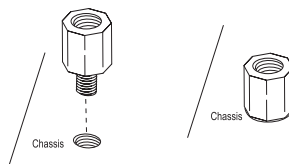
**Note 1:** To avoid damaging the motherboard and its components, please do not use a force greater than 8 lbf-in on each mounting screw during motherboard installation.

**Note 2:** Some components are very close to the mounting holes. Please take precautionary measures to avoid damaging these components when installing the motherboard to the chassis.

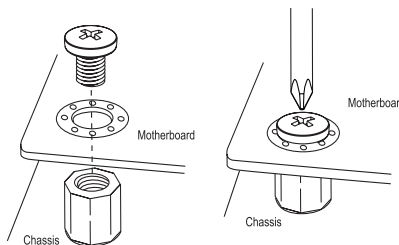
## Installing the Motherboard

1. Install the I/O shield into the back of the chassis, if applicable.

2. Locate the mounting holes on the motherboard. See the previous page for the location.



3. Locate the matching mounting holes on the chassis. Align the mounting holes on the motherboard against the mounting holes on the chassis.



4. Install standoffs in the chassis as needed.
5. Install the motherboard into the chassis carefully to avoid damaging other motherboard components.
6. Using the Phillips screwdriver, insert a pan head #6 screw into a mounting hole on the motherboard and its matching mounting hole on the chassis.
7. Repeat Step 5 to insert #6 screws into all mounting holes.
8. Make sure that the motherboard is securely placed in the chassis.

 **Note:** Images displayed are for illustration only. Your chassis or components might look different from those shown in this manual.

## 2.4 Memory Support and Installation



**Note:** Check the Supermicro website for recommended memory modules.



**Important:** Exercise extreme care when installing or removing memory modules to prevent any possible damage.

### Memory Support

The X12DGU supports up to 8TB 3DS LRDIMM/LRDIMM/3DS RDIMM/RDIMM DDR4 (288-pin) ECC memory with speeds of 3200/2933/2666 MHz in 32 memory slots and up to 8TB Intel Optane PMem 200 Series with speeds up to 3200 MHz. (See the notes below.)



**Note 1:** The Intel Optane Persistent Memory (PMem) 200 Series are supported by 3rd Gen Intel Xeon Scalable (83xx/63xx/53xx/4314 Series) Processors.

**Note 2:** Memory speed and capacity support depends on the processors used in the system.

### DDR4 Memory Support for 3rd Gen Intel Xeon Scalable Processors

| DDR4 Memory Support for the 3rd Gen Intel Xeon Scalable Processors |                             |                        |                         |                                                                                   |                              |
|--------------------------------------------------------------------|-----------------------------|------------------------|-------------------------|-----------------------------------------------------------------------------------|------------------------------|
| Type                                                               | Ranks Per DIMM & Data Width | DIMM Capacity (GB)     |                         | Speed (MT/s); Voltage (V);<br>Slots Per Channel (SPC) and DIMMs Per Channel (DPC) |                              |
|                                                                    |                             |                        |                         | 1DPC<br>(1-DIMM Per Channel)                                                      | 2DPC<br>(2-DIMM Per Channel) |
|                                                                    |                             | 8Gb                    | 16Gb                    | 1.2 V                                                                             | 1.2 V                        |
| RDIMM                                                              | SRx8                        | 8GB                    | 16GB                    | 3200                                                                              | 3200                         |
|                                                                    | SRx4                        | 16GB                   | 32GB                    |                                                                                   |                              |
|                                                                    | DRx8                        | 16GB                   | 32GB                    |                                                                                   |                              |
|                                                                    | DRx4                        | 32GB                   | 64GB                    |                                                                                   |                              |
| RDIMM 3Ds                                                          | (4R/8R) X4                  | 2H- 64 GB<br>4H-128 GB | 2H- 128 GB<br>4H-256 GB |                                                                                   |                              |
| LRDIMM                                                             | QRx4                        | 64GB                   | 128GB                   | 3200                                                                              | 3200                         |
| LRDIMM - 3Ds                                                       | (4R/8R) X4                  | 4H-128 GB              | 2H- 128 GB<br>4H-256 GB | 3200                                                                              | 3200                         |

| Key Parameters for DIMM Configurations |                                                                                                                                                                                  |
|----------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Parameters                             | Possible Values                                                                                                                                                                  |
| Number of Channels                     | 8                                                                                                                                                                                |
| Number of DIMMs per Channel            | 1DPC (1 DIMM Per Channel) or 2DPC (2 DIMMs Per Channel)                                                                                                                          |
| DIMM Type                              | RDIMM (w/ECC), 3DS RDIMM, LRDIMM, 3DS LRDIMM                                                                                                                                     |
| DIMM Construction                      | non-3DS RDIMM Raw Cards: A/B (2Rx4), C (1Rx4), D (1Rx8), E (2Rx8)<br>3DS RDIMM Raw Cards: A/B (4Rx4)<br>non-3DS LRDIMM Raw Cards: D/E (4Rx4)<br>3DS LRDIMM Raw Cards: A/B (8Rx4) |

## Memory Population Table for 3rd Gen Intel Xeon Scalable Processors

| DDR4 Memory Population Table for X12DP 32-DIMM Motherboards |                                                                                                                                                                                                                                                                                                                                                        |
|-------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>When 1 CPU is used:</b>                                  | <b>Memory Population Sequence</b>                                                                                                                                                                                                                                                                                                                      |
| 1 CPU & 1 DIMM                                              | CPU1: P1-DIMMA1                                                                                                                                                                                                                                                                                                                                        |
| 1 CPU & 2 DIMMs<br>(Note)                                   | CPU1: P1-DIMMA1/P1-DIMME1                                                                                                                                                                                                                                                                                                                              |
| 1 CPU & 4 DIMMs<br>(Note)                                   | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1                                                                                                                                                                                                                                                                                                          |
| 1 CPU & 6 DIMM                                              | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1/P1-DIMMB1/P1-DIMMF1                                                                                                                                                                                                                                                                                      |
| 1 CPU & 8 DIMMs<br>(Note)                                   | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1/P1-DIMMB1/P1-DIMMF1/P1-DIMMD1/P1-DIMMH1                                                                                                                                                                                                                                                                  |
| 1 CPU & 12 DIMMs<br>(Note)                                  | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1/P1-DIMMB1/P1-DIMMF1/P1-DIMMA2/P1-DIMME2/<br>P1-DIMMC2/P1-DIMMG2/P1-DIMMB2/P1-DIMMF2                                                                                                                                                                                                                      |
| 1 CPU & 16 DIMMs<br>(Note)                                  | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1/P1-DIMMB1/P1-DIMMF1/P1-DIMMD1/P1-DIMMH1/<br>P1-DIMMA2/P1-DIMME2/P1-DIMMC2/P1-DIMMG2/P1-DIMMB2/P1-DIMMF2/P1-DIMMD2/P1-DIMMH2                                                                                                                                                                              |
| <b>When 2 CPUs are used</b>                                 | <b>Memory Population Sequence</b>                                                                                                                                                                                                                                                                                                                      |
| 2 CPUs & 2 DIMMs<br>(Note)                                  | CPU1: P1-DIMMA1<br>CPU2: P2-DIMMA1                                                                                                                                                                                                                                                                                                                     |
| 2 CPUs & 4 DIMMs<br>(Note)                                  | CPU1: P1-DIMMA1/P1-DIMME1<br>CPU2: P2-DIMMA1/P2-DIMME1                                                                                                                                                                                                                                                                                                 |
| 2 CPUs & 6 DIMMs                                            | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1<br>CPU2: P2-DIMMA1/P2-DIMME1                                                                                                                                                                                                                                                                             |
| 2 CPUs & 8 DIMMs<br>(Note)                                  | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1<br>CPU2: P2-DIMMA1/P2-DIMME1/P2-DIMMC1/P2-DIMMG1                                                                                                                                                                                                                                                         |
| 2 CPUs & 10 DIMMs                                           | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1/P1-DIMMB1/P1-DIMMF1<br>CPU2: P2-DIMMA1/P2-DIMME1/P2-DIMMC1/P2-DIMMG1                                                                                                                                                                                                                                     |
| 2 CPUs & 12 DIMMs<br>(Note)                                 | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1/P1-DIMMB1/P1-DIMMF1<br>CPU2: P2-DIMMA1/P2-DIMME1/P2-DIMMC1/P2-DIMMG1/P2-DIMMB1/P2-DIMMF1                                                                                                                                                                                                                 |
| 2 CPUs & 14 DIMMs                                           | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1/P1-DIMMB1/P1-DIMMF1/P1-DIMMD1/P1-DIMMH1<br>CPU2: P2-DIMMA1/P2-DIMME1/P2-DIMMC1/P2-DIMMG1/P2-DIMMB1/P2-DIMMF1                                                                                                                                                                                             |
| 2 CPUs & 16 DIMMs<br>(Note)                                 | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1/P1-DIMMB1/P1-DIMMF1/P1-DIMMD1/P1-DIMMH1<br>CPU2: P2-DIMMA1/P2-DIMME1/P2-DIMMC1/P2-DIMMG1/P2-DIMMB1/P2-DIMMF1/P2-DIMMD1/P2-DIMMH1                                                                                                                                                                         |
| 2 CPUs & 18 DIMMs                                           | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1/P1-DIMMB1/P1-DIMMF1/P1-DIMMA2/P1-DIMME2/<br>P1-DIMMC2/P1-DIMMG2/P1-DIMMB2/P1-DIMMF2<br>CPU2: P2-DIMMA1/P2-DIMME1/P2-DIMMC1/P2-DIMMG1/P2-DIMMB1/P2-DIMMF1                                                                                                                                                 |
| 2 CPUs & 20 DIMMs                                           | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1/P1-DIMMB1/P1-DIMMF1/P1-DIMMA2/P1-DIMME2/<br>P1-DIMMC2/P1-DIMMG2/P1-DIMMB2/P1-DIMMF2<br>CPU2: P2-DIMMA1/P2-DIMME1/P2-DIMMC1/P2-DIMMG1/P2-DIMMB1/P2-DIMMF1/P2-DIMMD1/P2-DIMMH1                                                                                                                             |
| 2 CPUs & 22 DIMMs                                           | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1/P1-DIMMB1/P1-DIMMF1/P1-DIMMD1/P1-DIMMH1/<br>P1-DIMMA2/P1-DIMME2/P1-DIMMC2/P1-DIMMG2/P1-DIMMB2/P1-DIMMF2/P1-DIMMD2/P1-DIMMH2<br>CPU2: P2-DIMMA1/P2-DIMME1/P2-DIMMC1/P2-DIMMG1/P2-DIMMB1/P2-DIMMF1                                                                                                         |
| 2 CPUs & 24 DIMMs<br>(Note)                                 | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1/P1-DIMMB1/P1-DIMMF1/P1-DIMMD1/P1-DIMMH1/<br>P1-DIMMA2/P1-DIMME2/P1-DIMMC2/P1-DIMMG2/P1-DIMMB2/P1-DIMMF2/P1-DIMMD2/P1-DIMMH2<br>CPU2: P2-DIMMA1/P2-DIMME1/P2-DIMMC1/P2-DIMMG1/P2-DIMMB1/P2-DIMMF1/P2-DIMMD1/P2-DIMMH1                                                                                     |
| 2 CPUs & 28 DIMMs                                           | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1/P1-DIMMB1/P1-DIMMF1/P1-DIMMD1/P1-DIMMH1/<br>P1-DIMMA2/P1-DIMME2/P1-DIMMC2/P1-DIMMG2/P1-DIMMB2/P1-DIMMF2/P1-DIMMD2/P1-DIMMH2<br>CPU2: P2-DIMMA1/P2-DIMME1/P2-DIMMC1/P2-DIMMG1/P2-DIMMB1/P2-DIMMF1/P2-DIMMD1/P2-DIMMH1/<br>P2-DIMMA2/P2-DIMME2/P2-DIMMC2/P2-DIMMG2                                         |
| 2 CPUs & 32 DIMMs<br>(Note)                                 | CPU1: P1-DIMMA1/P1-DIMME1/P1-DIMMC1/P1-DIMMG1/P1-DIMMB1/P1-DIMMF1/P1-DIMMD1/P1-DIMMH1/<br>P1-DIMMA2/P1-DIMME2/P1-DIMMC2/P1-DIMMG2/P1-DIMMB2/P1-DIMMF2/P1-DIMMD2/P1-DIMMH2<br>CPU2: P2-DIMMA1/P2-DIMME1/P2-DIMMC1/P2-DIMMG1/P2-DIMMB1/P2-DIMMF1/P2-DIMMD1/P2-DIMMH1/<br>P2-DIMMA2/P2-DIMME2/P2-DIMMC2/P2-DIMMG2/P2-DIMMB2/P2-DIMMF2/P2-DIMMD2/P2-DIMMH2 |



**Note:** This memory configuration is recommended by Supermicro for optimal memory performance. Please use this configuration to maximize your memory performance.

## PMem 200 Series Population table for X12DGU Motherboards (with 32 Slots)

 **Note:** The Intel® Optane™ Persistent Memory (PMem) 200 Series are supported by 3rd Gen Intel Xeon Scalable (83xx/63xx/53xx/4314 Series) Processors.

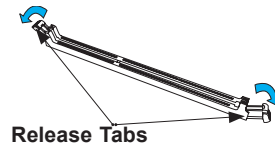
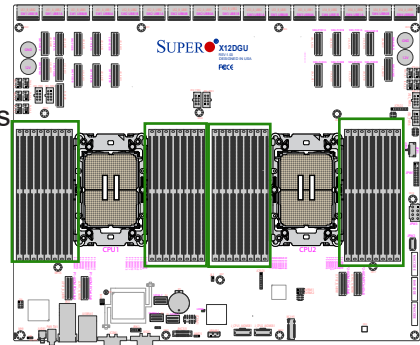
| PMem 200 Series Population Table for X12DP 32-DIMM Motherboards (within 1 CPU socket) |        |                |           |           |           |           |           |           |           |           |           |           |           |           |           |           |           |           |      |
|---------------------------------------------------------------------------------------|--------|----------------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|------|
| DDR4+ PMem                                                                            | Mode   | AD Inter-leave | P1-DIMMF1 | P1-DIMMF2 | P1-DIMME1 | P1-DIMME2 | P1-DIMMH1 | P1-DIMMH2 | P1-DIMMG1 | P1-DIMMG2 | P1-DIMMC2 | P1-DIMMC1 | P1-DIMMD2 | P1-DIMMD1 | P1-DIMMA2 | P1-DIMMA1 | P1-DIMMB2 | P1-DIMMB1 |      |
| 4+4                                                                                   | AD MM  | One - x4       | PMem      | -         | DDR4      | -         | PMem      | -         | DDR4      | -         | -         | DDR4      | -         | PMem      | -         | DDR4      | -         | PMem      |      |
|                                                                                       |        | One - x4       | DDR4      | -         | PMem      | -         | DDR4      | -         | PMem      | -         | -         | PMem      | -         | DDR4      | -         | PMem      | -         | DDR4      |      |
| 6+1                                                                                   | AD     | One - x1       | DDR4      | -         | DDR4      | -         | -         | -         | DDR4      | -         | -         | DDR4      | -         | PMem      | -         | DDR4      | -         | DDR4      |      |
|                                                                                       |        |                | -         | -         | DDR4      | -         | DDR4      | -         | DDR4      | -         | -         | DDR4      | -         | DDR4      | -         | DDR4      | -         | PMem      |      |
|                                                                                       |        |                | DDR4      | -         | DDR4      | -         | PMem      | -         | DDR4      | -         | -         | DDR4      | -         | -         | -         | DDR4      | -         | DDR4      |      |
|                                                                                       |        |                | PMem      | -         | DDR4      | -         | DDR4      | -         | DDR4      | -         | -         | DDR4      | -         | DDR4      | -         | DDR4      | -         | -         |      |
|                                                                                       |        |                | DDR4      | -         | DDR4      | -         | DDR4      | -         | -         | -         | -         | PMem      | -         | DDR4      | -         | DDR4      | -         | DDR4      |      |
|                                                                                       |        |                | DDR4      | -         | -         | -         | DDR4      | -         | DDR4      | -         | -         | DDR4      | -         | DDR4      | -         | PMem      | -         | DDR4      |      |
|                                                                                       |        |                | DDR4      | -         | DDR4      | -         | DDR4      | -         | PMem      | -         | -         | -         | DDR4      | -         | DDR4      | -         | DDR4      | -         | DDR4 |
|                                                                                       |        |                | DDR4      | -         | PMem      | -         | DDR4      | -         | DDR4      | -         | -         | DDR4      | -         | DDR4      | -         | -         | -         | -         | DDR4 |
| 8+1                                                                                   | AD     | One - x1       | DDR4      | -         | DDR4      | -         | DDR4      | -         | DDR4      | -         | -         | DDR4      | -         | DDR4      | PMem      | DDR4      | -         | DDR4      |      |
|                                                                                       |        |                | DDR4      | -         | DDR4      | -         | DDR4      | -         | DDR4      | -         | PMem      | DDR4      | -         | DDR4      | -         | DDR4      | -         | DDR4      |      |
|                                                                                       |        |                | DDR4      | -         | DDR4      | PMem      | DDR4      | -         | DDR4      | -         | -         | DDR4      | -         | DDR4      | -         | DDR4      | -         | DDR4      |      |
|                                                                                       |        |                | DDR4      | -         | DDR4      | -         | DDR4      | -         | DDR4      | PMem      | -         | DDR4      | -         | DDR4      | -         | DDR4      | -         | DDR4      |      |
|                                                                                       |        |                | DDR4      | -         | DDR4      | -         | DDR4      | -         | DDR4      | -         | -         | DDR4      | PMem      | DDR4      | -         | DDR4      | -         | DDR4      |      |
|                                                                                       |        |                | DDR4      | -         | DDR4      | -         | DDR4      | -         | DDR4      | -         | -         | DDR4      | -         | DDR4      | -         | DDR4      | PMem      | DDR4      |      |
|                                                                                       |        |                | DDR4      | PMem      | DDR4      | -         | DDR4      | -         | DDR4      | -         | -         | DDR4      | -         | DDR4      | -         | DDR4      | -         | DDR4      |      |
|                                                                                       |        |                | DDR4      | -         | DDR4      | -         | DDR4      | PMem      | DDR4      | -         | -         | DDR4      | -         | DDR4      | -         | DDR4      | -         | DDR4      |      |
| 8+4                                                                                   | AD MM  | One - x4       | DDR4      | -         | DDR4      | PMem      | DDR4      | -         | DDR4      | PMem      | PMem      | DDR4      | -         | DDR4      | PMem      | DDR4      | -         | DDR4      |      |
|                                                                                       |        | Two - x2       | DDR4      | -         | DDR4      | PMem      | DDR4      | PMem      | DDR4      | -         | -         | DDR4      | PMem      | DDR4      | PMem      | DDR4      | -         | DDR4      |      |
|                                                                                       |        | Two - x2       | DDR4      | PMem      | DDR4      | -         | DDR4      | -         | DDR4      | PMem      | PMem      | DDR4      | -         | DDR4      | -         | DDR4      | PMem      | DDR4      |      |
|                                                                                       |        | One - x4       | DDR4      | PMem      | DDR4      | -         | DDR4      | PMem      | DDR4      | -         | -         | DDR4      | PMem      | DDR4      | -         | DDR4      | PMem      | DDR4      |      |
| 8+8                                                                                   | AD, MM | One - x8       | DDR4      | PMem      | DDR4      | PMem      | DDR4      | PMem      | DDR4      | PMem      | PMem      | DDR4      | PMem      | DDR4      | PMem      | DDR4      | PMem      | DDR4      |      |
| 12+2                                                                                  | AD     | One - x2       | PMem      | -         | DDR4      | DDR4      | DDR4      | DDR4      | DDR4      | DDR4      | DDR4      | DDR4      | DDR4      | DDR4      | DDR4      | -         | PMem      |           |      |
|                                                                                       |        |                | DDR4      | DDR4      | DDR4      | DDR4      | PMem      | -         | DDR4      | DDR4      | DDR4      | DDR4      | DDR4      | -         | PMem      | DDR4      | DDR4      | DDR4      |      |
|                                                                                       |        |                | DDR4      | DDR4      | PMem      | -         | DDR4      | DDR4      | DDR4      | DDR4      | DDR4      | DDR4      | DDR4      | -         | PMem      | DDR4      | DDR4      | DDR4      |      |
|                                                                                       |        |                | DDR4      | DDR4      | DDR4      | DDR4      | DDR4      | DDR4      | PMem      | -         | -         | PMem      | DDR4      | DDR4      | DDR4      | DDR4      | DDR4      | DDR4      |      |

| Legend (for the table above) |                                                                     |
|------------------------------|---------------------------------------------------------------------|
| DDR4 Type and Capacity       |                                                                     |
| DDR4                         | See Validation Matrix (DDR4 DIMMs validated with PMem)              |
| Capacity                     |                                                                     |
| PMem                         | Any Capacity (Uniformly for all channels for a given configuration) |

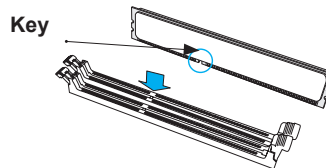
- Mode definitions: AD = App Direct Mode, MM = Memory Mode.
- No mixing of PMem and NVDIMMs within the platform.
- For MM, NM/FM ratio is between 1:4 and 1:16. (NM = Near Memory (DRAM); FM = Far Memory (PMem)).
- Matrix targets configs for optimized PMem to DRAM cache ratio in MM mode.
- For each individual population, different PMem rearrangements among channels are permitted so long as the configuration doesn't break X12DP Memory population rules.
- Ensure the same DDR4 DIMM type and capacity are used for each DDR4 + PMem population.
- If the system detects an unvalidated configuration, then the system issues a BIOS warning. The CLI functionality is limited in non-POR configurations, and select commands will not be supported.

## DIMM Installation

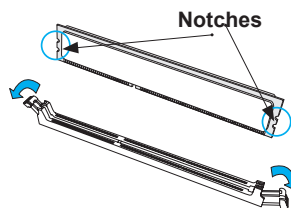
1. Insert the desired number of DIMMs into the memory slots based on the recommended DIMM population tables in the previous section. Locate DIMM memory slots on the motherboard as shown on the right.
2. Push the release tabs outwards on both ends of the DIMM slot to unlock it.



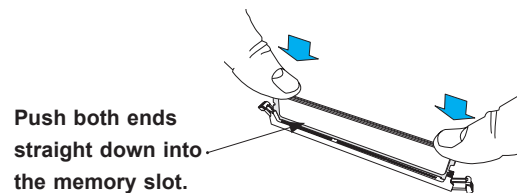
3. Align the key of the memory module with the DIMM socket key on the memory slot.



4. Align the notches on both ends of the module against the latches on the ends of the slot.

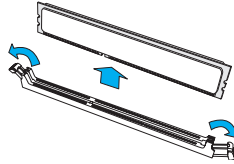


5. Push both ends of the module straight down into the slot until the module snaps into place.
6. Press the release tabs to the lock positions to secure the memory module into the slot.



## DIMM Removal

Press both release tabs on the ends of the memory module to unlock it. Once the memory module has been loosened, remove it from the memory slot.

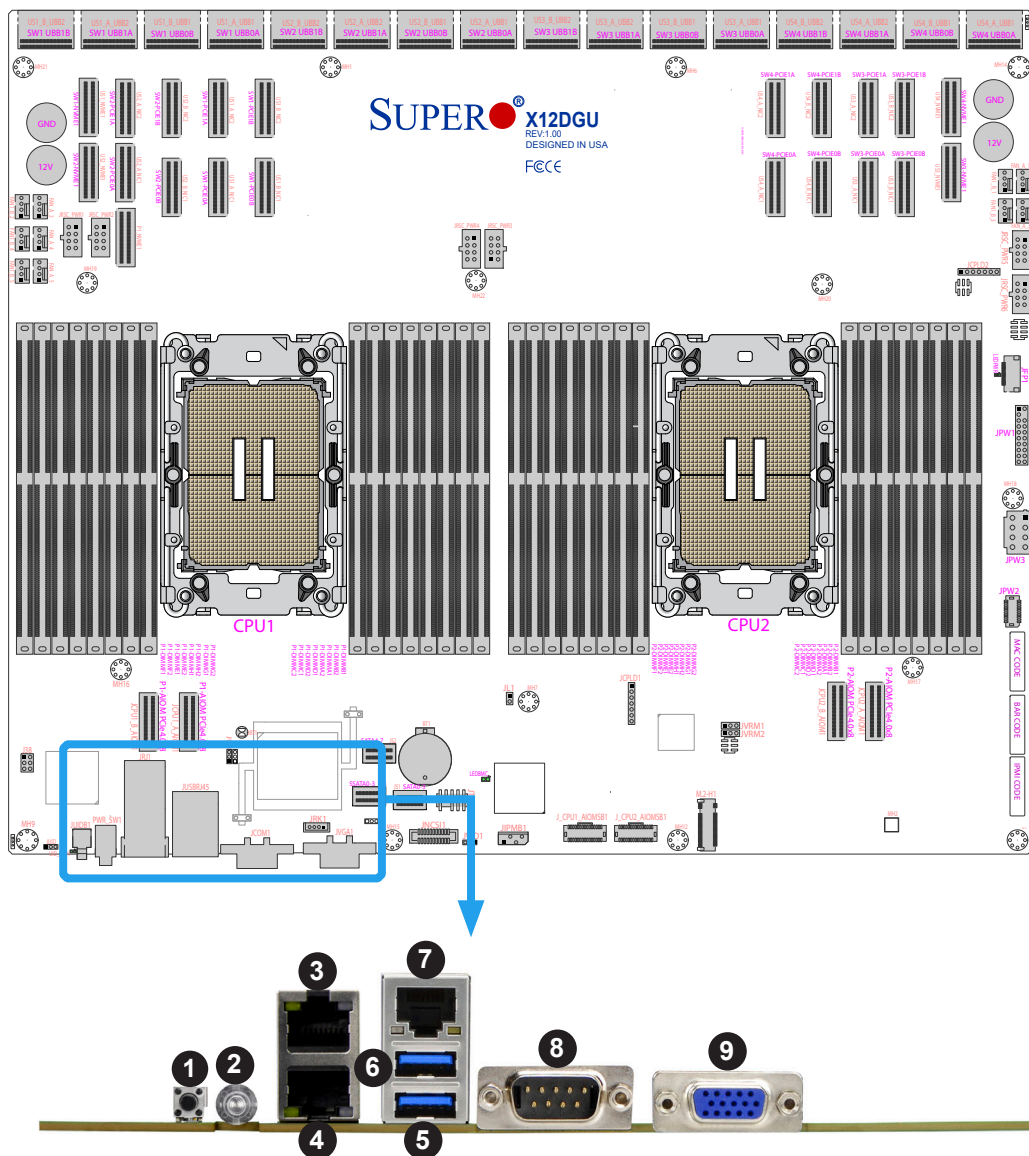


**Warning!** Please do not use excessive force when pressing the release tabs on the ends of the DIMM socket to avoid causing any damage to the memory module or the DIMM socket. Please handle memory modules with care. Carefully follow all the instructions given on page 1 of this chapter to avoid ESD-related damages done to your memory modules or components.

## 2.5 Rear I/O Connectors/Ports

See layout below for the locations and descriptions of the various I/O ports on the rear of the motherboard.

I/O Port Locations and Definitions

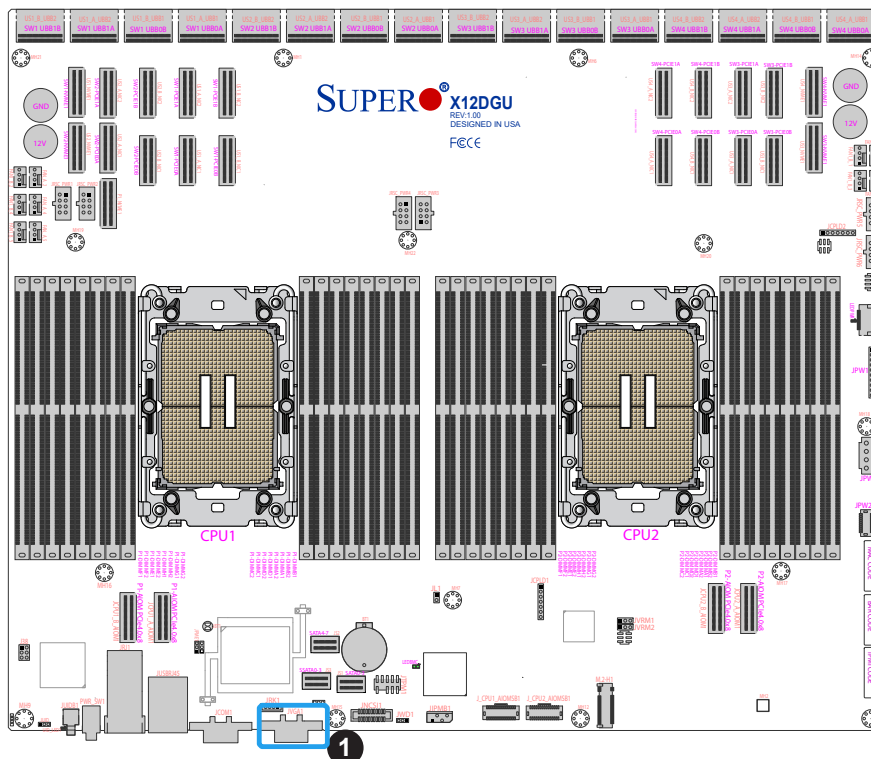


| Rear I/O Ports |                          |   |                       |
|----------------|--------------------------|---|-----------------------|
| #              | Description              | # | Description           |
| 1              | UID LED/BMC reset switch | 6 | USB1 (USB 3.0)        |
| 2              | Power switch             | 7 | Dedicated LAN for BMC |
| 3              | LAN1                     | 8 | Serial port           |
| 4              | LAN2                     | 9 | VGA port              |
| 5              | USB0 (USB 3.0)           |   |                       |

## VGA Port

The rear VGA port is located on the JVGA1 on the rear side of the motherboard. The VGA connection provides analog interface support between the computer and the video displays. Refer to the layout below for the location of VGA port.

### 1. (Rear) VGA

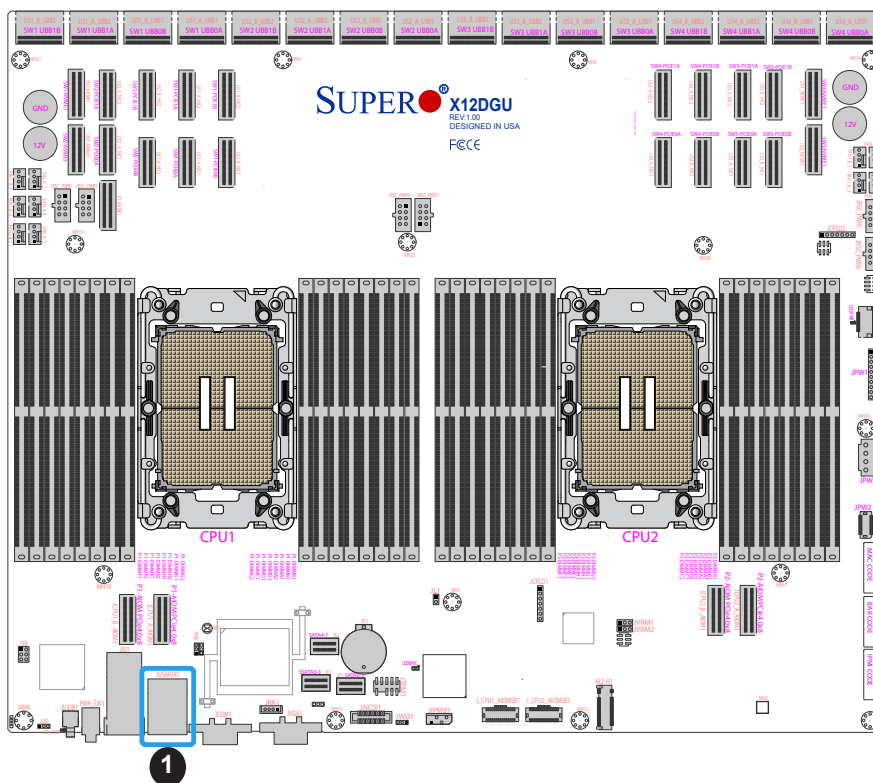


## Universal Serial Bus (USB) 3.0 Ports

Two USB 3.0 ports are located on the rear side of the motherboard (JUSBRJ45). Refer to the layout below for the location of JUSBRJ45.

| Rear I/O Panel USB 0/1 (3.0)<br>Pin Definitions |             |      |            |
|-------------------------------------------------|-------------|------|------------|
| Pin#                                            | Definition  | Pin# | Definition |
| A1                                              | VBUS        | B1   | Power      |
| A2                                              | D-          | B2   | USB_N      |
| A3                                              | D+          | B3   | USB_P      |
| A4                                              | GND         | B4   | GND        |
| A5                                              | Std_a_SSRX- | B5   | USB3_RN    |
| A6                                              | Std_a_SSRX+ | B6   | USB3_RP    |
| A7                                              | GND         | B7   | GND        |
| A8                                              | Std_a_SSTX- | B8   | USB3_TN    |
| A9                                              | Std_a_SSTX+ | B9   | USB3_TP    |

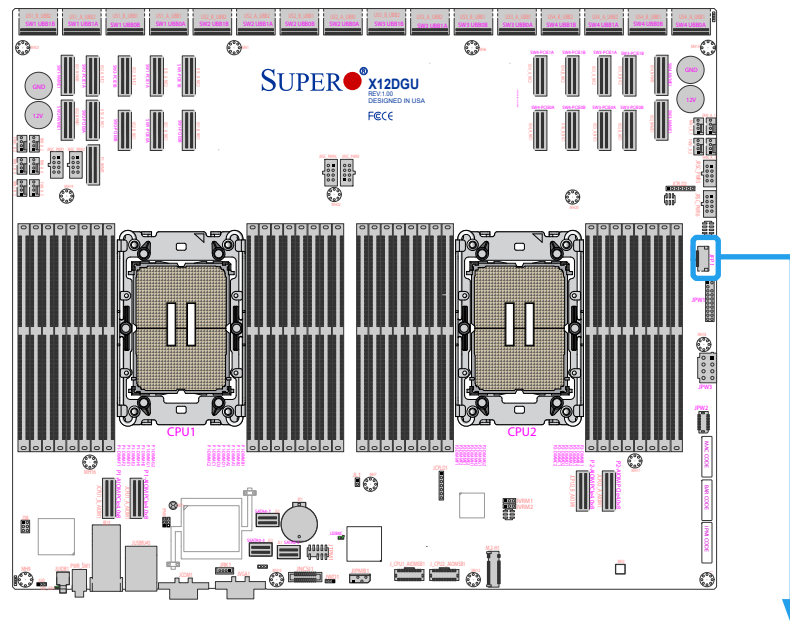
1. Rear I/O panel USB ports (JUSBRJ45)



## 2.6 Front Control Panels

### Front Control Panel Header with I<sup>2</sup>C

There is a front control panel headers located on this motherboard. Front control panel header 1, located at JFP1, contains header pins for various buttons and LED indications with I<sup>2</sup>C support for front access. This front control panel headers are designed specifically for use with Supermicro chassis. See the figure below for the pin-out descriptions for JFP1.



Front Control Panel Header 1 (JFP1)

#### JFP1

|    |   |                                        |
|----|---|----------------------------------------|
| 1  | ○ | Power Button                           |
| 2  | ○ | Reset/UID Button                       |
| 3  | ○ | UID LED_N                              |
| 4  | ○ | Fail LED_N (OH/FF/PF)                  |
| 5  | ○ | LAN-2 Activity LED                     |
| 6  | ○ | LAN-1 Activity LED (Aggregate all LAN) |
| 7  | ○ | HDD Activity LED                       |
| 8  | ○ | Standby LED_N                          |
| 9  | ○ | Power/RoT LED_N                        |
| 10 | ○ | P3V3_STBY                              |
| 11 | ○ | Ground                                 |
| 12 | ○ | I2C Data                               |
| 13 | ○ | I2C Clock                              |
| 14 | ○ | Ground                                 |
| 15 | ○ | Power Fail LED_P                       |
| 16 | ○ | P5V_USB                                |
| 17 | ○ | P5V_USB                                |
| 18 | ○ | P5V_USB                                |
| 19 | ○ | Power Fail LED_N                       |
| 20 | ○ | Ground                                 |

#### JFP1 Header Pins

## Front Control Panel LEDs

| Front Control Panel (JFP1)<br>LED Indicators       |                                                                                  |            |              |                                      |                                |                   |
|----------------------------------------------------|----------------------------------------------------------------------------------|------------|--------------|--------------------------------------|--------------------------------|-------------------|
| Event                                              | Power (LED1)                                                                     | HDD (LED2) | LAN (LED3/4) | UID (LED5)                           | Information (LED5)             | Power Fail (LED6) |
| Power On                                           | Solid On                                                                         |            |              |                                      |                                |                   |
| HDD Activity                                       |                                                                                  | Blinking   |              |                                      |                                |                   |
| NIC Activity                                       |                                                                                  |            | Blinking     |                                      |                                |                   |
| Overheat                                           |                                                                                  |            |              |                                      | Solid On                       |                   |
| Fan Fail                                           |                                                                                  |            |              |                                      | Blinking @1Hz                  |                   |
| Power Fail                                         |                                                                                  |            |              |                                      | Blinking @1/4Hz                | Solid On          |
| Local UID On                                       |                                                                                  |            |              | Solid On                             |                                |                   |
| Remote UID On                                      |                                                                                  |            |              | Blinking 1Hz                         |                                |                   |
| Checking                                           | BMC/BIOS<br>Blinking @4HZ                                                        |            |              |                                      |                                |                   |
| Recovering/Updating                                | BMC Blinking<br>@4HZ<br>BMC 2 Blinks<br>@4Hz,<br>1 Pause @2Hz<br>(on-on-off-off) |            |              | BIOS/BMC<br>Blinking<br>@10Hz        |                                |                   |
| Flash Not Detected or<br>Golden Image Check Failed | BMC/BIOS<br>Blinking @1HZ                                                        |            |              |                                      |                                |                   |
| CPLD Recovery Mode                                 |                                                                                  |            |              | Blinking<br>@10Hz<br>(MB UID<br>LED) | Blinking @10Hz<br>(FP Red LED) |                   |

## Power On and BMC/BIOS Status LED Button

The Power On and BMC/BIOS Status LED button is located on Pin 1 of the front control panel header located at JFP1. Momentarily contacting Pin 1 of JFP1 will power on/off the system or display BMC/BIOS status. Refer to the table below for more information.

| Power Button<br>BMC/BIOS Status LED Indicator                             |                                                     |
|---------------------------------------------------------------------------|-----------------------------------------------------|
| Status                                                                    | Event                                               |
| Green: solid on                                                           | System power on                                     |
| BMC/BIOS blinking green at 4Hz                                            | BMC/BIOS checking                                   |
| BIOS blinking gree at 4Hz                                                 | BIOS recovery/update in progress                    |
| BMC blinking red x2 (2 blinks red) at 4Hz, 1 pause at 2Hz (on-on-off-off) | BMC recovery/update in progress                     |
| BMC/BIOS blinking green at 1Hz                                            | Flash not detected or golden image checking failure |

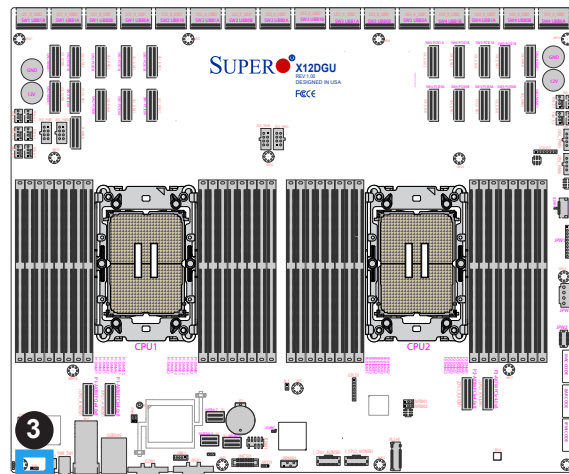
## BMC Reset Button/Front UID Switch

The BMC Reset button/Front UID switch connection is located on Pin 2 of JFP1, which is used in conjunction with the Reset button/UID switch select jumper located at JUID. To configure Pin 2 of JFP1 for front UID switch use in a chassis that supports front UID connection, close Pin 1 and Pin 2 of jumper JUID. To set Pin 2 of JFP1 for BMC Reset, close Pin 3 and Pin 4 of jumper JUID. Please refer to the jumper section for more information on JUID. Also see the layout below for the location of JUID.

| JFP1 |   |                                        |
|------|---|----------------------------------------|
| 1    | ○ | Power Button <b>1</b>                  |
| 2    | ○ | Reset/UID Button <b>2</b>              |
| 3    | ○ | UID LED_N                              |
| 4    | ○ | Fail LED_N (OH/FF/PF)                  |
| 5    | ○ | LAN-2 Activity LED                     |
| 6    | ○ | LAN-1 Activity LED (Aggregate all LAN) |
| 7    | ○ | HDD Activity LED                       |
| 8    | ○ | Standby LED_N                          |
| 9    | ○ | Power/RoT LED_N                        |
| 10   | ○ | P3V3_STBY                              |
| 11   | ○ | Ground                                 |
| 12   | ○ | I2C Data                               |
| 13   | ○ | I2C Clock                              |
| 14   | ○ | Ground                                 |
| 15   | ○ | Power Fail LED_P                       |
| 16   | ○ | P5V_USB                                |
| 17   | ○ | P5V_USB                                |
| 18   | ○ | P5V_USB                                |
| 19   | ○ | Power Fail LED_N                       |
| 20   | ○ | Ground                                 |

| Front UID Switch/Reset Button Select Jumper (JUID)<br>Jumper Settings |                                                  |
|-----------------------------------------------------------------------|--------------------------------------------------|
| State                                                                 | Description                                      |
| Close Pin 1 and Pin 2 of JUID                                         | Pin 2 of JFP1: used for front UID switch support |
| Close Pin 3 and Pin 4 of JUID                                         | Pin 2 of JFP1: used for BMC Reset support        |

1. Power On and BMC/BIOS Status LED Button
2. Reset Button/UID Switch Connection
3. JUID Jumper (UID Switch/Reset Button Select)



## UID LED

The unit identifier LED connection is located on Pin 3 of JFP1. See the figure below for more information on JFP1.

## Fail LED (Information LED for OH/FF/PF)

The Fail LED (Information LED for OH/Fan Fail/PWR Fail) connection is located on Pin 4 of JFP1. The LED provides warnings of overheating, power failure, or fan failure. Refer to the figure below for more information.

| Fail LED (Information LED) (OH/Fan Fail/PWR Fail)<br>LED States         |                                                                                                                                  |
|-------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------|
| Status                                                                  | Description                                                                                                                      |
| <b>Solid red (on)</b>                                                   | An overheat condition has occurred.                                                                                              |
| <b>Blinking red (1Hz)</b>                                               | Fan failure: check for an inoperative fan.                                                                                       |
| <b>Blinking red (0.25Hz)</b>                                            | Power failure: check for a non-operational power supply                                                                          |
| <b>Blinking red (10Hz) (FP red LED)</b>                                 | CPLD recovery mode error(s)                                                                                                      |
| <b>Solid blue</b>                                                       | UID has been activated locally. Use this function to locate a unit in a rack mount environment that might be in need of service. |
| <b>Blinking blue (1Hz)</b>                                              | Local UID has been activated locally on. Use this function to identify a unit that might be in need of service.                  |
| <b>BIOS/BMC blinking blue (10Hz)</b>                                    | BIOS/BMC: recovery and/or update in progress                                                                                     |
| <b>Red Info LED blinking (10Hz) and MB UID LED blue blinking (10Hz)</b> | CPLD: recovery and/or update in progress                                                                                         |

### JFP1

|    |                       |                                        |   |
|----|-----------------------|----------------------------------------|---|
| 1  | <input type="radio"/> | Power Button                           |   |
| 2  | <input type="radio"/> | Reset/UID Button                       |   |
| 3  | <input type="radio"/> | UID LED_N                              | 1 |
| 4  | <input type="radio"/> | Fail LED_N (OH/FF/PF)                  | 2 |
| 5  | <input type="radio"/> | LAN-2 Activity LED                     |   |
| 6  | <input type="radio"/> | LAN-1 Activity LED (Aggregate all LAN) |   |
| 7  | <input type="radio"/> | HDD Activity LED                       |   |
| 8  | <input type="radio"/> | Standby LED_N                          |   |
| 9  | <input type="radio"/> | Power/RoT LED_N                        |   |
| 10 | <input type="radio"/> | P3V3_STBY                              |   |
| 11 | <input type="radio"/> | Ground                                 |   |
| 12 | <input type="radio"/> | I2C Data                               |   |
| 13 | <input type="radio"/> | I2C Clock                              |   |
| 14 | <input type="radio"/> | Ground                                 |   |
| 15 | <input type="radio"/> | Power Fail LED_P                       |   |
| 16 | <input type="radio"/> | P5V_USB                                |   |
| 17 | <input type="radio"/> | P5V_USB                                |   |
| 18 | <input type="radio"/> | P5V_USB                                |   |
| 19 | <input type="radio"/> | Power Fail LED_N                       |   |
| 20 | <input type="radio"/> | Ground                                 |   |

1. UID LED Indicator
2. Fail LED (Information LED for OH/FF/PF)

## LAN1/LAN2 (NIC1/NIC2)

The NIC (Network Interface Controller) LED connection for LAN Port 1 is located on Pin 6 of JFP1, and LAN Port 2 is on Pin 5. Refer to the tables below.

| LAN1/LAN2 LED<br>LED States |               |
|-----------------------------|---------------|
| Color                       | State         |
| NIC 2: Blinking green       | LAN 2: Active |
| NIC 1: Blinking green       | LAN 1: Active |

## HDD Activity LED

The HDD activity LED connection is located on Pin 7 of JFP1. When this LED is blinking green, it indicates HDD activity. Refer to the table below.

| HDD LED<br>LED State |            |
|----------------------|------------|
| Color                | State      |
| Blinking Green       | HDD Active |

| JFP1 |                                                   |
|------|---------------------------------------------------|
| 1    | ○ Power Button                                    |
| 2    | ○ Reset/UID Button                                |
| 3    | ○ UID LED_N                                       |
| 4    | ○ Fail LED_N (OH/FF/PF)                           |
| 5    | ○ LAN-2 Activity LED <b>1</b>                     |
| 6    | ○ LAN-1 Activity LED (Aggregate all LAN) <b>2</b> |
| 7    | ○ HDD Activity LED <b>3</b>                       |
| 8    | ○ Standby LED_N                                   |
| 9    | ○ Power/RoT LED_N                                 |
| 10   | ○ P3V3_STBY                                       |
| 11   | ○ Ground                                          |
| 12   | ○ I2C Data                                        |
| 13   | ○ I2C Clock                                       |
| 14   | ○ Ground                                          |
| 15   | ○ Power Fail LED_P                                |
| 16   | ○ P5V_USB                                         |
| 17   | ○ P5V_USB                                         |
| 18   | ○ P5V_USB                                         |
| 19   | ○ Power Fail LED_N                                |
| 20   | ○ Ground                                          |

1. LAN 2 Activity LED
2. LAN 1 Activity LED
3. HDD Activity LED

### Standby Power LED

The LED indicator for standby power is located on Pin 8 of JFP1. If this LED is on, standby power is on.

### RoT (Root of Trust) Power LED

The Power LED for RoT (Root of Trust) connection is located on Pin 9 of JFP1. If this LED is on, power for the RoT chip is on.

| JFP1 |                       |                                        |
|------|-----------------------|----------------------------------------|
| 1    | <input type="radio"/> | Power Button                           |
| 2    | <input type="radio"/> | Reset/UID Button                       |
| 3    | <input type="radio"/> | UID LED_N                              |
| 4    | <input type="radio"/> | Fail LED_N (OH/FF/PF)                  |
| 5    | <input type="radio"/> | LAN-2 Activity LED                     |
| 6    | <input type="radio"/> | LAN-1 Activity LED (Aggregate all LAN) |
| 7    | <input type="radio"/> | HDD Activity LED                       |
| 8    | <input type="radio"/> | Standby LED_N                          |
| 9    | <input type="radio"/> | Power/RoT LED_N                        |
| 10   | <input type="radio"/> | P3V3_STBY                              |
| 11   | <input type="radio"/> | Ground                                 |
| 12   | <input type="radio"/> | I2C Data                               |
| 13   | <input type="radio"/> | I2C Clock                              |
| 14   | <input type="radio"/> | Ground                                 |
| 15   | <input type="radio"/> | Power Fail LED_P                       |
| 16   | <input type="radio"/> | P5V_USB                                |
| 17   | <input type="radio"/> | P5V_USB                                |
| 18   | <input type="radio"/> | P5V_USB                                |
| 19   | <input type="radio"/> | Power Fail LED_N                       |
| 20   | <input type="radio"/> | Ground                                 |

1. Standby Power LED
2. RoT Power LED

## Standby Power

A Standby Power (I<sup>2</sup>C) connection is located on Pin 10 ~ Pin 14 of JFP1 to provide power to the system when it is in standby mode. Refer to the table below for Pin definitions.

| 3.3V Standby PWR<br>Pin Definitions |                        |
|-------------------------------------|------------------------|
| Pin#                                | Definition             |
| 10                                  | P3V3 Standby           |
| 11                                  | Ground                 |
| 12                                  | I <sup>2</sup> C Data  |
| 13                                  | I <sup>2</sup> C Clock |
| 14                                  | Ground                 |

## Power Fail LED Indicators

Power Failure LED Indicators are located on Pin 15 and Pin 19 of JFP1. Refer to the table below for pin definitions.

| FP Power LED<br>Pin Definitions (JFP1) |                          |
|----------------------------------------|--------------------------|
| Pin#                                   | Definition               |
| 15                                     | PWR Failure LED-Positive |
| 19                                     | PWR Failure LED-Negative |

### JFP1

|    |                       |                                        |
|----|-----------------------|----------------------------------------|
| 1  | <input type="radio"/> | Power Button                           |
| 2  | <input type="radio"/> | Reset/UID Button                       |
| 3  | <input type="radio"/> | UID LED_N                              |
| 4  | <input type="radio"/> | Fail LED_N (OH/FF/PF)                  |
| 5  | <input type="radio"/> | LAN-2 Activity LED                     |
| 6  | <input type="radio"/> | LAN-1 Activity LED (Aggregate all LAN) |
| 7  | <input type="radio"/> | HDD Activity LED                       |
| 8  | <input type="radio"/> | Standby LED_N                          |
| 9  | <input type="radio"/> | Power/RoT LED_N                        |
| 10 | <input type="radio"/> | P3V3_STBY                              |
| 11 | <input type="radio"/> | Ground                                 |
| 12 | <input type="radio"/> | I2C Data                               |
| 13 | <input type="radio"/> | I2C Clock                              |
| 14 | <input type="radio"/> | Ground                                 |
| 15 | <input type="radio"/> | Power Fail LED_P                       |
| 16 | <input type="radio"/> | P5V_USB                                |
| 17 | <input type="radio"/> | P5V_USB                                |
| 18 | <input type="radio"/> | P5V_USB                                |
| 19 | <input type="radio"/> | Power Fail LED_N                       |
| 20 | <input type="radio"/> | Ground                                 |

1. (3.3V) Standby Power
2. PWR Fail LED (Positive)
3. PWR Fail LED (Negative)

## FP USB Power

Front Panel USB power connections are located on Pin 16 ~ Pin 18 of JFP1 to provide power to front USB devices. Refer to the table below for Pin definitions.

| FP USB PWR<br>Pin Definitions |             |
|-------------------------------|-------------|
| Pin#                          | Definition  |
| 16                            | +5V USB PWR |
| 17                            |             |
| 18                            |             |

### JFP1

|    |   |                                        |
|----|---|----------------------------------------|
| 1  | ○ | Power Button                           |
| 2  | ○ | Reset/UID Button                       |
| 3  | ○ | UID LED_N                              |
| 4  | ○ | Fail LED_N (OH/FF/PF)                  |
| 5  | ○ | LAN-2 Activity LED                     |
| 6  | ○ | LAN-1 Activity LED (Aggregate all LAN) |
| 7  | ○ | HDD Activity LED                       |
| 8  | ○ | Standby LED_N                          |
| 9  | ○ | Power/RoT LED_N                        |
| 10 | ○ | P3V3_STBY                              |
| 11 | ○ | Ground                                 |
| 12 | ○ | I2C Data                               |
| 13 | ○ | I2C Clock                              |
| 14 | ○ | Ground                                 |
| 15 | ○ | Power Fail LED_P                       |
| 16 | ○ | P5V_USB                                |
| 17 | ○ | P5V_USB                                |
| 18 | ○ | P5V_USB                                |
| 19 | ○ | Power Fail LED_N                       |
| 20 | ○ | Ground                                 |

**1**

### 1. FP USB Power

## 2.7 Connectors

### Power Connections

#### Power Supply Connectors

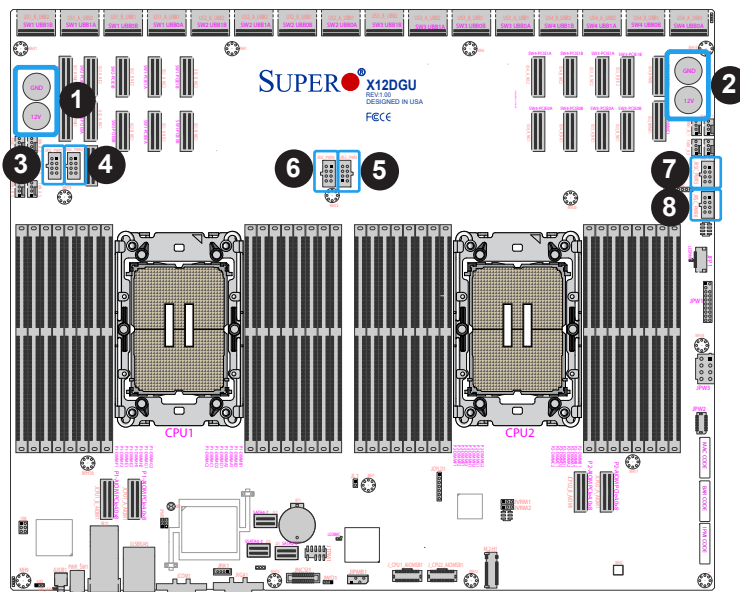
Two power connectors, located at **1** and **2**, connect your system to the redundant power supply. All these power connectors meet the ATX SSI EPS 12V specification and must be connected to your power supply to provide adequate power to your system. Additionally, six 8-pin power connectors (JRSC\_PWR1~ JRSC\_PWR6) are used for riser cards.



**Important:** To provide adequate power to your system, be sure to connect the main power connectors to the power supply. Failure to do so may void the manufacturer warranty on your motherboard.

| 12V 8-pin Power Pin Definitions |            |
|---------------------------------|------------|
| Pin#                            | Definition |
| 1 - 4                           | Ground     |
| 5 - 6                           | +12V       |
| 7                               | +3.3V      |
| 8                               | +3.3V_STBY |

#### Required Connection



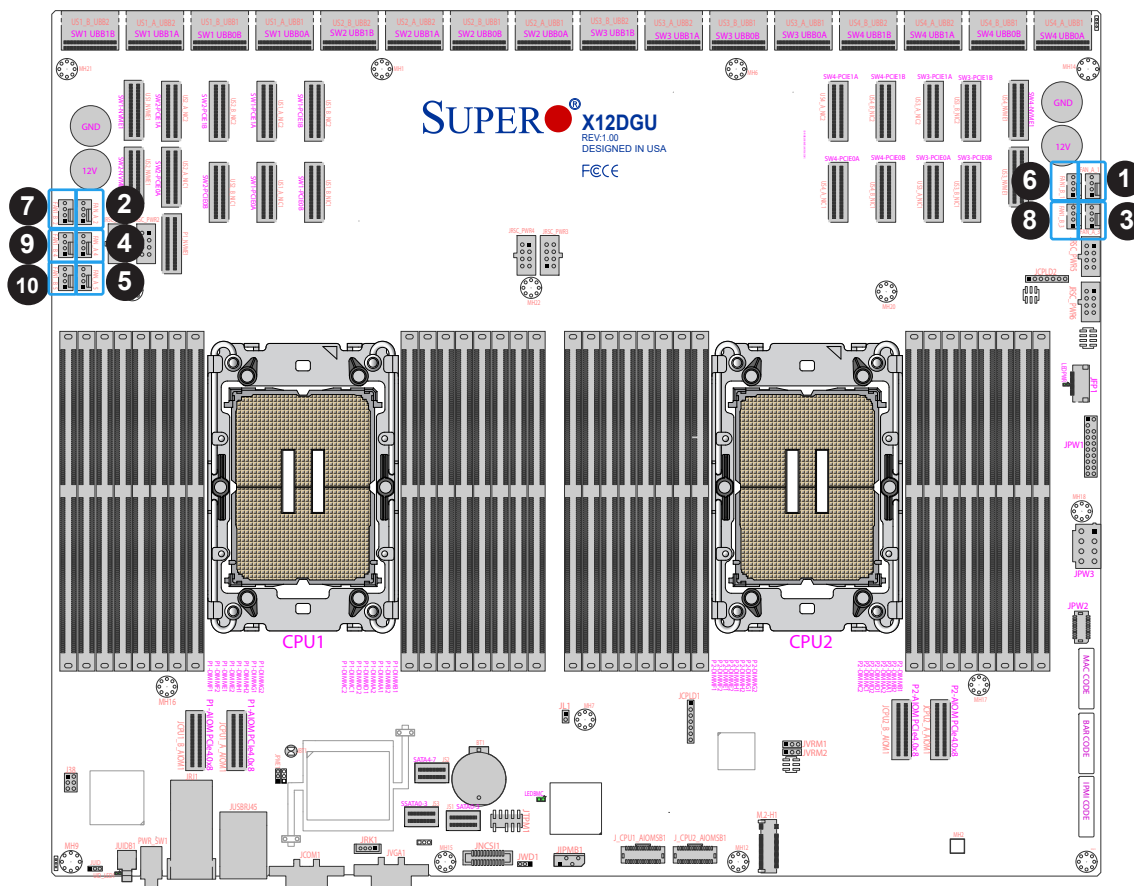
1. Main power connector
2. Main power connector
3. JRSC\_PWR1
4. JRSC\_PWR2
5. JRSC\_PWR3
6. JRSC\_PWR4
7. JRSC\_PWR5
8. JRSC\_PWR6

## Headers

### Fan Headers

There are ten 4-pin fan headers (FAN1A/1B/2A/2B/3A/3B/4A/4B/5A/5B). These fan headers are used for the cooling fans for your system. Fan speed control for these fans is supported by Thermal Management via the BMC 2.0 interface. Refer to the layout below for the locations of the fan headers.

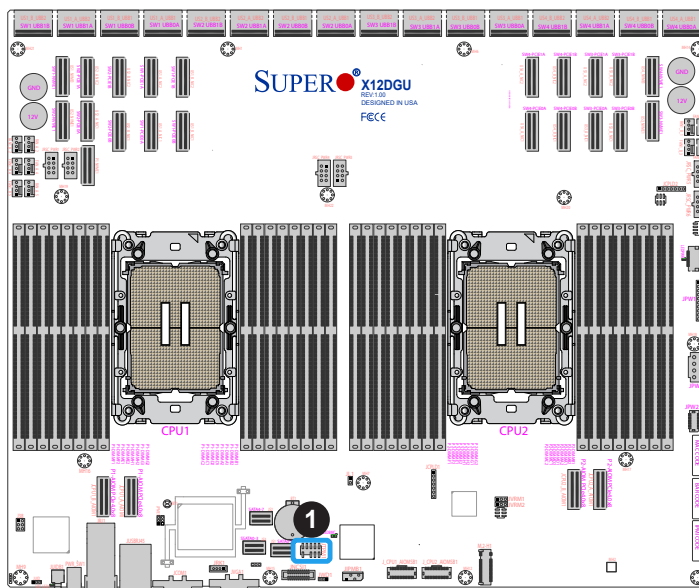
1. FAN\_A\_1
2. FAN\_A\_2
3. FAN\_A\_3
4. FAN\_A\_4
5. FAN\_A\_5
6. FAN\_B\_1
7. FAN\_B\_2
8. FAN\_B\_3
9. FAN\_B\_4
10. FAN\_B\_5



## TPM/Port 80 Header

The JTPM1 header is used to connect a Trusted Platform Module (TPM)/Port 80, which is available from Supermicro (optional). A TPM/Port 80 connector is a security device that supports encryption and authentication in hard drives. It allows the motherboard to deny access if the TPM associated with the hard drive is not installed in the system. See the layout below for the location of the TPM header. Please go to the following link for more information on the TPM: <http://www.supermicro.com/manuals/other/TPM.pdf>.

| Trusted Platform Module Header<br>Pin Definitions |             |      |            |
|---------------------------------------------------|-------------|------|------------|
| Pin#                                              | Definition  | Pin# | Definition |
| 1                                                 | +3.3V       | 2    | SPI_CS#    |
| 3                                                 | RESET#      | 4    | SPI_MISO   |
| 5                                                 | SPI_CLK     | 6    | GND        |
| 7                                                 | SPI_MOSI    | 8    | NC         |
| 9                                                 | +3.3V Stdby | 10   | SPI_IRQ#   |



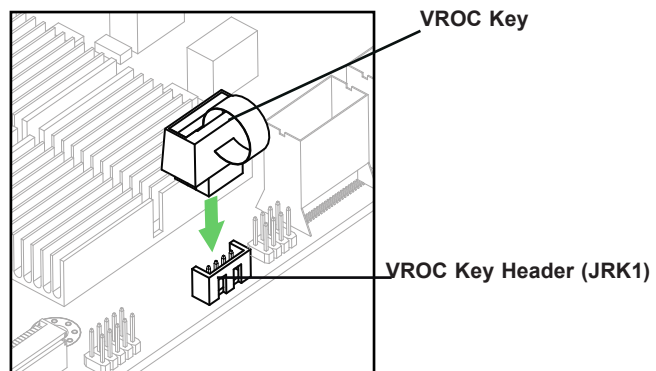
1. TPM Header

## VROC RAID Key Header

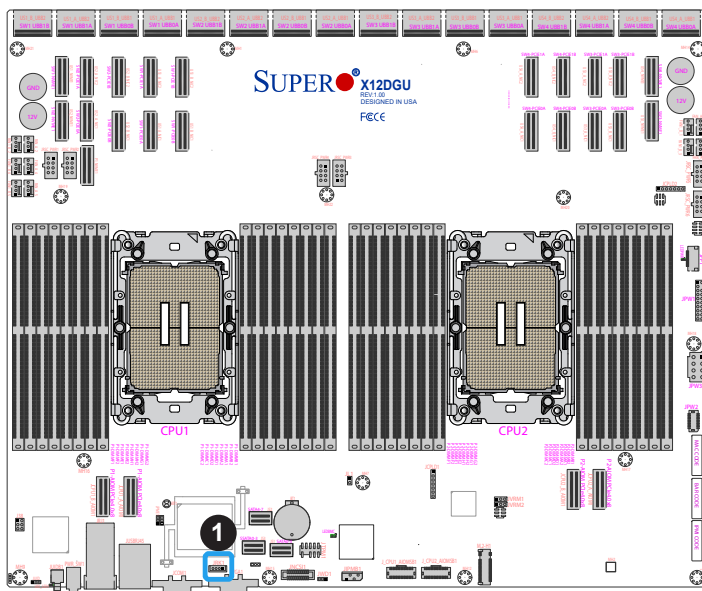
A VROC RAID Key header is located at JRK1 on the motherboard. Install a VROC RAID Key on JRK1 for NVMe RAID support as shown in the illustration below. Please refer to the layout below for the location of JRK1.

 **Note:** For detailed instructions on how to configure VROC RAID settings, please refer to the VROC RAID Configuration User's Guide posted on the web page under the link: <http://www.supermicro.com/support/manuals/>.

| Intel VROC Key Pin Definitions |              |
|--------------------------------|--------------|
| Pin#                           | Definition   |
| 1                              | Ground       |
| 2                              | 3.3V Standby |
| 3                              | Ground       |
| 4                              | PCH RAID Key |



 **Note:** The graphics contained in this user's manual are for illustration only. The components installed in your system may or may not look exactly the same as the graphics shown in the manual.



1. VROC RAID Key (JRK1)

### 6-pin BMC External I<sup>2</sup>C Header

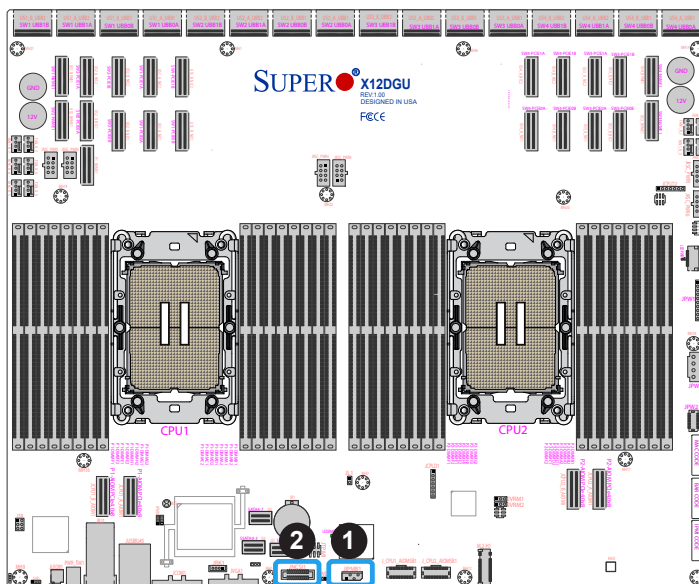
A System Management Bus header for the BMC is located at JIPMB1. Connect the appropriate cable here to use the IPMB I<sup>2</sup>C connection on your system. Refer to the layout for the location of JIPMB1.

### NC-SI Connector

The NC-SI (Network Controller Sideband Interface) connector is located at (JNCSI1). This connector is used to connect a Network Interface Card (NIC) to the motherboard to allow the onboard BMC (Baseboard Controller) to communicate with a network.

**Note:** For detailed instructions on how to configure Network Interface Card (NIC) settings, please refer to the Network Interface Card Configuration User's Guide posted on the web page under the link: <http://www.supermicro.com/support/manuals/>.

1. BMC External Header (JIPMB1)
2. NC-SI Connector (JNCSI1)

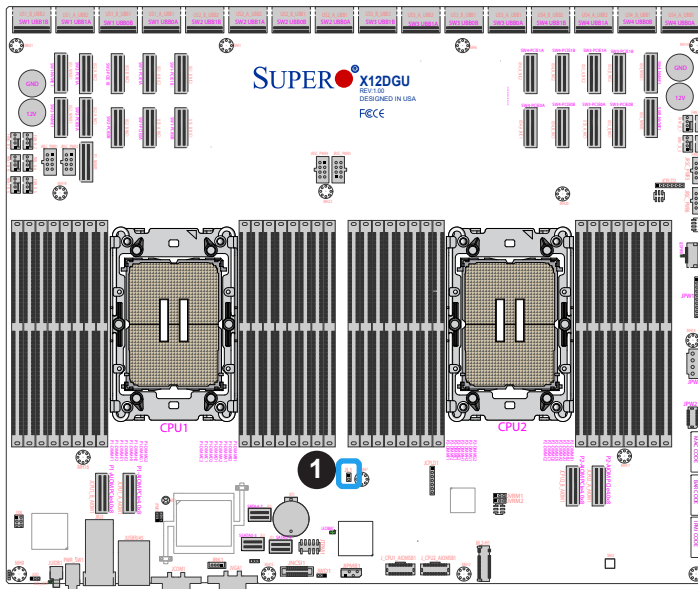


## Chassis Intrusion

A Chassis Intrusion header is located at JL1 on the motherboard. Attach the appropriate cable from the chassis to inform you when the chassis is opened. Refer to the table below for pin definitions.

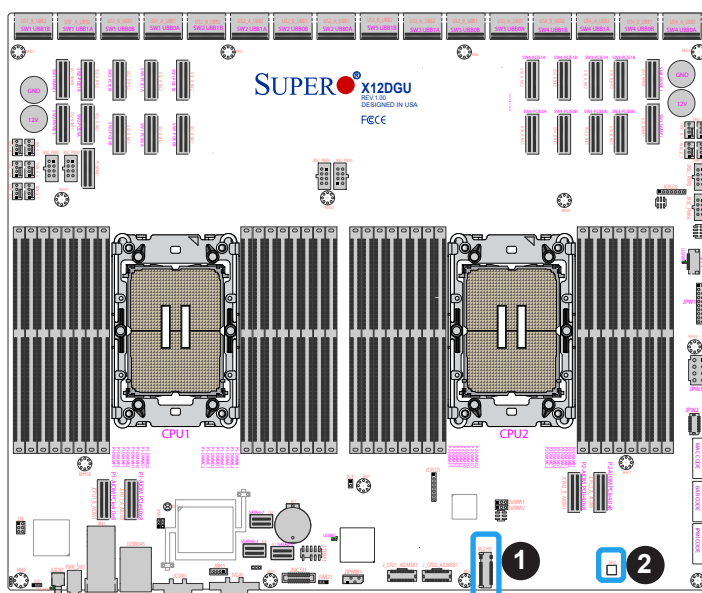
| Chassis Intrusion<br>Pin Definitions |                 |
|--------------------------------------|-----------------|
| Pin#                                 | Definition      |
| 1                                    | Intrusion Input |
| 2                                    | Ground          |

### 1. Chassis Intrusion (JL1)



### PCIe 4.0 M.2 Slot

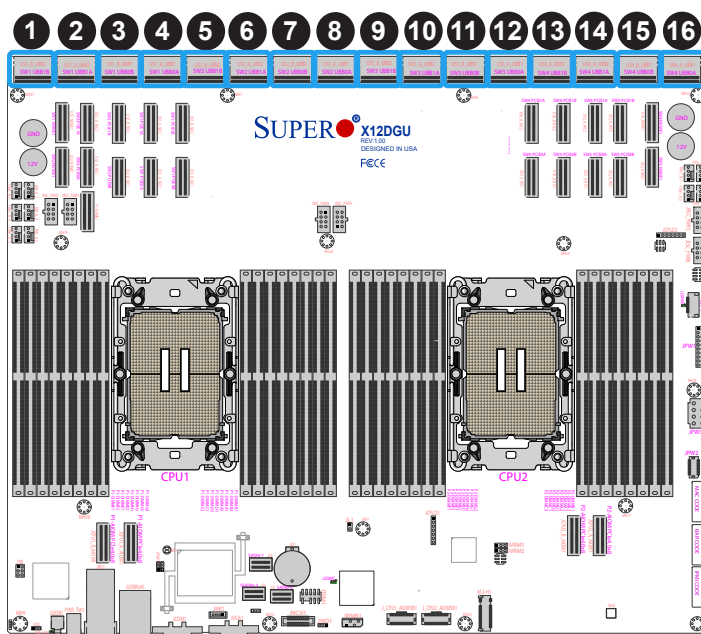
An M.2 slot is located at M.2-H1 on the motherboard. This M.2/SATA3 Hybrid slot supports PCIe 4.0 x4 M.2 NVMe/SATA3 SSDs in the 2280 form factor. To accommodate the 2280 form factor, an M.2 mounting hole (MH2) is provided on the motherboard. Refer to the layout below for the location of the M.2/SATA3 Hybrid slot and the mounting hole.



1. M.2-H1
2. Mounting hole (MH2)

## GPU Connectors

There are 16 SlimSAS GPU slots on the X12DGU that provide eight PCIe 4.0 x16 connections for the GPU devices on the motherboard. Refer to the layout for the location of the GPU slots.



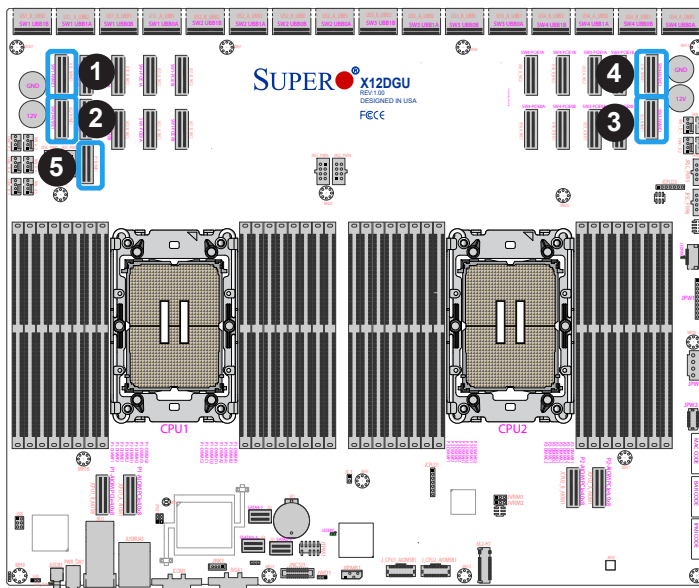
1. US1\_B\_UBB2
2. US1\_A\_UBB2
3. US1\_B\_UBB1
4. US1\_A\_UBB1
5. US2\_B\_UBB2
6. US2\_A\_UBB2
7. US2\_B\_UBB1
8. US2\_A\_UBB1
9. US1\_A\_UBB1
10. US3\_A\_UBB2
11. US3\_B\_UBB1
12. US3\_A\_UBB1
13. US4\_B\_UBB2
14. US4\_B\_UBB2
15. US4\_A\_UBB2
16. US4\_A\_UBB1

## NVMe Headers

Five SlimSAS NVMe headers, located at on US1\_NVME1/US2\_NVME1/US3\_NVME1/US4\_NVME1/P1\_NVME1, provide four PCIe 4.0 x16 connections on the motherboard. US1\_NVME1/US2\_NVME1/P1\_NVME1 are connected to CPU1, and US3\_NVME1/US4\_NVME1 are connected to CPU2. Use these SlimSAS connectors to support high-speed PCIe storage devices.

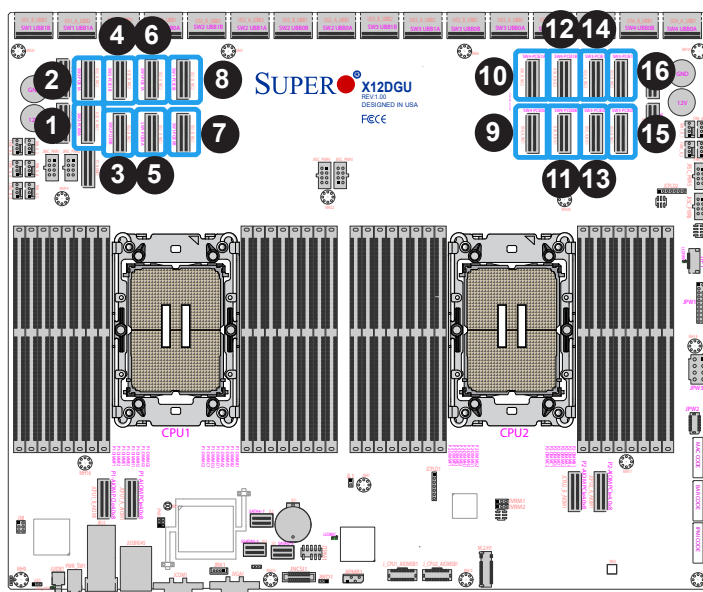
**Note:** When installing an NVMe device on a motherboard, please be sure to connect the first NVMe port (US1\_NVME1/US3\_NVME1) first for your system to work properly.

1. US1\_NVME1
2. US2\_NVME1
3. US3\_NVME1
4. US4\_NVME1
5. P1\_NVME1



## Network Interface Card (NIC) Connectors

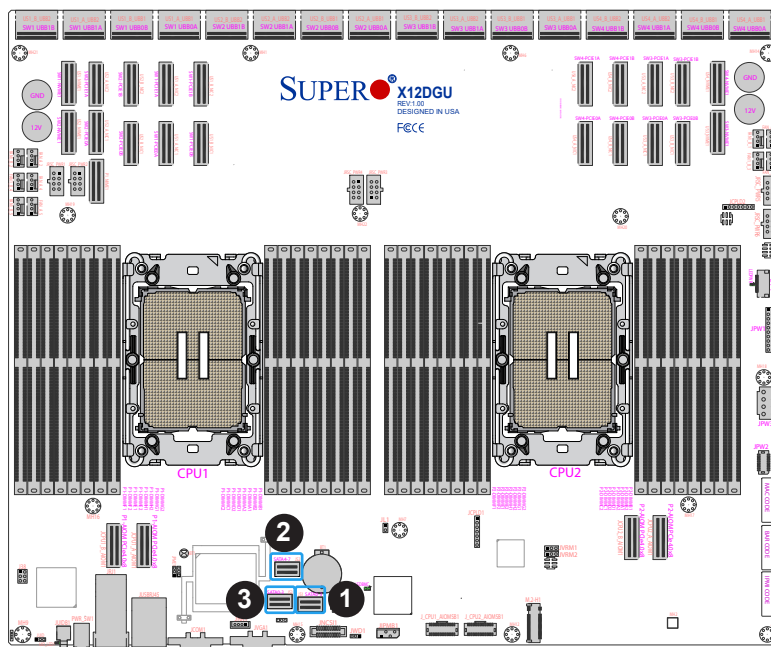
There are 16 SlimSAS PCIe 4.0 x16 NIC slots on X12DGU, which provide four PCIe 4.0 x16 connections for the network interface cards on the motherboard. Refer to the layout for the location of the NIC slots.



1. US2\_A\_NIC1
2. US2\_A\_NIC2
3. US2\_B\_NIC1
4. US2\_B\_NIC2
5. US1\_A\_NIC1
6. US1\_A\_NIC2
7. US1\_B\_NIC1
8. US1\_B\_NIC2
9. US4\_A\_NIC1
10. US4\_A\_NIC2
11. US4\_B\_NIC1
12. US4\_A\_NIC2
13. US3\_A\_NIC1
14. US3\_A\_NIC2
15. US3\_B\_NIC1
16. US3\_B\_NIC2

## SATA 3.0 Ports

Three SATA 3.0 headers, located at JS1, JS2, and JS3, support twelve SATA 3.0 connections (SATA0~7/SSATA0~3) on the motherboard. These SATA 3.0 ports are connected to the Intel C621A chipset. Connecting a proper SATA cable to JS1/JS2/JS3 to use SATA 3.0 connections.



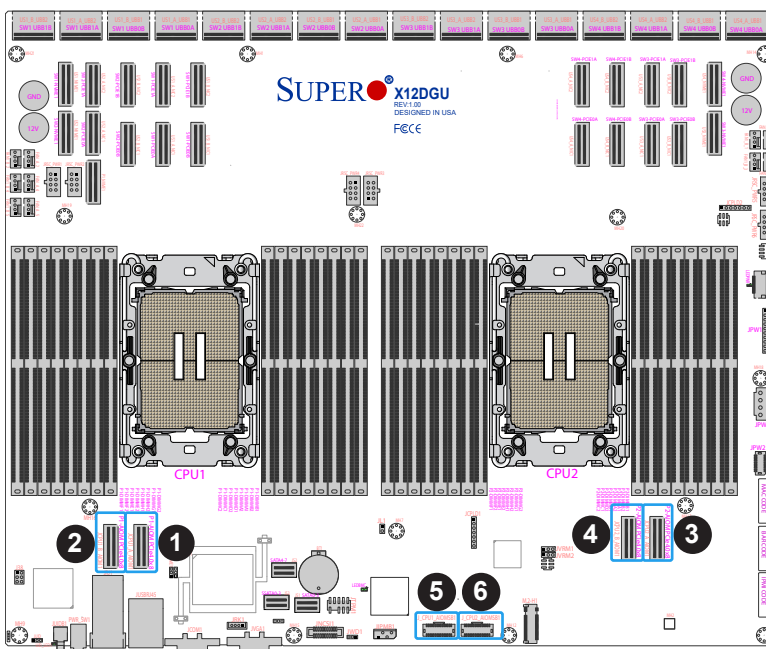
1. JS1
2. JS2
3. JS3

### Advanced I/O Module (AIOM) Connectors

Four Supermicro proprietary Advanced I/O Module (AIOM) connectors that provide two PCIe 4.0 x16 connections to add-on module are located at JCPU1\_A\_AIOM1/JCPU1\_B\_AIOM1/ JCPU2\_A\_AIOM1/JCPU2\_B\_AIOM1. JCPU1\_A\_AIOM1 and JCPU1\_B\_AIOM1 are connected to CPU1. JCPU2\_A\_AIOM1 and JCPU2\_B\_AIOM1 are connected to CPU2. See the layout below for the locations of those connectors.

### AIOM Sideband Connectors

Two Supermicro proprietary Advanced I/O Module (AIOM) sideband connectors are located at J\_CPU1\_AIOMSB1 and J\_CPU2\_AIOMSB1 on the motherboard. J\_CPU1\_AIOMSB1 is connected to CPU1, and J\_CPU2\_AIOMSB1 is connected to CPU2. See the layout below for the locations of those connectors.

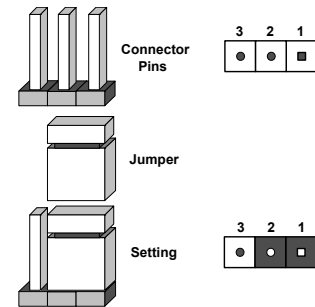


1. JCPU1\_A\_AIOM1
2. JCPU1\_B\_AIOM1
3. JCPU2\_A\_AIOM1
4. JCPU2\_B\_AIOM1
5. J\_CPU1\_AIOMSB1
6. J\_CPU2\_AIOMSB1

## 2.8 Jumper Settings

### How Jumpers Work

To modify the operation of the motherboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. See the diagram below for an example of jumping Pin 1 and Pin 2. Refer to the motherboard layout page for jumper locations.



**Note:** On two-pin jumpers, "Closed" means the jumper is on and "Open" means the jumper is off the pins.

### CMOS Clear

JBT1 is used to clear CMOS, which will also clear any passwords. Instead of pins, this jumper consists of contact pads to prevent accidentally clearing the contents of CMOS.

#### To Clear CMOS



1. First power down the system and unplug the power cord(s).
2. Remove the cover of the chassis to access the motherboard and remove the battery from the motherboard.
3. Short the CMOS pads, JBT1, with a metal object such as a small screwdriver for at least four seconds.
4. Remove the screwdriver (or shorting device).
5. Replace the cover, reconnect the power cord(s), and power on the system.

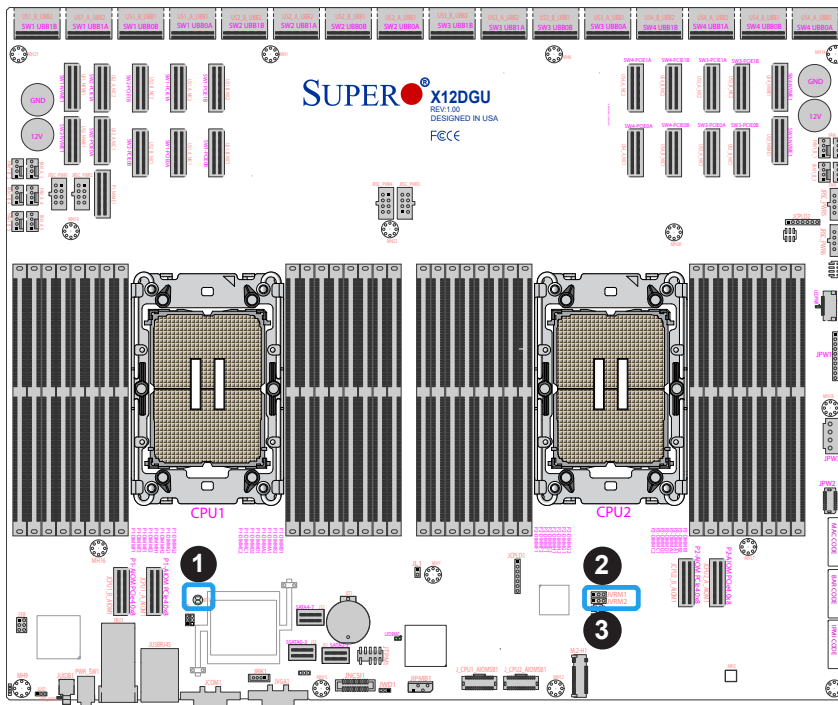


**Note :** Clearing CMOS will also clear all passwords.

**BMC and PCH I<sup>2</sup>C/SDA to VRM and BMC and PCH I<sup>2</sup>C/SCI to VRM Select Jumper**

Use JVRM1 and JVRM2 to select between BMC and PCH I<sup>2</sup>C/SDA for VRM support or BMC and PCH I<sup>2</sup>C/SCI for VRM support. Connect a cable to JVRM1 and JVRM2 to enable BMC and PCH I<sup>2</sup>C/SDA for VRM support. See the table below for jumper settings.

| BMC and PCH I <sup>2</sup> C/SDA to VRM and BMC and PCH I <sup>2</sup> C/SCI to VRM Select Jumper Jumper Settings |                                                            |
|-------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------|
| Jumper Setting                                                                                                    | Definition                                                 |
| Closed                                                                                                            | BMC and PCH I <sup>2</sup> C/SDA for VRM support (Default) |
| Open                                                                                                              | BMC and PCH I <sup>2</sup> C/SCI for VRM support           |

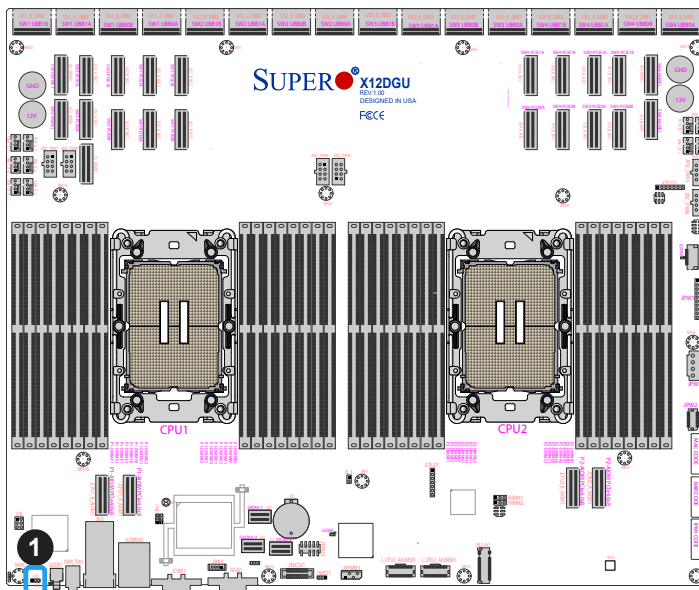


1. Clear CMOS (JBT1)
2. JVRM1
3. JVRM2

## UID LED and BMC\_Reset Button Select Jumper

Jumper JUID is used in conjunction with Pin 2 of Front Control Panel header 1 (JFP1) to function as a BMC\_Reset button or a UID LED button. To configure Pin 2 of JFP1 for front UID button use in a chassis that supports front UID connection, close Pins 1 and 2 of jumper JUID. To set Pin 2 of JFP1 for BMC reset support, close Pins 3 and 4 of jumper JUID. Please refer to the table below for more information on JUID. Also see the layout below for the location of JUID.

| Front UID Switch/Reset Button Select Jumper (JUID)<br>Jumper Settings |                                                                     |
|-----------------------------------------------------------------------|---------------------------------------------------------------------|
| State                                                                 | Description                                                         |
| Close Pin 1 and Pin 2 of JUID                                         | Pin 2 of JFP1: used for front UID switch support ( <b>Default</b> ) |
| Close Pin 3 and Pin 4 of JUID                                         | Pin 2 of JFP1: used for BMC Reset support                           |



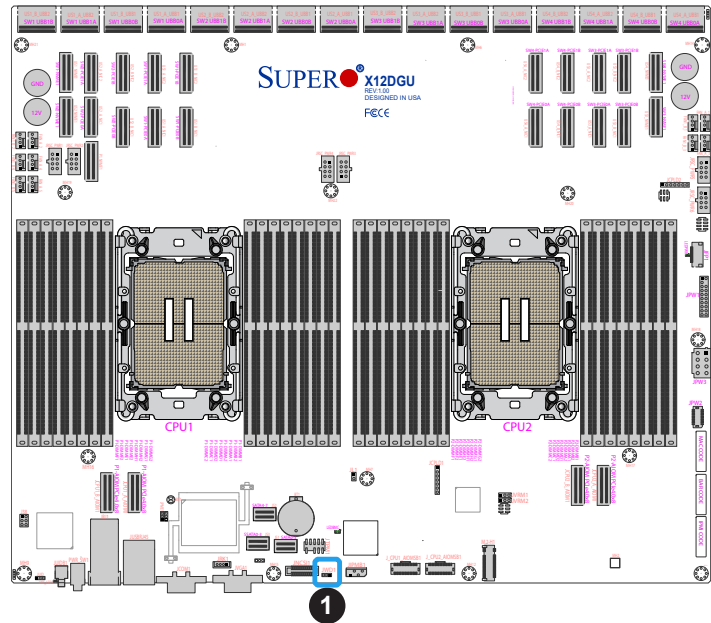
1. JUID

Watchdog

Watchdog (JWD1) is a system monitor that can reboot the system when a software application hangs. Close pins 1-2 to reset the system if an application hangs. Close pins 2-3 to generate a non-maskable interrupt (NMI) signal for the application that hangs. Refer to the table below for jumper settings. The Watchdog timer must also be enabled in the BIOS.

| Watchdog<br>Jumper Settings |            |
|-----------------------------|------------|
| Jumper Setting              | Definition |
| Pins 1-2                    | Reset      |
| Pins 2-3                    | NMI        |
| Open                        | Disabled   |

1. JWD1

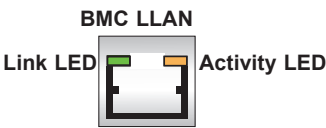


## 2.9 LED Indicators

### BMC LAN LEDs

A dedicated BMC LAN connection is provided on the rear side of the motherboard. The LED on the right indicates activity, and the LED on the left indicates the speed of the connection. Refer to the table below for more information.

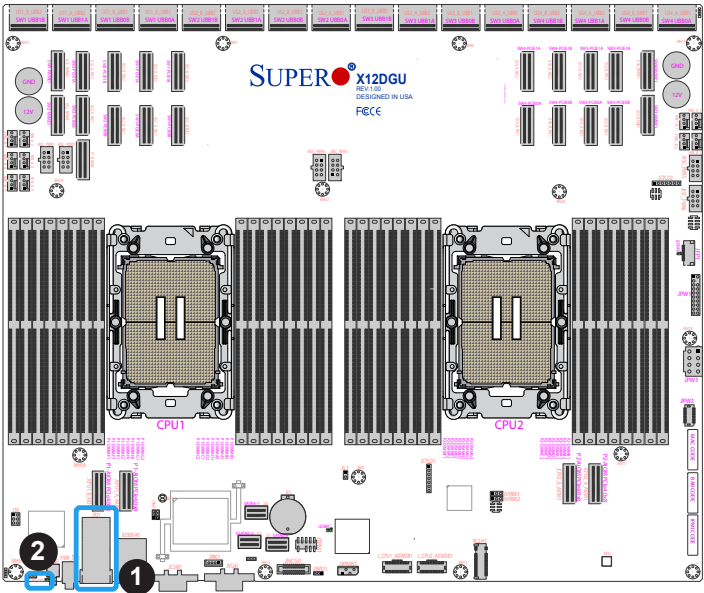
| BMC LAN LEDs     |                 |            |
|------------------|-----------------|------------|
|                  | Color/State     | Definition |
| Link (left)      | Green: Solid    | 100 Mbps   |
|                  | Amber: Solid    | 1Gbps      |
| Activity (Right) | Amber: Blinking | Active     |



### Unit ID LED

The front UID LED indicator is located at (UID\_LED1). This UID indicator provides easy identification of a system that may need service.

| UID LED<br>LED Indicator |                   |
|--------------------------|-------------------|
| LED Color                | Definition        |
| Blue: On                 | System Identified |



- 1. BMC LAN LEDs
- 2. UID\_LED1

## Onboard Power LED

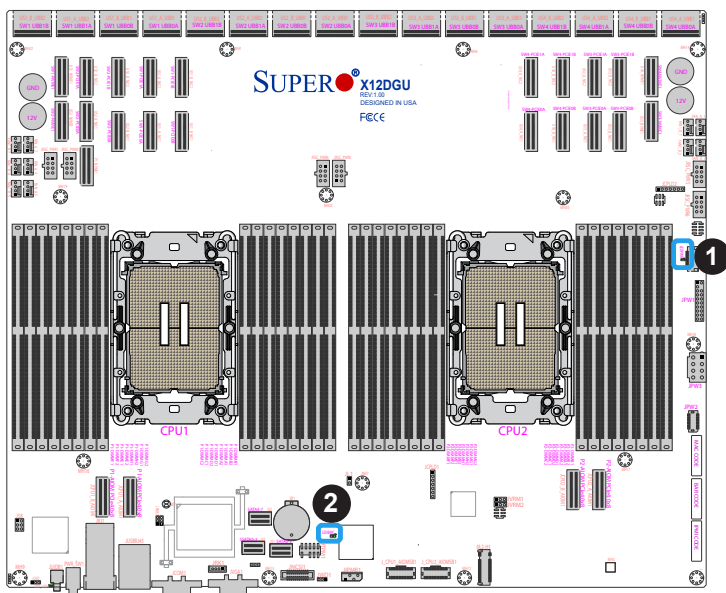
The Onboard Power LED is located at LEDPWR on the motherboard. When this LED is on, the system power is on. Be sure to turn off the system power and unplug the power cords before removing or installing components. Refer to the table below for more information.

| Onboard Power LED Indicator |                                              |
|-----------------------------|----------------------------------------------|
| LED Color                   | Definition                                   |
| Off                         | System Power Off (power cable not connected) |
| Green                       | System Power On                              |

## BMC Heartbeat LED

A BMC Heartbeat LED is located at LEDBMC on the motherboard. When LEDBMC is blinking green, the BMC is functioning normally. Refer to the layout below for the location of LEDBMC.

| BMC Heartbeat LED Indicator |            |
|-----------------------------|------------|
| LED Color                   | Definition |
| Green:<br>Blinking          | BMC Normal |



1. Onboard Power LED (LEDPWR)
2. BMC Heartbeat LED (LEDBMC)

# Chapter 3

## Troubleshooting

### 3.1 Troubleshooting Procedures

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the 'Technical Support Procedures' and/or 'Returning Merchandise for Service' section(s) in this chapter. Always disconnect the AC power cord before adding, changing or installing any non hot-swap hardware components.

#### Before Power On

1. Make sure that there are no short circuits between the motherboard and chassis.
2. Disconnect all ribbon/wire cables from the motherboard, including those for the keyboard and mouse.
3. Remove all add-on cards.
4. Install the CPU (making sure it is fully seated) and connect the front panel connectors to the motherboard.

#### No Power

1. Make sure that there are no short circuits between the motherboard and the chassis.
2. Make sure that the ATX power connectors are properly connected.
3. Check that the 115V/230V switch, if available, on the power supply is properly set.
4. Turn the power switch on and off to test the system, if applicable.
5. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

## No Video

1. If the power is on, but you do not have video, remove all add-on cards and cables.
2. Remove all memory modules and turn on the system (if the alarm is on, check the specs of memory modules, reset the memory, or try a different one).

## System Boot Failure

If the system does not display POST (Power-On-Self-Test) or does not respond after the power is turned on, check the following:

1. Remove all components from the motherboard, especially the DIMM modules. Power on the system and check if the power-on LED (LEDPWR) and the BMC Heartbeat LED (LEDBMC) are on, and system fans are spinning.
2. Turn on the system with only one DIMM module installed. If the system boots, check for bad DIMM modules or slots by following the Memory Errors Troubleshooting procedure in this chapter.

## Memory Errors

1. Make sure that the memory modules are compatible with the system and are properly installed. See Chapter 2 for installation instructions. (For memory compatibility, refer to the "Tested Memory List" link on the motherboard's product page to see a list of supported memory.)
2. Check if different speeds of DIMMs have been installed. It is strongly recommended that you use the same RAM type and speed for all DIMM modules in the system.
3. Make sure that you are using the correct type of ECC DDR4 modules recommended by the manufacturer.
4. Check for bad DIMM modules or slots by swapping a single module among all memory slots and check the results.

## Losing the System's Setup Configuration

1. Make sure that you are using a high-quality power supply. A poor-quality power supply may cause the system to lose the CMOS setup information. Refer to Chapter 1 for details on recommended power supplies.
2. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

## When the System Becomes Unstable

### ***A. If the system becomes unstable during or after OS installation, check the following:***

1. CPU/BIOS support: Make sure that your CPU is supported and that you have the latest BIOS installed in your system.
2. Memory support: Make sure that the memory modules are supported by testing the modules using memtest86 or a similar utility.



**Note:** Click on the "Tested Memory List" link on the motherboard's product page to see a list of supported memory.

3. HDD support: Make sure that all hard disk drives (HDDs) work properly. Replace the bad HDDs with good ones.
4. System cooling: Check the system cooling to make sure that all heatsink fans and CPU/system fans, etc., work properly. Check the hardware monitoring settings in the BMC to make sure that the CPU and system temperatures are within the normal range. Also check the front panel Overheat LED and make sure that it is not on.
5. Adequate power supply: Make sure that the power supply provides adequate power to the system. Make sure that all power connectors are connected. Please refer to our website for more information on the minimum power requirements.
6. Proper software support: Make sure that the correct drivers are used.

### ***B. If the system becomes unstable before or during OS installation, check the following:***

1. Source of installation: Make sure that the devices used for installation are working properly, including boot devices such as DVD drives or USB flash drives.
2. Cable connection: Check to make sure that all cables are connected and working properly.
3. Using the minimum configuration for troubleshooting: Remove all unnecessary components (starting with add-on cards first), and use the minimum configuration (but with the CPU and a memory module installed) to identify the trouble areas. Refer to the steps listed in Section A above for proper troubleshooting procedures.
4. Identifying bad components by isolating them: If necessary, remove a component in question from the chassis, and test it in isolation to make sure that it works properly. Replace a bad component with a good one.

5. Check and change one component at a time instead of changing several items at the same time. This will help isolate and identify the problem.
6. To find out if a component is good, swap this component with a new one to see if the system will work properly. If so, then the old component is bad. You can also install the component in question in another system. If the new system works, the component is good and the old system has problems.

## 3.2 Technical Support Procedures

Before contacting Technical Support, please take the following steps. Also, please note that as a motherboard manufacturer, Supermicro also sells motherboards through its channels, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problems with the specific system configuration that was sold to you.

1. Please go through the Troubleshooting Procedures and Frequently Asked Questions (FAQ) sections in this chapter or see the FAQs on our website (<http://www.supermicro.com/FAQ/index.php>) before contacting Technical Support.
2. BIOS upgrades can be downloaded from our website ([http://www.supermicro.com/ResourceApps/BIOS\\_BMC\\_Intel.html](http://www.supermicro.com/ResourceApps/BIOS_BMC_Intel.html)).
3. If you still cannot resolve the problem, include the following information when contacting Supermicro for technical support:
  - Motherboard model and PCB revision number
  - BIOS release date/version (This can be seen on the initial display when your system first boots up.)
  - System configuration
4. An example of a Technical Support form is on our website at <http://www.supermicro.com/RmaForm/>.
5. Distributors: For immediate assistance, please have your account number ready when placing a call to our Technical Support department. We can be reached by email at [support@supermicro.com](mailto:support@supermicro.com).

## 3.3 Frequently Asked Questions

### **Question: What type of memory does my motherboard support?**

**Answer:** This motherboard supports up to 8TB 3DS LRDIMM/LRDIMM/3DS RDIMM/RDIMM DDR4 (288-pin) ECC memory with speeds up to 3200/2933/2666 MHz in 32 DIMM slots, and up to 8TB Intel Optane PMem 200 Series with speeds up to 3200 MHz. (See the notes below). To enhance memory performance, do not mix memory modules of different speeds and sizes. Please follow all memory installation instructions given on Section 2-4 in Chapter 2.



**Note:** The Intel® Optane™ Persistent Memory (PMem) 200 Series are supported by the 3rd Gen Intel Xeon Scalable (83xx/63xx/53xx/4314 Series) Processors.

### **Question: How do I update my BIOS?**

**Answer:** It is recommended that you do not upgrade your BIOS if you are not experiencing any problems with your system. Updated BIOS files are located on our website at [http://www.supermicro.com/ResourceApps/BIOS\\_BMC\\_Intel.html](http://www.supermicro.com/ResourceApps/BIOS_BMC_Intel.html). Please check our BIOS warning message and the information on how to update your BIOS on our website. Select your motherboard model and download the BIOS file to your computer. Also, check the current BIOS revision to make sure that it is newer than your BIOS before downloading.



**Note 1:** The SPI BIOS chip used on this motherboard cannot be removed. Send your motherboard back to our RMA Department at Supermicro for repair.

**Note 2:** For BIOS Update and Recovery instructions, please refer to the Firmware Update and Recovery Instructions for Supermicro's X12 Motherboards User's Guide posted at <http://www.supermicro.com/support/manuals/>.

## 3.4 Battery Removal and Installation

### Battery Removal

To remove the onboard battery, follow the steps below:

1. Power off your system and unplug your power cable.
2. Locate the onboard battery as shown below.
3. Using a tool such as a pen or a small screwdriver, push the battery lock outwards to unlock it. Once unlocked, the battery will pop out from the holder.
4. Remove the battery.

### Proper Battery Disposal

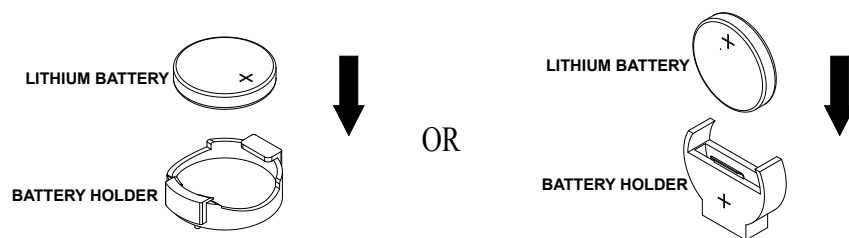
**Warning:** Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

### Battery Installation

To install an onboard battery, follow the steps below:

1. Power off your system and unplug your power cable.
2. Locate the onboard battery as shown below
3. Identify the battery's polarity. The positive (+) side should be facing up.
4. Insert the battery into the battery holder and push it down until you hear a click to ensure that the battery is securely locked.

**Warning:** When replacing a battery, be sure to only replace it with the same type.



## 3.5 Returning Merchandise for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning the motherboard to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton, and the shipping package is mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete. For faster service, you can also request a RMA authorization online (<http://www.supermicro.com/RmaForm/>).

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alternation, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.

## Chapter 4

# UEFI BIOS

### 4.1 Introduction

This chapter describes the AMIBIOS™ setup utility for the X12DGU motherboard. The BIOS is stored on a chip and can be easily upgraded using the BMC WebUI or the SUM utility.



**Note:** Due to periodic changes to the BIOS, some settings may have been added or deleted and might not yet be recorded in this manual. Please refer to the Manual Download area of our website for any changes to the BIOS that may not be reflected in this manual.

#### Starting the Setup Utility

To enter the BIOS setup utility, press the <Delete> key while the system is booting up. In most cases, the <Delete> key is used to invoke the BIOS setup screen; however, in other cases, other hot keys, such as <F1>, <F2>, may be used for this purpose. Each main BIOS menu option is described in this manual.

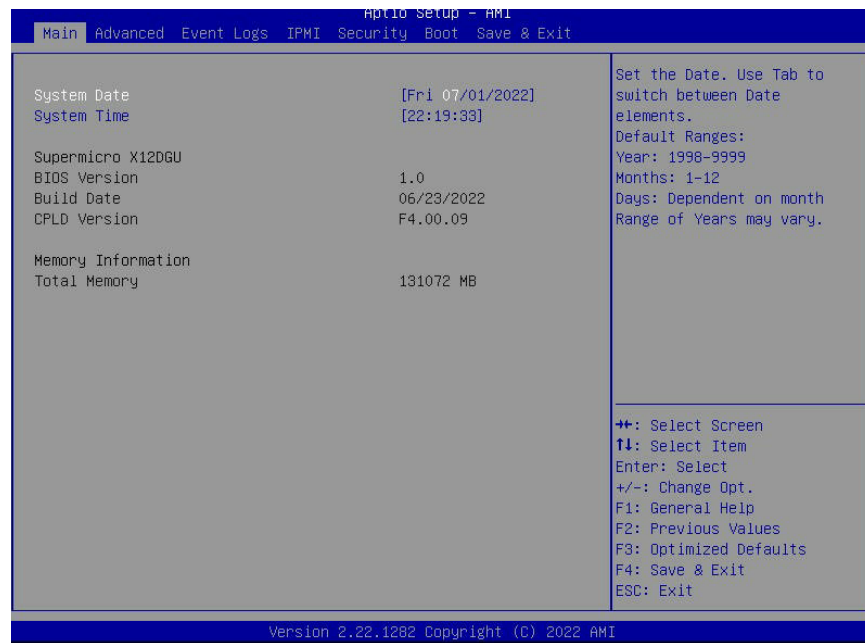
The Main BIOS screen has two main frames. The left frame displays all the options that can be configured. "Grayed-out" options cannot be configured. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it. Please note that BIOS has default text messages built in, and we retain the option to include, omit, or change any of these text messages. Settings printed in **Bold** are the default values.

A "►" indicates a submenu. Highlighting such an item and pressing the <Enter> key will open the list of settings within that submenu.

The BIOS setup utility uses a key-based navigation system called hot keys. Most of these hot keys (<F1>, <F2>, <F3>, <F4>, <Enter>, <ESC>, <Arrow> keys, etc.) can be used at any time during the setup navigation process.

## 4.2 Main Setup

When you first enter the AMI BIOS setup utility, you will see the Main setup screen. You can always return to the Main setup screen by selecting the Main tab on the top of the screen. The Main BIOS setup screen is shown below.



### System Date/System Time

Use this feature to change the system date and time. To change system date and time settings, please highlight *System Date* or *System Time* using the arrow keys and enter new values using the keyboard. Press the <Tab> key or the arrow keys to move between fields. The date must be entered in Day MM/DD/YYYY format. The time is entered in HH:MM:SS format.

 **Note:** The time is in the 24-hour format. For example, 5:30 P.M. appears as 17:30:00. The date's default value is the BIOS build date after the RTC (Real Time Clock) reset.

### Supermicro X12DGU

#### BIOS Version

This feature displays the version of the BIOS ROM used in the system.

#### Build Date

This feature displays the date when the version of the BIOS ROM used in the system was built.

#### CPLD Version

This feature displays the version of the CPLD (Complex-Programmable Logical Device) used in the system.

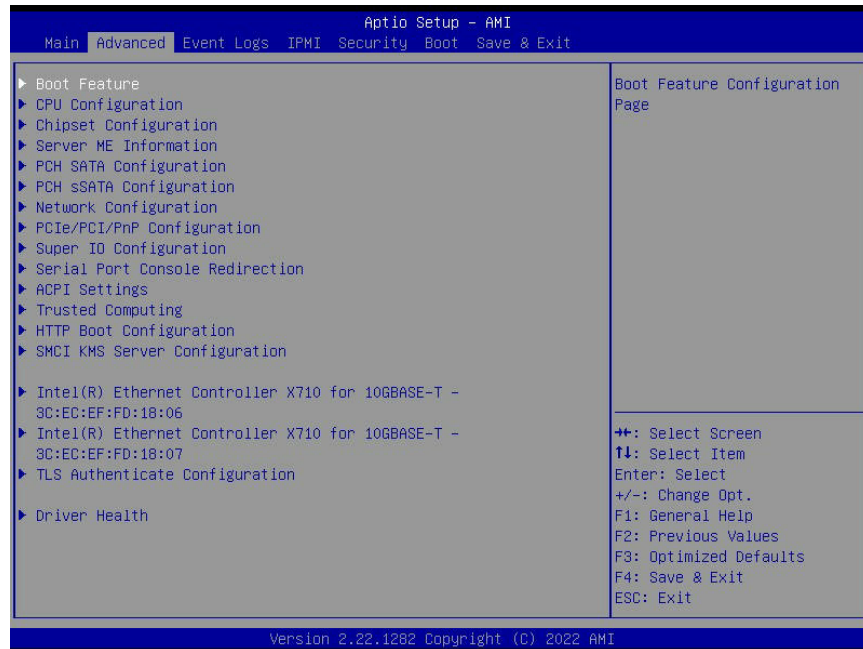
## **Memory Information**

### **Total Memory**

This feature displays the total size of memory available in the system.

## 4.3 Advanced Setup Configurations

Use the arrow keys to select the Advanced submenu and press <Enter> to access the submenu items:



**Warning:** Take Caution when changing the Advanced settings. An incorrect value may cause the system to malfunction. When this occurs, restore the setting to the manufacturer default setting.

### ► Boot Feature

#### Quiet Boot

Use this feature to select the screen between displaying POST messages or the OEM logo at bootup. Select Disabled to display the POST messages. Select Enabled to display the OEM logo instead of the normal POST messages. The options are Disabled and **Enabled**.



**Note:** BIOS POST (Power-on Self Test) messages are always displayed regardless of the setting for this feature.

#### Option ROM Messages

Use this feature to set the display mode for the Option ROM. Select Keep Current to use the current AddOn ROM display settings. Select Force BIOS to use the Option ROM display mode set by the system BIOS. The options are **Force BIOS** and Keep Current.

#### Bootup NumLock State

Use this feature to set the Power-on state for the Numlock key. The options are **On** and Off.

**Wait For 'F1' If Error**

Select Enabled to force the system to wait until the <F1> key is pressed if an error occurs. The options are **Disabled** and Enabled.

**INT19 Trap Response**

Interrupt 19 is the software interrupt that handles the boot disk function. When this feature is set to Immediate, the ROM BIOS of the host adaptors will "capture" Interrupt 19 at bootup immediately and allow the drives that are attached to these host adaptors to function as bootable disks. If this feature is set to Postponed, the ROM BIOS of the host adaptors will not capture Interrupt 19 immediately to allow the drives attached to these adaptors to function as bootable devices at bootup. The options are **Immediate** and Postponed.

**Re-try Boot**

When EFI (Extensible Firmware Interface) Boot is selected, the system BIOS will automatically reboot the system from an EFI boot device after an initial boot failure. Select Legacy Boot to allow the BIOS to automatically reboot the system from a Legacy boot device after an initial boot failure. The options are **Disabled**, Legacy Boot, and EFI Boot.

## Power Configuration

**Watch Dog Function**

Select Enabled to allow the Watch Dog timer to reboot the system when it is inactive for more than 5 minutes. The options are **Disabled** and Enabled.

*If this feature is set to Enabled, the following feature will display:*

**Watch Dog Action (Available when "Watch Dog Function" is set to Enabled.)**

This feature allows the user to determine how the watch dog function can be triggered. The options are **Reset** and NMI.

**Restore on AC Power Loss**

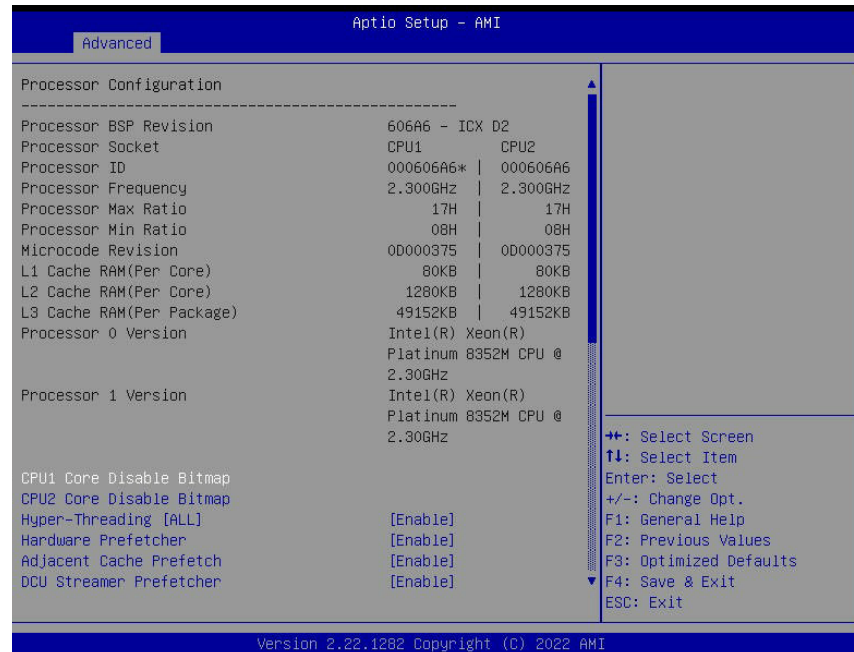
Use this feature to set the power state after a power outage. Select Power Off for the system power to remain off after a power loss. Select Power On for the system power to be turned on after a power loss. Select Last State to allow the system to resume its last power state before a power loss. The options are Stay Off, Power On, and **Last State**.

**Power Button Function**

This feature controls how the system shuts down when the power button is pressed. Select 4 Seconds Override to power off the system after pressing and holding the power button for 4 seconds or longer. Select Instant Off to instantly power off the system as soon as the user presses the power button. The options are **Instant Off** and 4 Seconds Override.

## ► CPU Configuration

**Warning:** Setting the wrong values in the following sections may cause the system to malfunction.



## ► Processor Configuration

The following CPU information will be displayed:

- Processor BSP Revision
- Processor Socket
- Processor ID
- Processor Frequency
- Processor Max Ratio
- Processor Min Ratio
- Microcode Revision
- L1 Cache RAM (Per Core)
- L2 Cache RAM (Per Core)
- L3 Cache RAM (Per Package)
- Processor 0 Version
- Processor 1 Version

## ►CPU1 Core Disable Bitmap/CPU2 Core Disable Bitmap

The following features will display:

Available Bitmap: The available Bitmap will displayed.

### Core Disable Bitmap (Hex)

Enter 0 to enable all CPU cores. Enter FFFFFFFFFF to disable all CPU cores. Please note that at least one core per CPU must be enabled. Disabling all cores is not allowed. The default option is **0**.

### Hyper-Threading (ALL)

Select Enable to use Intel Hyper-Threading Technology to enhance CPU performance. The options are Disable and **Enable**.

### Hardware Prefetcher

If this feature is set to Enable, the hardware prefetcher will prefetch data from the main system memory to Level 2 cache to help expedite data transaction to enhance memory performance. The options are **Enable** and Disable.

### Adjacent Cache Prefetch

Select Enable for the CPU to prefetch both cache lines for 128 bytes as comprised. Select Disable for the CPU to prefetch both cache lines for 64 bytes. The options are **Enable** and Disable. (**Note:** Refer to Intel's website for detailed information.)

### DCU Streamer Prefetcher

If this feature is set to Enable, the DCU (Data Cache Unit) streamer prefetcher will prefetch data streams from the cache memory to the DCU (Data Cache Unit) to speed up data accessing and processing to enhance CPU performance. The options are **Enable** and Disable.

### DCU IP Prefetcher

This feature allows the system to use the sequential load history, which is based on the instruction pointer of previous loads, to determine whether the system will prefetch additional lines. The options are **Enable** and Disable.

### LLC Prefetch

If this feature is set to Enable, LLC (hardware cache) prefetching on all threads will be supported. The options are Disable and **Enable**.

### Extended APIC (Extended Advanced Programmable Interrupt Controller)

Based on the Intel Hyper-Threading technology, each logical processor (thread) is assigned 256 APIC IDs (APIDs) in 8-bit bandwidth. When this feature is set to Enable, the APIC ID will be expanded from 8 bits to 16 bits to provide 512 APIDs to each thread to enhance CPU performance. The options are **Disable** and Enable.

**Intel Virtualization Technology (Available when two processors are installed on the motherboard)**

Select Enable to use Intel Virtualization Technology which will allow multiple workloads to share the same set of common resources. On shared virtualized hardware, various workloads (or tasks) can co-exist, sharing the same resources, while functioning in full independence from each other, and migrating freely across multi-level infrastructures and scale as needed. The settings are Disable and **Enable**.

**Enable SMX**

Select Enable to support Safer Mode Extensions (SMX) which provides a programming interface for system software to establish a controlled environment to support the trusted platform configured by the end user and to verify a virtual machine monitor before it is allowed to run. The options are **Disable** and Enable.

**PPIN Control**

Select Unlock/Enable to use the Protected-Processor Inventory Number (PPIN) in the system. The options are **Lock/Disable** and Unlock/Enable.

**AES-NI**

Select Enable to use the Intel Advanced Encryption Standard (AES) New Instructions (NI) to ensure data security. The options are Disable and **Enable**.

-----  
TME, TME-MT, TDX  
-----

**Total Memory Encryption (TME)**

Select Enabled for total memory encryption support to enhance memory data security. The options are **Disabled** and Enabled.

*If this feature is set to Enabled, the followings item will display:*

**Total Memory Encryption Multi-Tenant (TME-MT) (Available when "Total Memory Encryption" is set to Enabled & "Limit CPU PA to 46 Bits" below is set to Disable)**

Select Enabled for Total Memory Encryption Multi-Tenant support to maximize memory data security. The options are **Disabled** and Enabled.

*If this feature is set to Enabled, the followings item will display:*

**Max TME-MT Keys (Available when Total Memory Encryption is set to Enabled)**

This feature displays the value of maximum Total Memory Encryption Multi-Tenant (TME-MT) keys.

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## Software Guard Extension (SGX)

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**Note:** For SGX to work properly, please use the CPUs that support this feature and be sure to install one CPU per channel.

### **SGX Factory Reset (Available when TME-MT is set to Enabled and the SGX feature is supported by the CPU used in the system)**

Select Enabled to reset the factory default setting for SGX (Software Guard Extension). The options are **Disabled** and Enabled.

### **SW (Software) Guard Extensions (SGX) (Available when TME-MT is set to Enabled and the SGX feature is supported by the CPU used in the system)**

Select Enabled to support Software Guard Extensions (SGX) for memory data security enhancement. The options are **Disabled** and Enabled.

### **SGX Package Info In-Band Access (Available when TME-MT is set to Enabled and the SGX feature is supported by the CPU used in the system)**

If this feature is set to Enabled, Software Guard Extensions (SGX) package information will become available for in-band access. The options are **Disabled** and Enabled.

### **Limit CPU PA to 46 bits**

Select Enable to limit CPU physical address to 46 bits to support the older Hyper-v CPU platform. The options are Disable and **Enable**.

## ►Advanced Power Management Configuration

### **Power Technology**

Select Energy Efficient to support power-saving mode. Select Custom to customize system power settings. Select Disabled to disable power-saving settings. The options are Disable, Energy Efficient, and **Custom**.

### **Power Performance Tuning (Available when "Power Technology" is set to Custom)**

Select BIOS to allow the system BIOS to configure the Power-Performance Tuning Bias setting. The options are **OS Controls EPB** and BIOS Controls EPB.

**ENERGY\_PERF\_BIAS\_CFG Mode (ENERGY PERFORMANCE BIAS CONFIGURATION Mode) (Available when "Power Performance Tuning" is set to BIOS Controls EPB)**

Use this feature to configure the proper operation setting for your machine by achieving the desired system performance level and energy saving (efficiency) level at the same time. Select Maximum Performance to maximize system performance; however, this may cause maximum power consumption. The options are Maximum Performance, Performance, **Balanced Performance**, Balanced Power, and Power.

**►CPU P State Control (Available when "Power Technology" is set to Custom)****SpeedStep (P-States)**

EIST (Enhanced Intel SpeedStep Technology) allows the system to automatically adjust processor voltage and core frequency for power consumption and heat dissipation reduction. Please refer to Intel's website for detailed information. The options are Disable and **Enable**.

**AVX P1 (Not available when "SpeedStep (P-states)" is set to Disable)**

Select Normal for the Intel® Advance Vector Extensions (Intel® AVX) feature to operate normally, which will provide a set of instructions for doing Single Instruction Multiple Data (SIMD) operations in Intel processors by adding MMX and SSE support. The options are **Normal**, Level 1, and Level 2.

**Dynamic SST-PP (Speed Select Technology-Power Profile)**

Select Base for Intel SST-PP to operate at the base level. The options are **Disable** and Enable.

**Intel SST-PP (Speed Select Technology-Power Profile) (Available when "Dynamic SST-PP" is set to Disable)**

Select Base for Intel SST-PP to operate at the base level. The options are **Base**, Config 3, and Config 4.

The following sub-items will display:

- Intel SST-PP
- Core Count
- Current P1 Ration [0]
- Package TDP [W]
- Tjmax

**EIST (Enhanced Intel SpeedStep Technology) PSD Function (Available when "SpeedStep" is set to Enable)**

This feature reduces the latency that occurs when one P-state changes to another, thus allowing the transitions of P-state changing to occur more frequently. This will permit more demand-based P-state changing or switching to occur more frequently based on the real-time energy needs of the applications so that the power-vs-performance balance can be optimized for energy efficiency. The options are **HW\_ALL** and **SW\_ALL**.

**Turbo Mode (Available when "SpeedStep" is set to Enable)**

Select enable to allow the CPU to operate at the manufacturer-defined turbo speed by increasing CPU clock frequency. This feature is available when it is supported by the processors used in the system. The options are **Disable** and **Enable**. CPU Flex Ratio Override (Available when supported by the CPU installed on the motherboard)

Select enable to override the CPU Flex-Ratio setting, which is the minimum multiplier that allows the computer to clock. The options are **Disable** and **Enable**.

**CPU Flex Ratio Override (Available when supported by the CPU installed on the motherboard)**

Select enable to override the CPU Flex-Ratio setting, which is the minimum multiplier that allows the computer to clock. The options are **Disable** and **Enable**.

**CPU Core Flex Ratio (Available when supported by the CPU installed on the motherboard and when "CPU Flex Ratio Override" is set to Enable)**

Use this feature to configure the Core Ratio Multiplier settings for non-Turbo mode processors. The default setting is **23**.

**►Hardware PM (Power Management) State Control (Available when "Power Technology" is set to Custom)****Hardware P-States**

If this feature is set to **Disable**, system hardware will choose a P-state setting for the system based on the OS request. If this feature is set to **Native Mode**, hardware will choose a P-state setting based on the OS guidance. If this feature is set to **Native Mode with No Legacy Support**, system hardware will choose a P-state setting independently without OS guidance. The options are **Disable**, **Native Mode**, **Out of Band Mode**, and **Native Mode with No Legacy Support**.

**►Frequency Prioritization****RAPL Prioritization**

Select **Enable** to prioritize RAPL (Running Average Power Limit) which sets the power consumption limit for a processor to save energy. The options are **Enable** and **Disable**.

## ►CPU C State Control

### Enable Monitor/Mwait

Select Enable to support Monitor and Mwait, which are two instructions in Streaming SIMD Extension 3 (SSE3), to improve synchronization between multiple threads for CPU performance enhancement. The options are Disable and **Enable**.

### CPU C6 Report

Select Enable to allow the BIOS to report the CPU C6 state (ACPI C3) to the operating system. During the CPU C6 state, power to all caches is turned off. The options are Disable, Enable, and **Auto**.

### Enhanced Halt State (C1E)

Select Enable to enable "Enhanced Halt State" support, which will significantly reduce the CPU's power consumption by minimizing CPU's clock cycles and reduce voltage during a "Halt State". The options are Disable and **Enable**.

## ►Package C State Control (Available when "Power Technology" is set to Custom)

### Package C State

Use this feature to optimize and reduce CPU package power consumption in the idle mode. Please note that the changes you've made in this setting will affect all CPU cores or the circuits of the entire system. The options are C0/C1 state, C2 state, C6 (non-Retention) state, and **Auto**.

## ►CPU T State Control (Available when "Power Technology" is set to Custom)

### Software Controlled T-States

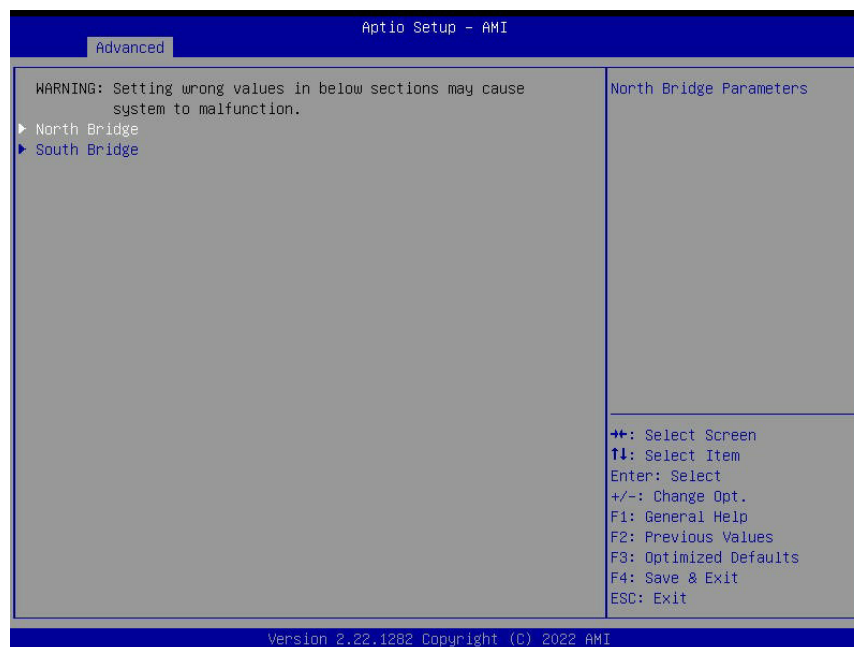
If this feature is set to Enable, CPU throttling will be controlled by the OS, which will reduce the speed of CPU. The options are **Disable** and Enable.

### T-State Throttle Level (Available when "Software Controlled T-States" is set to Enable)

Select Enable to configure the on-die thermal throttling setting. The options are **Disable**, 6.25%, 12.5%, 18.75%, 25.0%, 31.25%, 37.5%, 43.75%, 50.0%, 56.25%, 62.5%, 68.75%, 75.0%, 81.25%, 87.5%, and 93.75%.

## ► Chipset Configuration

**Warning:** Setting the wrong values in the following items may cause the system to malfunction.



## ► North Bridge

This feature allows the user to configure Intel North Bridge parameters.

### ► Uncore Configuration

The following information will be displayed:

- Number of CPU
- Number of IIO
- Current UPI Link Speed
- Current UPI Link Frequency
- Global MMIO Low Base/Limit
- Global MMIO High Base/Limit
- PCIe Configuration Base/Size

### Degrade Precedence

Use this feature to select the degrading precedence option for Ultra Path Interconnect (UPI) connections. Select Topology Precedent to degrade UPI features if the system options are in conflict. Select Feature Precedent to degrade UPI topology if system options are in conflict. The options are **Topology Precedence** and Feature Precedence.

### Link L0p Enable

Select Enable for the system BIOS to enable Link L0p support which will allow the CPU to reduce the UPI links from full width to half width in the event when the CPU's workload is low in an attempt to save power. This feature is available for the system that uses Intel processors with UPI technology support. The options are **Disable**, Enable, and Auto.

 **Note:** You can change the performance settings for non-standard applications by using this parameter. It is recommended that the default settings be used for standard applications.

### Link L1 Enable

Select Enable for the BIOS to activate Link L1 support which will power down the UPI links to save power when the system is idle. This feature is available for the system that uses Intel processors with UPI technology support. The options are **Disable**, Enable, and Auto.

 **Note:** Link L1 is an excellent feature for an idle system. L1 is used during Package C-States when its latency is hidden by other components during a wakeup.

### XPT Remote Prefetch

Select Enable to support XPT (Extended Prediction Table) Remote Prefetch which will allow an LLC request to be duplicated and sent to an appropriate memory controller in a remote machine based on the recent LLC history to reduce latency. The options are Disable, Enable, and **Auto**.

### KTI Prefetch

Select Enable for the KTI prefetcher to preload the L1 cache with data deemed relevant which will allow the memory read to start earlier on a DDR bus in an effort to reduce latency. Select Auto for the KTI prefetcher to automatically preload the L1 cache with relevant data whenever is needed. The options are Disable, Enable, and **Auto**.

### Local/Remote Threshold

Use this feature to set the threshold for the Interrupt Request (IRQ) signals, which handle hardware interruptions. The options are Disable, **Auto**, Low, Medium, and High.

### IO Directory Cache (IODC)

Select Enable for the IODC (I/O Directory Cache) to generate snoops instead of generating memory lockups for remote IIO (InvlToM) and/or WCiLF (Cores). Select Auto for the IODC to generate snoops (instead of memory lockups) for WCiLF (Cores). The options are Disable, **Auto**, Enable for Remote InvltoM Hybrid Push, InvltoM AllocFlow, Enable for Remote InvltoM Hybrid AllocNonAlloc, and Enable for Remote InvltoM and Remote WViLF.

### SNC (Sub NUMA)

Select Enable to use "Sub NUMA Clustering" (SNC), which supports full SNC (2-cluster) interleave and 1-way IMC interleave. Select Auto for 1-cluster or 2-cluster support depending on the status of IMC (Integrated Memory Controller) Interleaving. The options are **Disable** and Enable SNC2 (2-clusters).

### XPT Prefetch

Select Enable to support XPT (Extended Prediction Table) Prefetch which will allow an LLC request to be duplicated and sent to an appropriate memory controller based on the recent LLC history to reduce latency. The options are Disable, Enable, and **Auto**.

### Snoop Throttle Configuration

Use this feature to set the level of snoop throttle for the PCH, which will determine how much speed to decrease in operation when the system is in the snoop state. The options are Disabled, Low, Medium, High, and **Auto**.

### PCIe Remote P2P Relaxed Ordering

Select Disable to support PCIe remote peer-to-peer relaxed writing ordering, which will allow hardware to enforce peer-to-peer write ordering. The options are **Disable** and Enable.

### Stale AtoS (A to S)

The in-memory directory has three states: I, A, and S states. The I (-invalid) state indicates that the data is clean and does not exist in the cache of any other sockets. The A (-snoop All) state indicates that the data may exist in another socket in an exclusive or modified state. The S state (-Shared) indicates that the data is clean and may be shared in the caches across one or more sockets. When the system is performing "read" on the memory and if the directory line is in A state, we must snoop all other sockets because another socket may have the line in a modified state. If this is the case, a "snoop" will return the modified data. However, it may be the case that a line "reads" in an A state, and all the snoops come back with a "miss". This can happen if another socket reads the line earlier and then has silently dropped it from its cache without modifying it. If "Stale AtoS" is enabled, a line will transition to the S state when the line in the A state returns only snoop misses. That way, subsequent reads to the line will encounter it in the S state and will not have to snoop, saving the latency and snoop bandwidth. Stale "AtoS" may be beneficial in a workload where there are many cross-socket reads. The options are Disable, Enable, and **Auto**.

### LLC Dead Line Alloc

Select Enable to opportunistically fill the deadlines in the LLC. The options are Disable, **Enable**, and Auto.

## ►Memory Configuration

This feature allows the user to configure the Integrated Memory Controller (iMC) settings.

### Enhanced PPR

Select Enable to enable enhanced PPR (Post Package Repair) function for DDR4 modules. The options are **Disable** and Enable.

### Enforce POR (Plan of Record)

Select POR to enforce POR restrictions for DDR4 memory frequency and voltage programming. The options are **POR** and Disable.

### PPR Type

Post Package Repair (PPR) is a new feature available for the DDR4 Technology. PPR provides additional spare capacity within a DDR4 DRAM module that is used to replace faulty cell areas detected during system boot. PPR offers two types of memory repairs. Soft Post Package Repair (sPPR) provides a quick, temporary fix on a raw element in a bank group of a DDR4 DRAM device, while hard Post Package Repair (hPPR) will take a longer time to provide a permanent repair on a raw element. The options are PPR Disabled, **Hard PPR**, and Soft PPR.

### Memory Frequency

Use this feature to set the maximum memory frequency for onboard memory modules. The options are **Auto**, 2133, 2200, 2400, 2600, 2666, 2800, 2933, 3000, and 3200. (**Note:** Maximum memory frequency is dependent on the CPU SKU.)

### Data Scrambling for DDR4

Select Enable to enable data scrambling for DDR4 modules to enhance memory data security. The options are Disable and **Enable**.

### 2X Refresh Enable

Select Enable for memory 2X refresh support to enhance memory performance. The options are **Auto**, Disable, and Enable.

## ►Memory Topology

This item displays the information of onboard memory modules as detected by the BIOS, for example:

- P1-DIMMA1: 3200MT/s Micron DRx4 64GB RDIMM
- P2-DIMMA1: 3200MT/s Micron DRx4 64GB RDIMM

## ►Memory RAS (Reliability\_Availability\_Serviceability) Configuration

Use this submenu to configure the following Memory RAS settings.

### **Enable Pcode WA (Workaround) for SAI (Security Attribute of the Initiator) PG (Policy Group)**

Pcode, a register transfer language designed for reverse engineering, translates individual processor instructions into a sequence of Pcode operations in order to facilitate the construction of data-flow graphs and disassembling of processor instructions for machine application. Select Enabled to allow Pcode to work around the SAI group policy to achieve a solution with a next-step instruction. The options are **Disabled** and Enabled.

### **Mirror Mode (Unavailable when "ADDDC Sparring" below is set to Disabled)**

Use this feature to configure the mirror mode settings for all 1LM/2LM memory modules in the system which will create a duplicate copy of data stored in the memory to increase memory security, but it will reduce the memory capacity into half. The options are **Disabled**, Full Mirror Mode, and Partial Mirror Mode.

### **UEFI ARM Mirror (Unavailable when "ADDDC Sparring" below is set to Disabled)**

If this feature is set to Enable, mirror mode configuration settings for UEFI-based Address Range memory will be enabled upon system boot. This will create a duplicate copy of data stored in the memory to increase memory security, but it will reduce the memory capacity into half. The options are **Disabled** and Enabled.

### **Correctable Error Threshold**

Use this feature to enter the threshold value for correctable memory errors. The default setting is **512**.

### **Partial Cache Line Sparring PCLS**

Select Enabled to support partial cache line sparring, which will allow partial of data contained in a cache line to be copied in the cache memory for safe-keeping/data security. The options are Disabled and **Enabled**.

### **ADDDC (Adaptive Double Device Data Correction) Sparing**

Select Enable for Adaptive Double Device Data Correction (ADDDC) support, which will not only provide memory error checking and correction but will also prevent the system from issuing a performance penalty before a device fails. Please note that virtual lockstep mode will only start to work for ADDDC after a faulty DRAM module is spared. The options are Disabled and **Enabled**.

### **Patrol Scrub**

Patrol Scrubbing is a process that allows the CPU to correct correctable memory errors detected in a memory module and send the corrections to the requestor (the original source). When this feature is set to Enable, the IO hub will read and write back one cache line every 16K cycles if there is no delay caused by internal processing. By using this method, roughly 64 GB of memory behind the IO hub will be scrubbed every day. The options are Disabled, Enabled, and **Enable at End of POST (Power\_On Self Test)**.

## **►IIO Configuration**

### **►CPU1 Configuration/CPU2 Configuration**

#### **IOU0 (IIO PCIe Port 1)**

Use this feature to configure the PCIe Bifurcation setting for a PCIe port specified by the user. The options are **Auto**, x4x4x4x4, x4x4x8, x8x4x4, x8x8, and x16.

#### **IOU1 (IIO PCIe Port 2)**

Use this feature to configure the PCIe Bifurcation setting for a PCIe port specified by the user. The options are **Auto**, x4x4x4x4, x4x4x8, x8x4x4, x8x8, and x16.

#### **IOU3 (IIO PCIe Port 4)**

Use this feature to configure the PCIe Bifurcation setting for a PCIe port specified by the user. The options are **Auto**, x4x4x4x4, x4x4x8, x8x4x4, x8x8, and x16.

#### **IOU4 (IIO PCIe Port 5)**

Use this feature to configure the PCIe Bifurcation setting for a PCIe port specified by the user. The options are **Auto**, x4x4x4x4, x4x4x8, x8x4x4, x8x8, and x16.

## **►Port 0/DMI**

### **Link Speed**

Use this feature to configure the link speed of a PCIe device installed in Port 0 or DMI port. The options are **Auto**, Gen 1 (2.5 GT/s), Gen 2 (5 GT/s), and Gen 3 (8 GT/s).

The following information will be displayed:

- PCIe Port Link Status
- PCIe Port Link Max
- PCIe Port Link Speed

### **Data Link Feature Exchange**

Use this feature to enable the data link feature negotiation in the PCIe Device Capabilities Register for the device installed in the DMI port. The options are Disable and **Enable**.

### **DMI Port MPSS**

Use this feature to set the maximum payload size in the PCIe Device Capabilities Register for the device installed in the DMI port. The options are 128B, 256B, and **Auto**.

## **►IOAT Configuration**

### **Disable TPH**

TPH (TLP Processing Hint) is used for data-tagging with a destination ID and a few important attributes. It can send critical data to a particular cache without writing through to memory. Select No for TLP Processing Hint support, which will allow a "TLP request" to provide "hints" to help optimize the processing of each transaction occurred in the target memory space. The options are **No** and Yes.

### **Prioritize TPH (TLP Processing Hint)**

Select Enable to prioritize the TPL requests that will allow the "hints" to be sent to help facilitate and optimize the processing of certain transactions in the system memory. The options are Enable and **Disable**.

### **Relaxed Ordering**

Select Yes to allow certain transactions to be processed and completed before other transactions that have already been enqueued. The options are **No** and Yes.

## **►Intel VT for Directed I/O (VT-d)**

### **Intel® VT for Directed I/O (VT-d)**

Select Enable to enable the Intel Virtualization Technology support for Direct I/O VT-d by reporting the I/O device assignments to the VMM (Virtual Machine Monitor) through the DMAR ACPI tables. This feature offers fully-protected I/O resource sharing across Intel platforms, providing greater reliability, security and availability in networking and data-sharing. The options are **Enable** and Disable.

**ACS (Access Control Services) Control (Available when Intel® VT for Directed I/O (VT-d) is set to Enable)**

Select Enable to program ACS control to Chipset PCIe Root Port bridges. Select Disable to program ACS control to all PCIe Root Port bridges. The options are **Enable** and Disable.

**Interrupt Remapping (Available when "Intel® VT for Directed I/O (VT-d)" is set to Enable)**

Select Enable to support I/O DMA transfer remapping and device-generated interrupts. The options are **Auto**, Enable, and Disable.

**►Intel® VMD (Volume Management Device) Technology**

This section describes the configuration settings for the Intel VMD Technology.



**Note 1.** After you've enabled VMD in the BIOS on a PCIe slot, this PCIe slot will be dedicated for VMD use only, and it will no longer support any PCIe device. To re-activate this slot for PCIe use, please disable VMD in the BIOS.

**Note 2.** PCIe slots and naming can differ depending on the PCIe devices installed on your motherboard.

**►Intel® VMD for Volume Management Device on CPU1****VMD Configuration for IOU 0/IOU 1/IOU 3/IOU 4****Enable/Disable VMD**

Select Enable to enable Intel Volume Management Device Technology support for the root port specified by the user. The options are Enable and **Disable**.

\*If Enable/Disable VMD is set to Enable to a port specified by the user, the following items will display for the port selected.

**VMD Port 1A~1D/VMD Port 2A~2D (Available for onboard NVMe-responding ports only)**

Select Enable to enable Intel Volume Management Device Technology support for the (M.2 HC S-SATA4/5) root port. The options are **Enable** and Disable.

**Hot Plug Capable**

Select Enable to enable Hot Plug support for the root ports specified by the user, which will allow the user to change the devices on those root ports without shutting down the system. The options are **Enable** and Disable.

**VMD for Direct Assign**

Select Enable to support VMD for Direct Assign feature. The options are **Disable** and Enable.

## ►Intel® VMD for Volume Management Device on CPU2

### VMD Configuration for IOU 0/IOU 1/IOU 3/IOU 4

#### Enable/Disable VMD

Select Enable to enable Intel Volume Management Device Technology support for the root port specified by the user. The options are Enable and **Disable**.

\*If Enable/Disable VMD is set to Enable to a port specified by the user, the following items will display for the port selected.

#### P1\_NVME0/P1\_NVME1 (Available when VMD Config for IOU0 is set to Enable)

Select Enable to enable Intel Volume Management Device Technology support for the VMD devices in this stack. The options are **Enable** and Disable.

#### VMD Port 2A~VMD Port 2D (Available when VMD Config for IOU1 is set to Enable)

Select Enable to enable Intel Volume Management Device Technology support for the VMD devices in this stack. The options are **Enable** and Disable.

#### VMD Port 4A~VMD Port 4D (Available when VMD Config for IOU3 is set to Enable)

Select Enable to enable Intel Volume Management Device Technology support for the VMD devices in this stack. The options are **Enable** and Disable.

#### VMD Port 5A~VMD Port 5B (Available when VMD Config for IOU4 is set to Enable)

Select Enable to enable Intel Volume Management Device Technology support for the VMD devices in this stack. The options are **Enable** and Disable.

#### Hot Plug Capable

Select Enable to enable Hot Plug support for the root ports specified by the user, which will allow the user to change the devices on those root ports without shutting down the system. The options are **Enable** and Disable.

#### VMD for Direct Assign

Select Enable to support VMD for Direct Assign feature. The options are **Disable** and Enable.

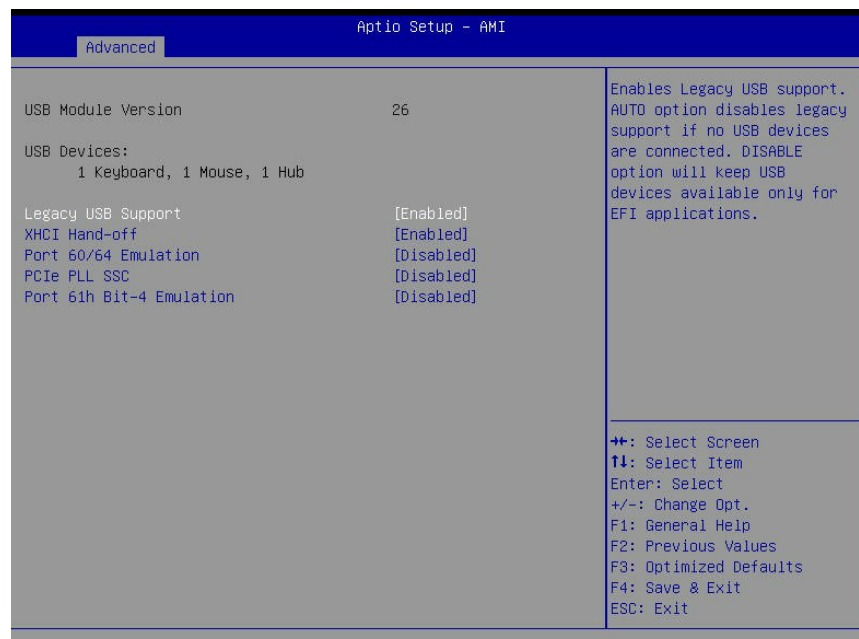
#### PCI-E ASPM Support (Global)

Select auto to enable Intel Volume Management Device Technology support for the root port specified by the user. The options are **Disable** and Auto.

## IIO eDPC (Enhanced Downstream Port Containment) Support

Use this feature to configure the setting for IIO Enhanced Downstream Port Containment (eDPC) support for your system in an effort to improve the error containment capacity within the PCIe subsystem when an uncorrected error is detected either at the root port or at the switch downstream port. Select Disable to disable IIO eDPC support. Select On Fatal Error to enable IIO eDPC support in your system when a fatal error occurs. Select On Fatal and Non-Fatal Error to enable IIO eDPC support when an error, fatal or non-fatal, has occurred. The options are **Disable**, On Fatal Error, and On Fatal and Non-Fatal Errors.

## ► South Bridge



- USB Module Version
- USB Devices

## Legacy USB Support

Select Enabled to support onboard legacy USB devices. Select Auto to disable legacy support if there are no legacy USB devices present. Select Disable to have all USB devices available for EFI applications only. The options are **Enabled**, Disabled and Auto.

## XHCI Hand-Off

This is a work-around solution for operating systems that do not support XHCI (Extensible Host Controller Interface) hand-off. The XHCI ownership change should be claimed by the XHCI driver. The options are **Enabled** and Disabled.

### **Port 60/64 Emulation**

Select Enabled for I/O port 60h/64h emulation support, which in turn, will provide complete legacy USB keyboard support for the operating systems that do not support legacy USB devices. The options are **Disabled** and Enabled.

### **PCIe PLL SSC**

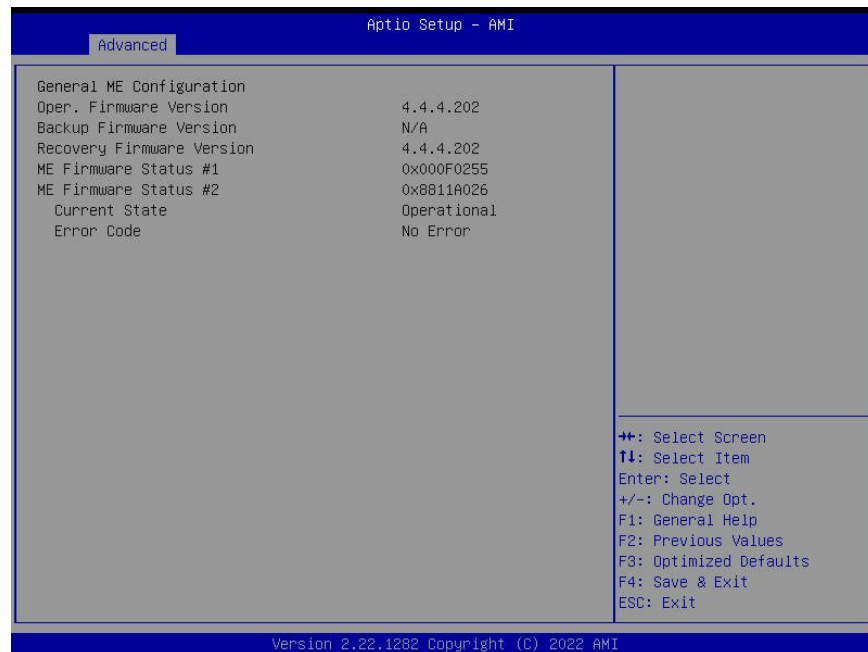
Select Enabled for PCH PCIe Spread Spectrum Clocking support, which will allow the BIOS to monitor and attempt to reduce the level of electromagnetic interference caused by the components whenever needed. The options are **Disabled** and Enabled.

### **Port 61h Bit-4 Emulation**

Select Enabled for I/O Port 61h-Bit 4 emulation support to enhance system performance. The options are **Disabled** and Enabled.

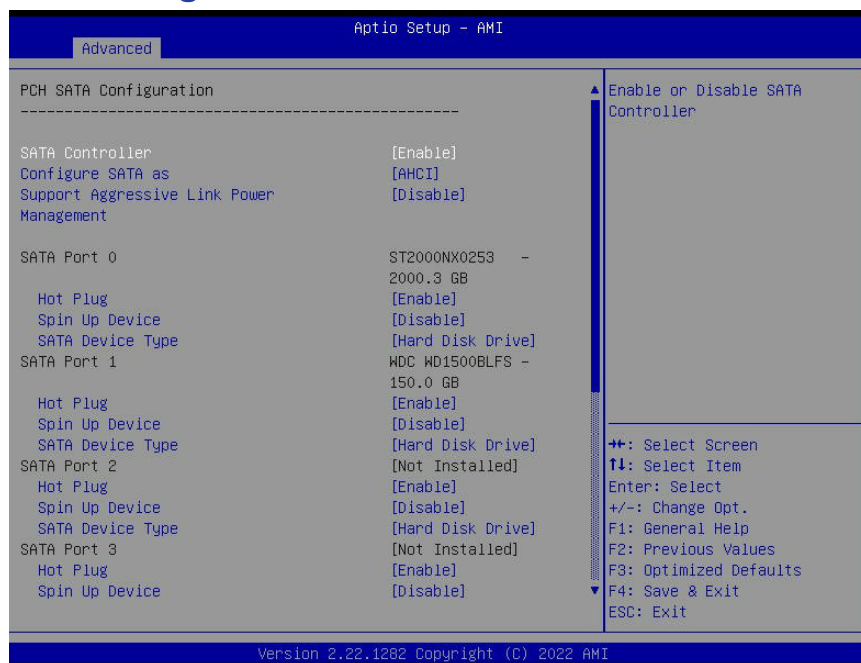
## ► Server ME (Management Engine) Configuration

This feature displays the following general ME configuration settings:



- General ME Configuration
- Oper. (Operation) Firmware Version
- Backup Firmware Version
- Recovery Firmware Version
- ME Firmware Status #1
- ME Firmware Status #2
  - Current State
  - Error Code

## ► PCH SATA Configuration



### SATA Controller

This feature enables or disables the onboard SATA controller supported by the Intel PCH chip. The options are Disable and **Enable**.

### Configure SATA as (Available when "SATA Controller" is set to Enable)

Select AHCI to configure a SATA drive specified by the user as an AHCI drive. Select RAID to configure a SATA drive specified by the user as a RAID drive. The options are **AHCI** and RAID.

### SATA RSTe Boot Info (Available when "Configure SATA as" is set to RAID)

Select Enable for full int13h support which will allow the system to boot using a device attached to the SATA controller. The options are Disable and **Enable**.



**Note:** For this feature to work properly, please set the CSM Storage OPRM policy to Legacy.)

### Support Aggressive Link Power Management

When this feature is set to Enable, the SATA AHCI controller manages the power use of the SATA link. The controller will put the link in a low power mode during an extended period of I/O inactivity, and will return the link to an active state when I/O activity resumes. The options are **Disable** and Enable.

**SATA RAID Option ROM/UEFI Driver (Available when "Configure SATA as" is set to RAID)**

Select EFI to load the EFI driver for system boot. Select Legacy to load a legacy driver for system boot. The options are Disable, **EFI**, and Legacy.

**SATA Port 0 - SATA Port 7****Hot Plug**

Select Enable to support Hot-plugging for the device installed on a selected SATA port which will allow the user to replace the device installed in the slot without shutting down the system. The options are Disable and **Enable**.

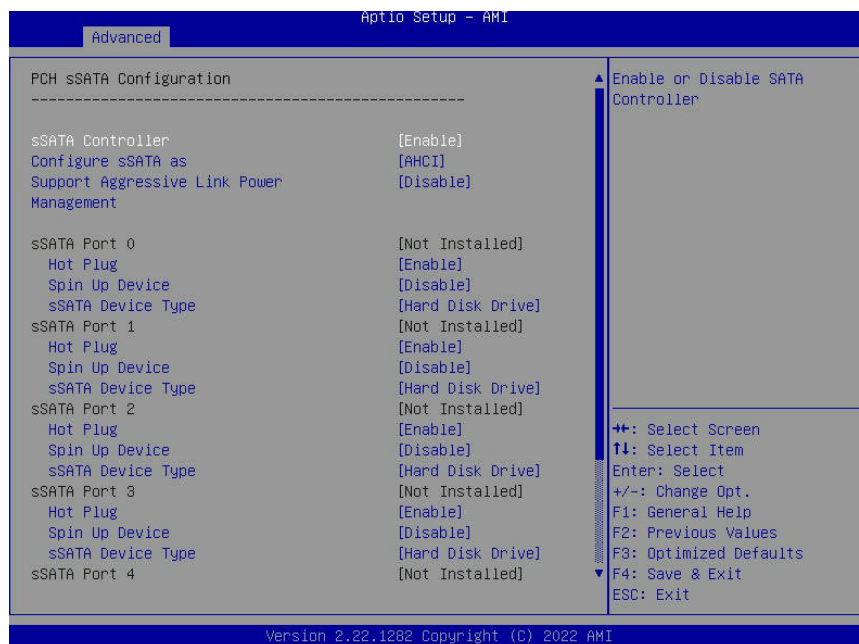
**Spin Up Device**

Select Enable for Staggered Spin Up support which will allow the SATA devices specified by the user to spin up one at a time at boot up in an effort to prevent all hard drive disks from spinning up at the same time, causing a power surge. The options are **Disable** and Enable.

**SATA Device Type**

Use this feature to specify if the device installed on the SATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are **Hard Disk Drive** and Solid State Drive.

## ► PCH sSATA Configuration



## PCH sSATA Configuration

### sSATA Controller

This feature enables or disables the onboard sSATA controller supported by the Intel PCH. The options are **Enable** and **Disable**.

### Configure sSATA as (Available when "sSATA Controller" is set to Enable)

Select AHCI to configure an sSATA drive specified by the user as an AHCI drive. Select RAID to configure an sSATA drive specified by the user as a RAID drive. The options are **AHCI** and **RAID**.

### sSATA RSTe Boot Info (Available when "Configure sSATA as" is set to RAID)

Select Enable for full int13h support which will allow the system to boot using a device attached to the SATA controller. The options are **Disable** and **Enable**.

 **Note:** For this feature to work properly, please set the CSM Storage OPRM policy to Legacy.

### Support Aggressive Link Power Management

When this feature is set to Enable, the sSATA AHCI controller manages the power use of the sSATA link. The controller will put the link in a low power mode during an extended period of I/O inactivity, and will return the link to an active state when I/O activity resumes. The options are **Disable** and **Enable**.

**sSATA RAID Option ROM/UEFI Driver (Available when "Configure sSATA as" is set to RAID)**

Select EFI to load the EFI driver for system boot. Select Legacy to load a legacy driver for system boot. The options are Disable, **EFI**, and Legacy.

**sSATA Port 0 - sSATA Port 5****Hot Plug**

Select Enable to support Hot-plugging for the device installed on an sSATA port specified by the user which will allow the user to replace the device installed in the slot without shutting down the system. The options are Disabled and **Enable**.

**Spin Up Device**

Select Enable for Staggered Spin Up support which will allow the SATA devices specified by the user to spin up one at a time at bootup preventing all hard drive disks from spinning up at the same time, causing a power surge. The options are **Disable** and Enable.

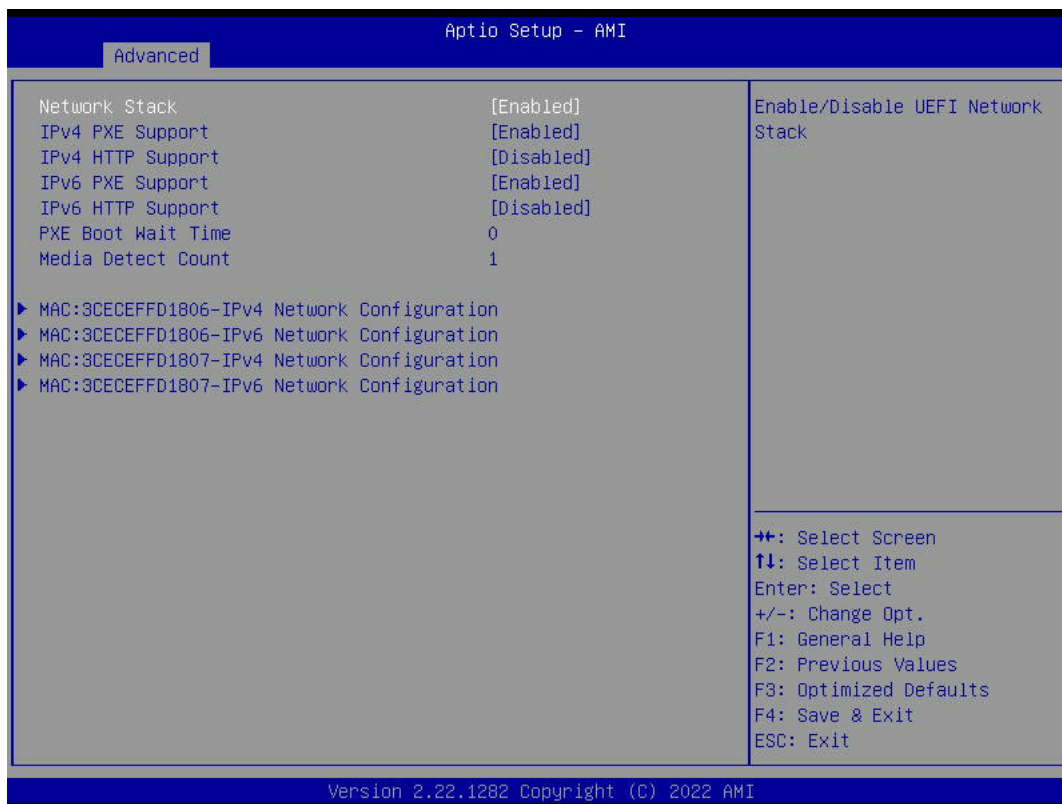
**sSATA Device Type**

Use this feature to specify if the device installed on the sSATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are **Hard Disk Drive** and Solid State Drive.

## ► Network Stack Configuration



**Note:** The display of MAC address is for illustration only. It is unique per system.



### Network Stack

Select Enabled to enable PXE (Preboot Execution Environment) or UEFI (Unified Extensible Firmware Interface) for network stack support. The options are Disabled and **Enabled**.

*\*If "Network Stack" is set to Enabled, the following items will display:*

### IPv4 PXE Support

Select Enabled to enable IPv4 PXE boot support. If this feature is disabled, it will not create the IPv4 PXE boot option. The options are Disabled and **Enabled**.

### IPv4 HTTP Support

Select Enabled to enable IPv4 HTTP boot support. If this feature is disabled, it will not create the IPv4 HTTP boot option. The options are **Disabled** and Enabled.

### IPv6 PXE Support

Select Enabled to enable IPv4 PXE boot support. If this feature is disabled, it will not create the IPv6 PXE boot option. The options are Disabled and **Enabled**.

### IPv6 HTTP Support

Select Enabled to enable IPv4 HTTP boot support. If this feature is disabled, it will not create the IPv6 HTTP boot option. The options are **Disabled** and Enabled.

**PXE Boot Wait Time**

Use this feature to set the wait time (in seconds) upon which the system BIOS will wait for user to press the <ESC> key to abort PXE boot instead of proceeding with PXE boot by connecting to a network server immediately. The default is **0**.

**Media Detect Count**

Use this feature to select the wait time (in seconds) for the BIOS ROM to detect the presence of a LAN media either via the Internet connection or via a LAN port. The default is **1**.

**►MAC: 3CECEFFD1806 - IPv4 Network Configuration****Configured**

Select Enabled to show whether the network address has been successfully configured or not. The options are **Disabled** and Enabled.

\*If this feature is set to Enabled, the following items will display.

**Enable DHCP**

Select Enabled to support Dynamic Host Configuration Protocol (DHCP) which will allow the AMI BIOS to search for a DHCP server attached to the network and request the next available IP address for this computer. The options are **Disabled** and Enabled.

**Local IP Address:** Use this feature to enter an IP address for the local machine.

**Local Netmask:** Use this feature to set the netmask for the local machine.

**Local Gateway:** Use this feature to set the gateway for the local machine.

**Local DNS (Domain Name System) Servers** Use this feature to set the DNS server for the local machine.

**Save Changes and Exit.**

Select Yes to save the changes that you've made and exit from this submenu.

**►MAC: 3CECEFFD1806 - IPv6 Network Configuration****►Enter Configuration Menu**

The following features will display:

- Interface Name
- Interface Type
- MAC Address

- Host Addresses
- Route Table
- Gateway Addresses
- DNS Addresses

### **Interface ID**

This feature displays the Interface ID used in the network.

### **DAD (Duplicate Address Detection) Transmit Count**

This feature displays the DAD Transmit Count. The default setting is **1**.

### **Policy**

Use this feature to select how the policy is to be configured. The options are **Automatic** and **Manual**.

### **Save Changes and Exit.**

Select Yes to save the changes that you've made and exit from this submenu.

## ► **MAC: 3CECEFFD1807 - IPv4 Network Configuration**

### **Configured**

Select Enabled to show whether the network address has been successfully configured or not. The options are Disabled and **Enabled**.

### **Enable DHCP**

Select Enabled to support Dynamic Host Configuration Protocol (DHCP) which will allow the BIOS to search for a DHCP server attached to the network and request the next available IP address for this computer. The options are **Disabled** and **Enabled**.

**Local Address:** Use this feature to enter an IP address for the local machine.

**Local Netmask:** Use this feature to set the netmask for the local machine.

**Local Gateway:** Use this feature to set the gateway for the local machine.

**Local DNS (Domain Name System) Servers** Use this feature to set the DNS server for the local machine.

### **Save Changes and Exit.**

Select Yes to save the changes that you've made and exit from this submenu.

## ►MAC: 3CECEFFD1807 - IPv6 Network Configuration

### ►Enter Configuration Menu

The following features will display:

- Interface Name
- Interface Type
- MAC Address
- Host Addresses
- Route Table
- Gateway Addresses
- DNS Addresses

### Interface ID

This feature displays the Interface ID used in the network.

### DAD (Duplicate Address Detection) Transmit Count

This feature displays the DAD Transmit Count. The default setting is **1**.

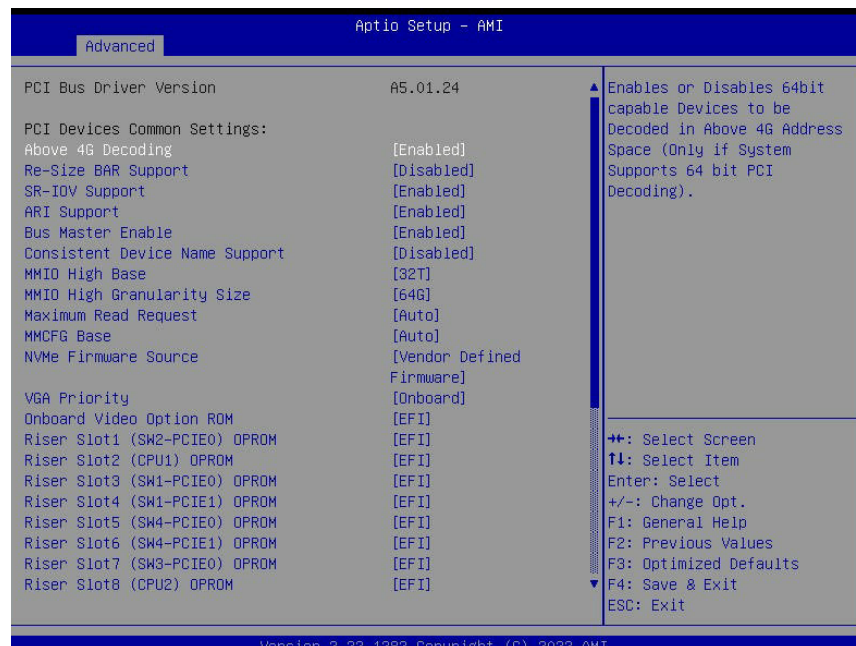
### Policy

Use this feature to select how the policy is to be configured. The options are **Automatic** and **Manual**.

### Save Changes and Exit.

Select Yes to save the changes that you've made and exit from this submenu.

## ►PCIe/PCI/PnP Configuration



The following PCI information will be displayed:

- PCI Bus Driver Version
- PCI Devices Common Settings

### Above 4G Decoding (Available if the system supports 64-bit PCI decoding)

Select Enabled to decode a PCI device that supports 64-bit in the space above 4G Address. The options are Disabled and **Enabled**.

### Re-Size BAR Support

Select Enabled for Re-Size BAR support. The options are **Disabled** and Enabled.

### SR-IOV Support

Select Enabled for Single-Root IO Virtualization support. The options are Disabled and **Enabled**.

### ARI Support

Select Enable for Alternative Routing-ID Interpretation (ARI) support. The options are Disabled and **Enabled**.

### Bus Master Enable

If this setting is set to Enabled, the PCI Bus Driver will enable the Bus Master Attribute for DMA transactions. If this setting is set to Disabled, the PCI Bus Driver will disable the Bus Master Attribute for Pre-Boot DMA protection. The options are Disabled and **Enabled**.

**Consistent Device Name Support**

Select Enable for ACPI (\_DSM) device name support. The options are **Disabled** and Enabled.

**MMIO High Base**

Use this feature to select the base memory size according to memory-address mapping for the IO hub. The options are 56T, 40T, **32T**, 24T, 16T, 4T, 2T, 1T, and 512G.

**MMIO High Granularity Size**

Use this feature to select the high memory size according to memory-address mapping for the IO hub. The options are 1G, 4G, 16G, **64G**, 256G, and 1024G.

**Maximum Read Request**

Select Auto for the system BIOS to automatically set the maximum size for a read request for a PCIe device to enhance system performance. The options are **Auto**, 128 Bytes, 256 Bytes, 512 Bytes, 1024 Bytes, 2048 Bytes, and 4096 Bytes.

**MMCFG Base**

This feature determines how the lowest MMCFG (Memory-Mapped Configuration) base is assigned to onboard PCI devices. The options are 1G, 1.5G, 1.75G, 2G, 2.25G, 3G, and **Auto**.

**NVMe Firmware Source**

This feature determines which type of the NVMe firmware should be used in your system. The options are **Vendor Defined Firmware**, and AMI Native Support.

**VGA Priority**

Use this feature to select the graphics device to be used as the primary video display for system boot. The options are **Onboard** and Offboard.

**Onboard Video Option ROM**

Select UEFI to allow the user to boot the computer using the UEFI (Unified Extensible Firmware Interface) device installed on the onboard video port. The options are Disabled and **UEFI**.

**Riser Slot1 (SW2-PCIE0) OPROM/Riser Slot2 (CPU1) OPROM/Riser Slot3 (SW1-PCIE0) OPROM/Riser Slot4 (SW1-PCIE1) OPROM/Riser Slot5 (SW4-PCIE0) OPROM/Riser Slot6 (SW4-PCIE1) OPROM/Riser Slot7 (SW3-PCIE0) OPROM/Riser Slot8 (CPU2) OPROM**

Select EFI to allow the user to boot the computer using an EFI (Extensible Firmware Interface) device installed on the riser slot specified by the user. The options are Disabled and **EFI**.

### **Onboard SAS Option ROM**

Select EFI to allow the user to boot the computer using an EFI (Extensible Firmware Interface) device installed on the SAS connector specified by the user. The options are Disabled and **EFI**.

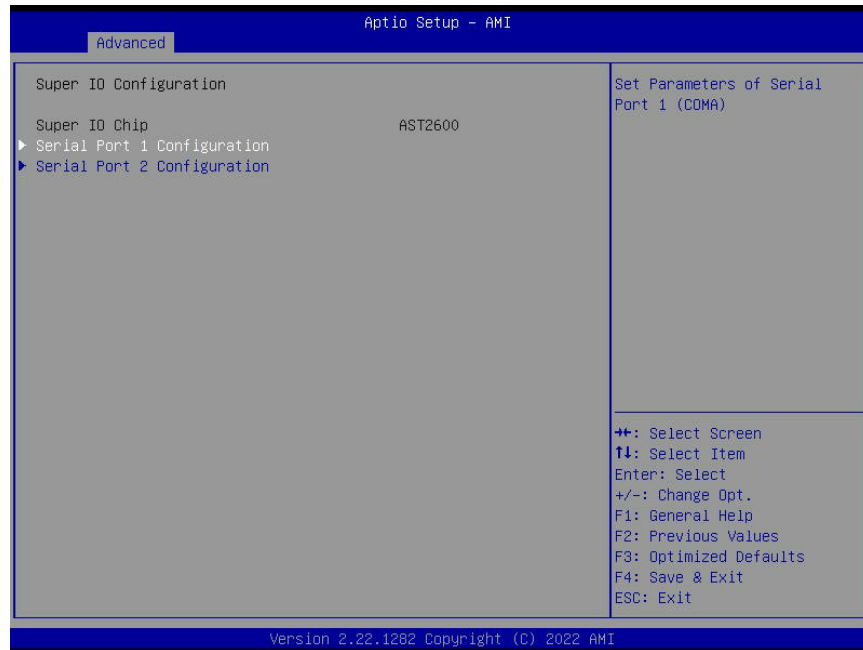
### **Onboard LAN1 Option ROM**

Select PXE to boot up your system using a legacy device installed on LAN 1 port. Select EFI to boot up your system using an EFI (Extensible Firmware Interface) device installed on the LAN port specified by the user. The default setting for LAN 1 port is **EFI**.

### **Onboard NVME 1~10 Option ROM**

Select EFI to allow the user to boot the computer using an EFI (Extensible Firmware Interface) device installed on the NVME connector specified by the user. The options are Disabled and **EFI**.

## ► Super IO Configuration



### Super IO Chip AST2600

## ► Serial Port 1 Configuration

### Serial Port 1

Select Enabled to enable Serial Port 1. The options are Disabled and **Enabled**.

#### Device Settings (Available when "Serial Port 1" is set to Enabled)

This feature displays the base I/O port address and the Interrupt Request address of Serial Port 1.

#### Change Settings (Available when "Serial Port 1" is set to Enabled)

This feature specifies the base I/O port address and the Interrupt Request address of Serial Port 1. Select **Auto** for the BIOS to automatically assign the base I/O and IRQ address to Serial Port 1. The options for Serial Port 1 are **Auto**, (IO=3F8h; IRQ=4), (IO=2F8h; IRQ=4), (IO=3E8h; IRQ=4), and (IO=2E8h; IRQ=4).

## ► Serial Port 2 Configuration

### Serial Port 2

Select Enabled to enable Serial Port 2. The options are Disabled and **Enabled**.

### **Device Settings (Available when "Serial Port 2" is set to Enabled)**

This feature displays the base I/O port address and the Interrupt Request address of Serial Port 2.

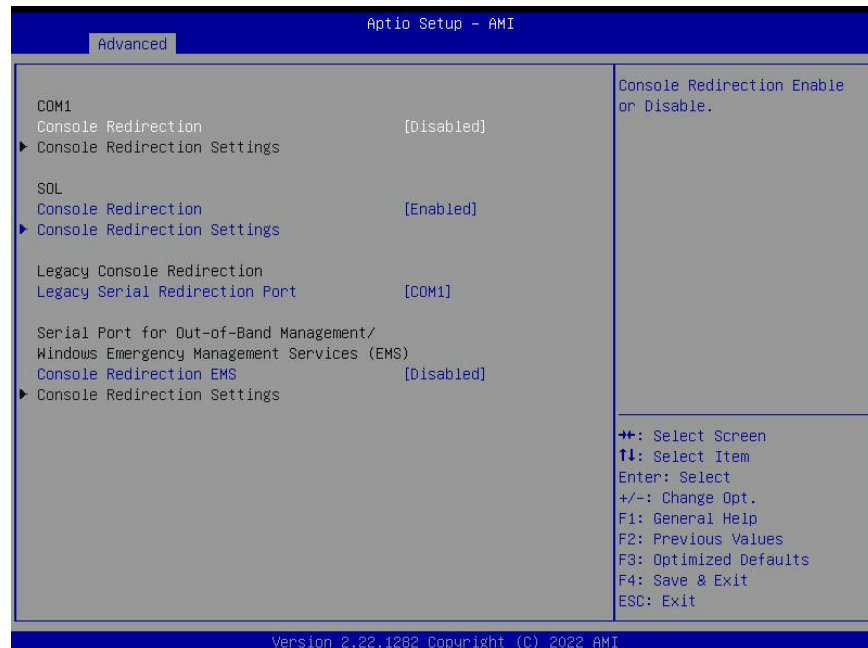
### **Change Settings (Available when "Serial Port 2" is set to Enabled)**

This feature specifies the base I/O port address and the Interrupt Request address of Serial Port 2. Select Auto for the BIOS to automatically assign the base I/O and IRQ address to Serial Port 2. The options for SOL are **Auto**, (IO=3F8h; IRQ=3), (IO=2F8h; IRQ=3), (IO=3E8h; IRQ=3), and (IO=2E8h; IRQ=3).

### **Serial Port 2 Attribute**

Select SOL to use Serial Port 2 as a Serial\_Over\_LAN (SOL) port for console redirection. The options are **SOL** and COM.

## ► Serial Port Console Redirection



### COM 1

#### Console Redirection

Select Enabled to enable COM Port 1 for Console Redirection, which will allow a client machine to be connected to a host machine at a remote site for networking. The options are **Disabled** and Enabled.

\*If the item above is set to Enabled, the following items will become available for configuration:

#### ► Console Redirection Settings (for COM 1)

##### Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, **VT100+**, VT-UTF8, and ANSI.

##### Bits Per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and **115200** (bits per second).

### Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 (Bits) and **8 (Bits)**.

### Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark, and Space.

### Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and 2.

### Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

### VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are Disabled and **Enabled**.

### Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

### Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

### Legacy OS Redirection Resolution

Use this feature to select the number of rows and columns used in Console Redirection for Legacy OS support. The options are 80x24 and **80x25**.

### Putty KeyPad

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SCO, ESCN, and VT400.

### Redirection After BIOS Post

Use this feature to enable or disable Legacy Console Redirection after BIOS POST. When the option - Bootloader is selected, Legacy Console Redirection is disabled before booting the OS. When the option - Always Enable is selected, Legacy Console Redirection remains enabled upon OS bootup. The options are **Always Enable** and Bootloader.

### SOL (Serial-Over-LAN)

#### Console Redirection (for SOL)

Select Enabled to use the SOL port for Console Redirection. The options are Disabled and **Enabled**.

\*If the feature above is set to Enabled, the following items will become available for configuration:

#### ►Console Redirection Settings (for SOL)

Use this feature to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

#### Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are VT100, **VT100+**, VT-UTF8, and ANSI.

#### Bits Per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and **115200** (bits per second).

#### Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 (Bits) and **8 (Bits)**.

#### Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark and Space.

### Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and **2**.

### Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start data-sending when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

### VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are Disabled and **Enabled**.

### Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

### Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

### Legacy OS Redirection Resolution

Use this feature to select the number of rows and columns used in Console Redirection for Legacy OS support. The options are 80x24 and **80x25**.

### Putty KeyPad

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SCO, ESCN, and VT400.

### Redirection After BIOS Post

Use this feature to enable or disable Legacy Console Redirection after BIOS POST. When the option - Bootloader is selected, Legacy Console Redirection is disabled before booting the OS. When the option - Always Enable is selected, Legacy Console Redirection remains enabled upon OS bootup. The options are **Always Enable** and Bootloader.

## Legacy Console Redirection Settings

### Legacy Serial Redirection Port

Use this feature to select the COM port to display redirection of Legacy OS and Legacy OPRM messages. The options are COM1 and **SOL**.

### Serial Port for Out-of-Band Management/Windows Emergency Management Services (EMS)

The feature allows the user to configure Console Redirection settings to support Out-of-Band Serial Port management.

### Console Redirection EMS

Select Enabled to use a COM port specified by the user for EMS Console Redirection. The options are **Disabled** and Enabled.

\*If the feature above is set to Enabled, the following items will become available for user's configuration:

## ► Console Redirection Settings (for EMS)

### Out-of-Band Management Port

This feature selects a serial port in a client server to be used by the Windows Emergency Management Services (EMS) to communicate with a remote host server. The options are **COM1** and SOL (Console Redirection).

### Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII character set. Select VT100+ to add color and function key support. Select ANSI to use the extended ASCII character set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are, VT100 VT100+, **VT-UTF8**, and ANSI.

### Bits Per Second

This feature sets the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in both host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 57600, and **115200** (bits per second).

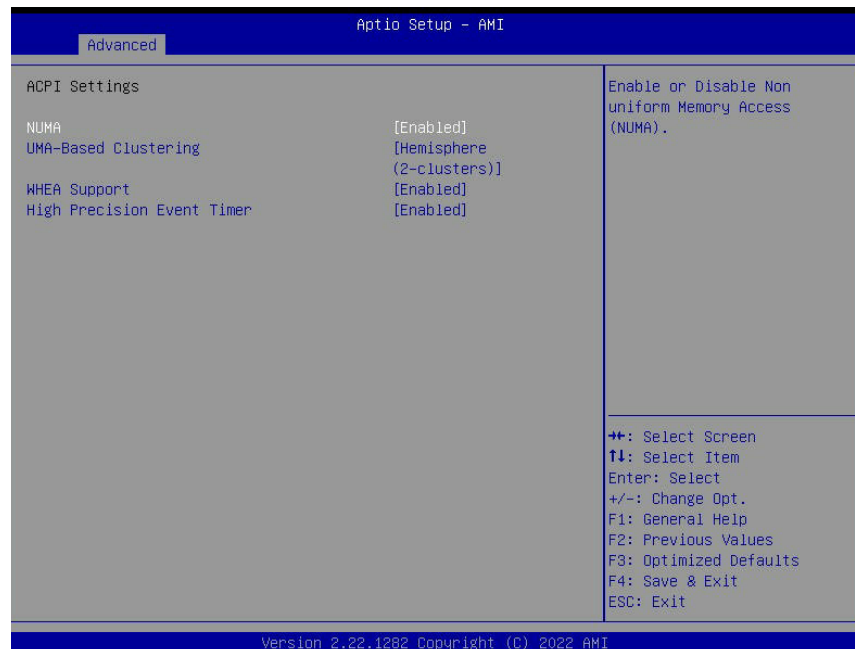
## Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop data-sending when the receiving buffer is full. Send a "Start" signal to start data-sending when the receiving buffer is empty. The options are **None**, Hardware RTS/CTS, and Software Xon/Xoff.

The setting for each of these features is displayed:

- Data Bits
- Parity
- Stop Bits

## ►ACPI Settings



### System ACPI Parameters

Use this feature to configure Advanced Configuration and Power Interface (ACPI) power management settings and parameters for your system.

#### NUMA

Select Enabled to enable Non-Uniform Memory Access support to enhance system performance. The options are Disabled and **Enabled**.

#### UMA (Uniform Memory Access)-Based Clustering (Available when SNC is Disabled)

When the option is set to Hemisphere, UMA-based clustering will support 2-cluster configuration for system performance enhancement. The options are Disabled (All2All) and **Hemisphere (2-clusters)**.

#### WHEA Support

Select Enabled to support the Windows Hardware Error Architecture (WHEA) platform and provide a common infrastructure for the system to handle hardware errors within the Windows OS environment to reduce system crashes and to enhance system recovery and health monitoring. The options are Disabled and **Enabled**.

#### High Precision Event Timer

Select Enable to activate the High Precision Event Timer (HPET) that produces periodic interrupts at a much higher frequency than a Real-time Clock (RTC) does in synchronizing multimedia streams, providing smooth playback and reducing the dependency on other timestamp calculation devices such as an x86 RDTSC Instruction embedded in the CPU. The High Performance Event Timer is used to replace the 8254 Programmable Interval Timer. The options are Disabled and **Enabled**.

## ►Trusted Computing (Available when a TPM device is installed and detected by the BIOS)



When a TPM (Trusted-Platform Module) device is detected in your machine, the following information will display:

- TPM 2.0 Device Found:
- Firmware Version:
- Vendor:

### TPM v1.2 Support

Select Enable to enable TPM (Trusted Platform Module) 2.0 support to enhance system integrity and data security. If there is a TPM jumper installed on the motherboard, please also enable the jumper for this feature to work properly. Please note that the OS will not show the security device when this feature is set to Enabled. Neither TCG EFI protocol nor INT1A interaction will be available for use. If you have made changes on the setting of this feature, be sure to reboot the system for the change to take effect. The options are Disable and **Enable**.

\*If this option is set to Enable, the following screen and items will display:

- Active PCR Banks
- Available PCR Banks

### SHA-1 PCR Bank

Select Enabled to enable SHA-1 PCR Bank support to enhance system integrity and data security. The options are **Enabled** and Disabled.

### SHA256 PCR Bank

Select Enabled to enable SHA256 PCR Bank support to enhance system integrity and data security. The options are **Enabled** and Disabled.

### Pending Operation

Use this feature to schedule a TPM-related operation to be performed by a security (TPM) device at the next system boot to enhance system data integrity. Your system will reboot to carry out a pending TPM operation. The options are **None** and TPM Clear.



**Note:** Your system will reboot to carry out a pending TPM operation.

### Platform Hierarchy (for TPM Version 2.0 and above)

Select Enabled for TPM Platform Hierarchy support which will allow the manufacturer to utilize the cryptographic algorithm to define a constant key or a fixed set of keys to be used for initial system boot. These early boot codes are shipped with the platform and are included in the list of "public keys". During system boot, the platform firmware uses the trusted public keys to verify a digital signature in an attempt to manage and control the security of the platform firmware used in a host system via a TPM device. The options are **Enabled** and Disabled.

### Storage Hierarchy

Select Enabled for TPM Storage Hierarchy support that is intended to be used for non-privacy-sensitive operations by a platform owner such as an IT professional or the end user. Storage Hierarchy has an owner policy and an authorization value, both of which can be set and are held constant (-rarely changed) through reboots. This hierarchy can be cleared or changed independently of the other hierarchies. The options are **Enabled** and Disabled.

### Endorsement Hierarchy

Select Enabled for Endorsement Hierarchy support, which contains separate controls to address the user's privacy concerns because the primary keys in the hierarchy are certified by the TPM key or by a manufacturer with restrictions on how an authentic TPM device that is attached to an authentic platform can be accessed and used. A primary key can be encrypted and certified with a certificate created by using TPM2\_ActivateCredential, which allows the user to independently enable "flag, policy, and authorization values" without involving other hierarchies. A user with privacy concerns can disable the endorsement hierarchy while still using the storage hierarchy for TPM applications, permitting the platform software to use the TPM. The options are **Enabled** and Disabled.

### **PH (Platform Hierarchy) Randomization (for TPM Version 2.0 and above)**

Select Enabled for Platform Hierarchy Randomization support, which is used only during the platform developmental stage. This feature cannot be enabled in the production platforms. The options are **Disabled** and Enabled.

### **TXT Support**

Select Enabled to enable Intel Trusted Execution Technology (TXT) support to enhance system integrity and data security. The options are **Disabled** and Enabled.



**Note 1:** If the option for this feature (TXT Support) is set to Enabled, be sure to disable EV DFX (Device Function On-Hide support when it is present in the BIOS for the system to work properly

**Note 2:** For more information on TPM, please refer to the TPM manual at <http://www.supermicro.com/manuals/other>.

## ► HTTP Boot Configuration

When this submenu is selected, the following items will display:



### HTTP Boot Configuration

#### HTTP Boot Policy

Use this feature to set the HTTP Boot policy. The options are Apply to all LANs, **Apply to Each LAN**, and Boot Priority #1 instantly.

#### HTTP Boot Checks Hostname

Select Enabled to enable the HTTP Boot to check if the hostname of TLS certificates matches the hostname provide by the remote server. The options are **Enabled** and Disabled.

#### Priority of HTTP Boot

##### Instance of Priority 1:

This feature sets the rank target port. The default setting is **1**.

##### Select IPv4 or IPv6

This feature specifies which connection the target LAN port should boot from. Select IPv4 to boot the target LAN from IPv4. The options are **IPv4** and IPv6.

#### Boot Description

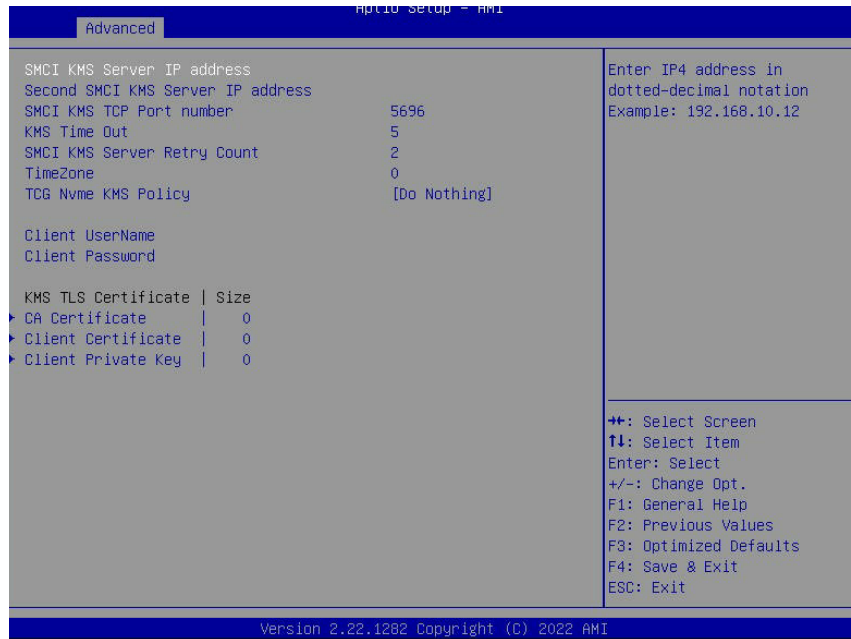
Use this feature to enter a boot description, which cannot be longer than 75 characters. Please be suer to enter a boot description; otherwise, the boot option for the URI cannot be created.

### **Boot URI**

Enter a Boot URI with 128 characters or shorter. This Boot URI determines how IPv4 Boot Option & IPv6 Boot Option will be created. This feature are only supported on Dual or EFI Boot Mode.

## ►SMCI KMS Server Configuration

When this submenu is selected, the following items will display:



### SMCI KMS Server IP address

Use this feature to enter the Supermicro Key Management Service (KMS) server IPv4 address in dotted-decimal notation.

### Second SMCI KMS Server IP address

Use this feature to enter the second Supermicro KMS server IPv4 address in dotted-decimal notation.

### SMCI KMS TCP Port number

Use this feature to enter the Supermicro KMS TCP port number. The valid range is 100 - 9999. The default setting is **5696**.

### KMS Time Out

Use this feature to enter the KMS server connecting timeout (in seconds). The default setting is **5** (seconds).

### SMCI KMS Server Retry Count

Use this feature to specify how many times of the connection retry to the KMS server. The valid range is 0 - 10. Press "+" or "-" on your keyboard to change the value. The default setting is **2** (retrying twice).

### TimeZone

Use this feature to enter the correct time zone. The default setting is **0** (not specified).

### **TCG Nvme KMS Policy**

Use this feature to select TCG NVMe KMS Policy. The options are Normal Unlock, **Do Nothing**, Reset All Devices, and Delete Key Id List.

### **Client Username**

Press <Enter> to set the client identity (UserName). The maximum length is 63 characters.

### **Client Password**

Press <Enter> to set the client identity (Password). The maximum length is 31 characters.

### **CA Certificate**

For the CA certificate, use this feature to enroll factory defaults or load the KMS TLS certificates from the file. The options are **Update**, Delete, and Export.

### **Client Certificate**

For the client certificate, use this feature to enroll factory defaults or load the KMS TLS certificates from the file. The options are **Update**, Delete, and Export.

### **Client Private Key**

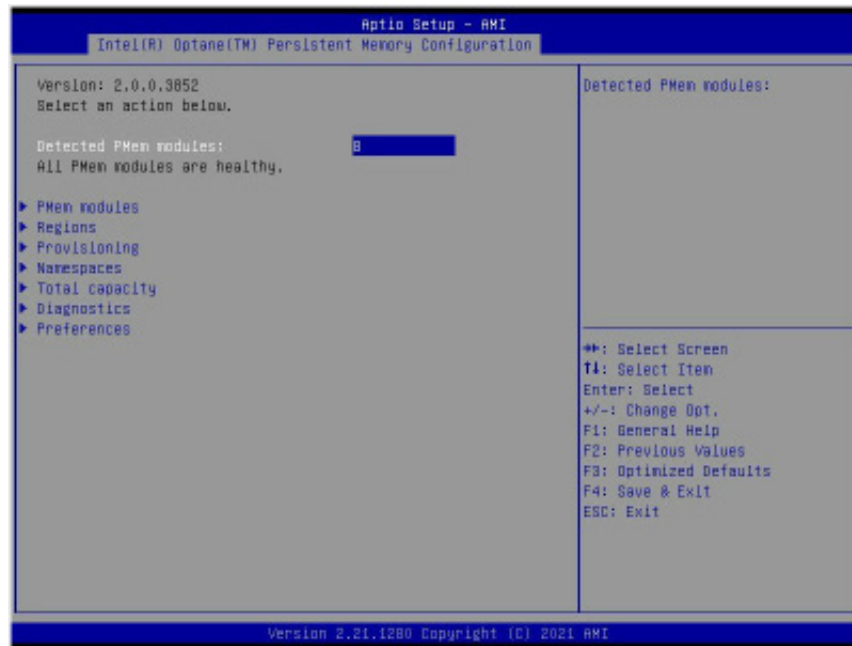
For the client private key, use this feature to enroll factory defaults or load the KMS TLS certificates from the file. The options are **Update**, Delete, and Export.

### **Private Key Password (Available when "Private Key Password" has been set)**

Use this feature to change the private key password.

## ► Intel® Optane™ Persistent Memory Configuration

When you select this submenu and press <Enter>, the following screen will display:



## ► Intel® Optane™ Persistent Memory Configuration

When you select this submenu and press <Enter>, the following screen will display:

- Version: This feature displays the version of PMem used in the system.
- Select an action below

**Detected PMem Modules:** All PMem Modules are healthy.

## ► PMem Modules

This submenu allows the user to view and configure the following settings for the PMem memory modules installed in the system:

Select a specific DIMM that you want to view.



**Note:** The following section, which describes the status of PMem memory, is for illustration only. The number or the status of PMem memory displayed on your BIOS screen will vary, depending on the PMem memory installed on your motherboard.

### DIMMs on Socket 0x0000:

#### ► DIMM IO 0x0010

#### ► DIMM IO 0x0110

#### ► DIMM IO 0x0210

#### ► DIMM IO 0x0310

### DIMMs on Socket 0x0001:

#### ► DIMM IO 0x1010

#### ► DIMM IO 0x1110

#### ► DIMM IO 0x1210

#### ► DIMM IO 0x1310

When you select this feature and press <Enter>, the following information will display:

- DIMM UID: This feature displays the unique ID of the PMem module.
- DIMM Handle: This feature displays the unique handle assigned to the PMem module.
- DIMM Physical ID: This feature displays the physical ID of the PMem module.
- Manageability State: This feature indicates the manageability state of the PMem module.
- Health State: This feature indicates the health state of the PMem module.
- Health State Reason: This feature indicates the reason that effectuates the health state of the PMem module.
- Capacity: This feature indicates the capacity of the PMem module.
- Firmware Version: This feature indicates the firmware version of the PMem module.
- Firmware API Version: This feature indicates the firmware API version of the PMem module.
- Firmware Active API Version: This feature indicates the firmware API version of the PMem module.
- Lock State: This feature indicates the lock state of the PMem module.
- SVN Downgrade: This feature indicates the status of SVN Downgrade of the PMem module.
- Secure Erase Policy: This feature indicates the status of the Secure Erase Policy of the PMem module.
- S3 Resume Opt-in: This feature indicates the status of the S3 Resume Opt-in support for the PMem module.
- Firmware Activate Opt-in: This feature indicates the status of the Firmware Activate Opt-in support for the PMem module.
- Staged Firmware Version: This feature indicates the status of the staged firmware version of the PMem module.
- Staged Firmware Activate: This feature indicates the status of the staged firmware activate support of the PMem module.
- Firmware Update Status: This feature indicates the firmware update status of the PMem module.
- Firmware Activation Quiesce Required: This feature indicates whether Firmware Activation Quiesce is required for the PMem module.

- **Firmware Activation Quiesce Required:** This feature indicates whether Firmware Activation Quiesce is required for the PMem module.
- **Firmware Activation Time:** This feature indicates the time needed to activate the firmware.
- **Manufacturer:** This feature indicates the manufacturer of the PMem module.

### Show More Details

Select Enabled to view more detailed information on the PMem module. The options are **Disabled** and **Enabled**.

*\*If this option is set to Enabled, the following items will display:*

- Serial Number
- Part Number
- Socket
- Memory Controller ID
- Vendor ID
- Device ID
- System Vendor ID
- Subsystem Vendor ID
- Subsystem Device ID
- Device Locator
- Subsystem Revision ID
- Interface Format Code
- Manufacturing Information Valid
- Manufacturing Date
- Manufacturing Location
- Memory Type
- Memory Bank Label
- Data Width Label [b]
- Total Width [b]

- Speed [MHz]
- Channel ID
- Channel Position
- Revision ID
- Form Factor
- Manufacturer ID
- Controller Revision ID
- IS New
- Memory Capacity
- APP Direct Capacity
- Unconfigured Capacity
- Inaccessible Capacity
- Reserved Capacity
- Avg (Average) Power Limit [mW]
- Memory Bandwidth Boost Feature
- Memory Bandwidth Boost Max Power Limit [mW]
- Memory Bandwidth Boost Average Power Time Constant [mS]
- Max Average Power Limit [mW]
- Max Memory Bandwidth Boost Max Power Limit [mW]
- Max Memory Bandwidth Boost Average Power Time Constant [mS]
- Memory Bandwidth Boost Average Power Time Constant Step [mS]
- Max Average Power Reporting Time Constant [mS]
- Average Power Reporting Time Constant Step [mS]
- Package Sparing Capable
- Package Sparing Enabled

- Package Spares Available
- Configuration Status
- SKU Violation
- Population Violation
- ARS Status
- Overwrite PMem Module Status
- Last Shutdown Time
- Average Power Reporting Time Constant [mS]
- Viral Policy Enable
- Viral State
- Thermal Throttle Loss %
- Latched Last Shutdown Status
- Unlatched Shutdown Status
- Security Capabilities
- Modes Supported
- Boot Status
- AIT DRAM Enabled
- Error Injection Enabled
- Max Controller Temperature [C]
- Software Triggers Enabled [0]
- Software Triggers Enabled Details
- Poison Error Injection Counter
- Poison Error Clear Counter
- Media Temperature Injections Counter
- Software Triggers Counter

- Max Media Temperature [C]
- Media Temperature Injection Enabled
- Master Passphrase Enabled
- Average Power
- Average Power 12V
- Average Power 1.2V
- eADR Enable
- Previous Power Cycle eADR Enabled
- Latch System Shutdown State
- Previous Power Cycle Latch System Showdown State

## ►Monitor Health

This submenu displays the following health information on a memory module being monitored.

- Current Alarm Threshold Status Controller Temperature: (within the alarm threshold on all PM modules)
- Media Temperature: (within the alarm threshold on all PM modules).
- Percentage Remaining: (within the alarm threshold on all PM modules).

### **Modify Alarm Thresholds Control Temperature [C]**

This feature indicates the temperature threshold upon which the alarm will be triggered.

#### **Controller Temperature [C]**

This feature indicates the media temperature of the PMem memory.

#### **Media Temperature [C]**

This feature indicates the media temperature of the PMem memory.

#### **Percentage Remaining [%]**

This feature displays the remaining percentage of the PMem memory.

## ►Back to Main Menu

Select this feature and press <Enter> to go back to the **Intel® Optane™ Persistent Memory Configuration** menu.

## ►Update Firmware

Use this feature to select the firmware image to be loaded on the PMem module. After loading the firmware image, please reboot the system and select update for the firmware to take effect. The following items will display:

### **Current Firmware Version**

This feature displays the current firmware version.

### **Selected Firmware Version**

Use this feature to select a new firmware version to use.

### **File**

Use this feature to specify the file path in the root directory that contains the new firmware for firmware update.

### **Staged Firmware Version:**

This feature indicates the staged firmware version of the PMem module specified by the user.

## ►Update

Select this feature to update the firmware settings.

## ►Back to Main Menu

Select this feature and press <Enter> to go back to the **Intel® Optane™ Persistent Memory Configuration** menu.

## ►Configure Security

Use this feature to configure the security settings for all onboard PMem modules.

### **State**

Select Enabled to configure the security settings for the PMem modules installed in the system. The options are **Disabled** and Enabled.

### **Enable Security**

This feature enables the security settings for the onboard PMem modules.

### **Secure Erase with User Password**

Use this feature to erase all the persistent data saved in the PMem modules by entering user password.

### **Freeze Lock**

Use this feature to enable the security lock for the onboard PMem modules.

## ►Back to Main Menu

Select this feature and press <Enter> to go back to the **Intel® Optane™ Persistent Memory Configuration** menu.

## ► **Configure Data Policy**

Use this feature to configure the data policy settings for all onboard PMem modules.

### **Average Power Reporting Time Constant [mS]**

This feature specifies the constant average power reporting time.

### **Modify Average Power Reporting Time Constant**

Use this feature to modify the constant average power reporting time.

## ► **Back to Main Menu**

Select this feature and press <Enter> to go back to the **Intel® Optane™ Persistent Memory Configuration** menu.

## ►Regions

### Current Configuration

## ►Region ID 1

When this submenu is selected, the following items will display:

- Region ID: This feature displays the Region ID of the PMem module.
- DIMM ID: This feature displays the DIMM ID of the PMem module.
- ISet ID: This feature displays the ISet ID of the PMem module.
- Persistent Memory Type: This feature indicates the memory type of the PMem module.
- Capacity: This feature indicates the capacity of the PMem module.
- Free Capacity: This feature indicates the capacity of the PMem module that is available for use.
- Health: This feature indicates the health state of the PMem module.
- Socket ID: This feature displays the Socket ID of the PMem module.

## ►Back to Regions Menu

## ►Back to Provisioning Menu

## ►Back to Main Menu

Select this feature and press <Enter> to go back to the **Intel® Optane™ Persistent Memory Configuration** menu.

- Persistent Memory Type:
- Capacity:
- Free Capacity:

## ►Back to Main Menu

Select this feature and press <Enter> to go back to the **Intel® Optane™ Persistent Memory Configuration** menu.

## ►Provisioning

This submenu configures the memory allocation goal for the onboard PMem memory modules.

## ►Create Goal Configuration

When this submenu is selected, the following items will display:

### Create Goal Configuration for

- Use this feature to select the target to create goal configuration for the PMem modules. The options are Platform and Socket.
- Reserved [%]: This feature reserves a percentage of the PMem capacity for a particular purpose and keep this portion of memory space from being mapped into the physical address of system for system use.
- Memory Mode [%]: This feature reserves a percentage of the PMem capacity for special use in a specific Memory Mode. (This value can be automatically set by the system.)

### Persistent Memory Type

This feature specifies the type of PMem memory capacity to be created. The options are **App Direct** and App Direct Not Interleave.

### Namespace Label Version

Use this feature to view and modify the namespace label version to initialize when creating goals. The options are **1.2** and 1.1.

## ►Create Goal Configuration

## ►Delete Goal Configuration

## ►Back to Previous Menu

Select this feature and press <Enter> to go back to the previous menu.

## ►Back to Main Menu

Select this feature and press <Enter> to go back to the **Intel® Optane™ Persistent Memory Configuration** menu.

## ►Namespaces

This subsection allows the user to select a namespace to view the following information on the selected namespace

### Namespace ID/Name/Health Status

#### ►0x00000201

Select this feature and press <Enter>, the following items will display:

- UUID
- ID
- Name
- Region
- Health
- Mode
- Block Size
- Units: Use this feature to change the namespace capacity (in the unit of B, MB, MiB, GB, **GiB**, TB, and TiB.)
- Capacity
- Label Version

►**Delete** After configuring the settings for the namespace above, click on <delete> to delete the changes you've made on the namespace. Please note that all data contained in the namespace will be deleted as well when you press <delete>.

## ►Back to Namespaces

## ►Back to Main Menu

Select this feature and press <Enter> to go back to the **Intel® Optane™ Persistent Memory Configuration** menu.

## ► Create Namespace

Use this submenu to create a namespace. The following information will display:

### **Name**

### **Region ID**

This feature displays the Region ID of the PMem module. The options are **0x0001** and 0x0001.

### **Mode**

Use this feature to set the Namespace mode. The options are **None** and Sector.

### **Capacity Input**

Select Remaining to use the maximum memory capacity currently available as system memory capacity. Select Manual to enter the system memory capacity manually. The options are **Remaining** and Manual.

### **Units**

Use this feature to select the type of unit to use when inputting namespace capacity in the system.

The options are B, MB, MiB, GB, **GiB**, TB, and TiB.

- **Capacity:** This feature displays the namespace capacity.

## ► Create Namespace

## ► Back to Namespaces

## ► Back to Main Menu

Select this feature and press <Enter> to go back to the **Intel® Optane™ Persistent Memory Configuration** menu.

## ►Total Capacity

This feature allows the user to set the total PMem resource capacity allocated across all segments in the host server.

### **PMem Module Capacities**

This section displays the following information:

- Volatile: This feature specifies Volatile information of the PMem module.
- AppDirect: This feature specifies the App. direct capacity of the PMem module.
- Inaccessible: This feature specifies the capacity of the PMem module that is not accessible to the user.
- Raw: This feature specifies the raw capacity of the PMem module.

### **DDR Capacities**

- Volatile: This feature specifies Volatile information of the PMem module.
- Cache: This feature specifies the capacity of the cache memory.
- Inaccessible: This feature specifies the capacity of the PMem module that is not accessible to the user.
- Raw: This feature specifies the raw capacity of the PMem module.

### **Total Memory Capacities**

- Volatile: This feature specifies Volatile information of the PMem module.
- AppDirect: This feature specifies the App. direct capacity of the PMem module.
- Cache: This feature specifies the capacity of the cache memory.
- Inaccessible: This feature specifies the capacity of the PMem module that is not accessible to the user.
- Raw: This feature specifies the raw capacity of the PMem module.

## ►Back to Main Menu

Select this feature and press <Enter> to go back to the **Intel® Optane™ Persistent Memory Configuration** menu.

## ►Diagnostics

### Perform Diagnostic Tests on DIMMs

When you select this submenu and press <enter>, the following items will display:

#### Choose Diagnostics Type:

Use this feature to choose the type of diagnostics test to be performed on the PMem module installed in the system

#### Quick Diagnostics

Select Enabled for the quick diagnostics test to be performed on the PMem module installed in the system when needed. The options are **Enabled** and Disabled.

#### DIMM ID

Select Enabled to display the DIMM ID of a PMem module upon which the diagnostic test will be performed. The options are **Enabled** and Disabled. Please note that more DIMM IDs will appear If more PMem modules are installed on the motherboard.

#### Config (Configure) Diagnostics

Select Enabled for the platform configuration diagnostics test to be performed on the PMem module. The options are **Enabled** and Disabled.

#### FW (Firmware) Diagnostics

Select Enabled for the firmware diagnostics test to be performed on the PMem module. The options are **Enabled** and Disabled.

#### Security Diagnostics

Select Enabled for the security diagnostics test to be performed on the PMem module. The options are **Enabled** and Disabled.

## ►Execute Tests

Select this feature and press <Enter> to execute the selected diagnostic tests. The following items will be displayed:

### ►Back to Diagnostics

The status of Diagnostics test will be displayed on this page:

- Quick
- Configuration
- Security
- Firmware

## ► Preferences

### View and/or modify user preferences

#### Default DIMM ID

This feature allows the user to view and to modify the default DIMM ID as displayed on the screen. The options are **Handle** and **UID**.

#### Capacity Units

This feature allows the user to view and to set the default capacity unit of the selected PMem to be displayed on the screen. The options are **Auto**, **Auto\_10**, **B**, **MB**, **MiB**, **GB**, **GiB**, **TB**, and **TiB**.

#### App Direct Settings

This feature displays the Application Direct Settings. The default setting is **4KB\_4KB (Recommended)**.

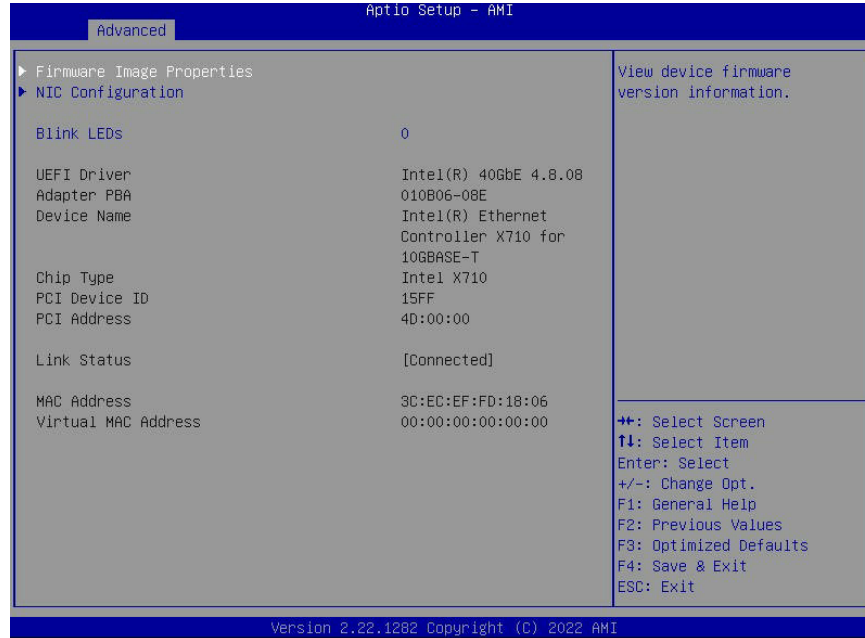
## ► Back to Main Menu

Use this feature to go back to the **Intel® Optane™ Persistent Memory Configuration** menu.

## ► Intel® Ethernet Controller X710 for 10GBASE-T - 3C:EC:EF:FD:18:06



**Note:** The display "MAC: 3C:EC:EF:FD:18:06" is for illustration only. It is unique per system.



## Firmware Image Properties

The following information will be displayed:

- Option ROM Version
- Unique NVM/EEPROM ID
- NVM Version

## ► NIC Configuration

### Link Speed

This feature displays the connection speed of a LAN port specified by the user.

### Wake On LAN

If this feature is set to Enabled, the LAN port specified by the user will be enabled when the system is powered on. The options are Disabled and **Enabled**.

### LLDP Agent

Use this feature to set the persistence of firmware's LLDP Agent. The options are Disabled and **Enabled**.

### **Blink LEDs**

Use this feature to set the duration of blinking for the LAN LED indicators, ranged from 0 to 15 seconds.

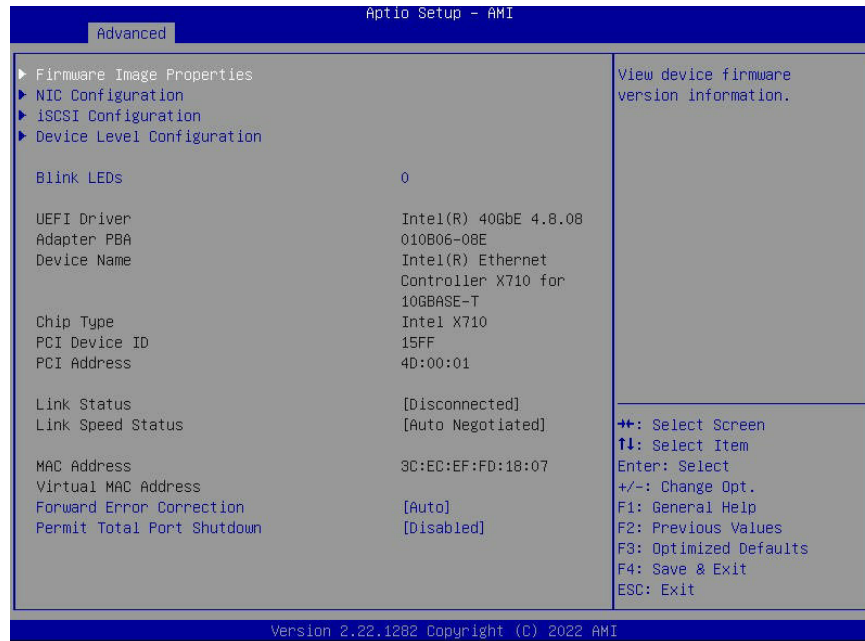
The following information will be displayed as well:

- UEFI Driver
- Adapter PBA
- Device Name
- Chip Type
- PCI Device ID
- PCI Address
- Link Status
- MAC Address
- Virtual MAC Address

## ► Intel® Ethernet Controller X710 for 10GBASE-T - 3C:EC:EF:FD:18:07



**Note:** The display "MAC: 3C:EC:EF:FD:18:07" is for illustration only. It is unique per system.



## Firmware Image Properties

The following information will be displayed:

- NVM Version
- Family Firmware Version
- EFI Version
- Minimum Security Revision

## ► NIC Configuration

### Link Speed

This feature displays the connection speed of a LAN port specified by the user.

### Wake On LAN

If this feature is set to Enabled, the LAN port specified by the user will be enabled when the system is powered on. The options are Disabled and **Enabled**.

### **Legacy Virtual LAN ID**

Use this feature to set the VLAN ID used for PXE VLAN mode, ranged from 0 to 4094. PXE VLAN will be disabled if the value is set to 0.

### **LLDP Agent**

Use this feature to set the persistence of firmware's LLDP Agent. The options are Disabled and **Enabled**.

## **►iSCSI Configuration**

### **►iSCSI General Parameters**

#### **TCP/IP Parameters via DHCP**

Use this feature to set the source of the initiator IP address, via either DHCP or static assignment. The options are **Disabled** and Enabled.

#### **iSCSI Parameters via DHCP**

Use this feature to set the source of the initiator IP address, via either DHCP or static assignment. The options are **Disabled** and Enabled.

#### **CHAP Authentication**

Use this feature to enable the ability of the initiator to use CHAP authentication when connecting to the iSCSI target. The options are **Disabled** and Enabled.

#### **CHAP Mutual Authentication**

Use this feature to enable the ability of the initiator to use CHAP mutual authentication when connecting to the iSCSI target. The options are **Disabled** and Enabled.

#### **TCP/IP Parameters via DHCP**

Use this feature to set whether IPv4 or IPv6 network addressing will be used for the iSCSI initiator and the target. Currently only IPv4 is supported.

#### **Commit Changes and Exit**

Select this feature to save the changes you've made and exit from the program.

## ► iSCSI Initiator Parameters

### **IP Address**

Use this feature to set the IP address of the iSCSI initiator.

### **Subnet Mask**

Use this feature to set the IPv4 subnet mask of the iSCSI initiator.

### **Default Gateway**

Use this feature to set the default gateway of the iSCSI initiator.

### **iSCSI Name**

Use this feature to set the iSCSI initiator Qualified Name (IQN).

The following information will be displayed:

- CHAP ID
- CHAP Secret

## ► iSCSI First Target Parameters

### **IP Address**

Use this feature to set the IP address of the iSCSI target.

### **TCP Port**

Use this feature to set the TCP port number of the iSCSI target.

### **Boot LUN**

Use this feature to set the iSCSI storage target boot logical unit number (LUN).

### **iSCSI Name**

Use this feature to set the iSCSI Qualified Name (IQN) of the iSCSI storage target.

The following information will be displayed:

- CHAP Secret

## ► Device Level Configuration

### **Virtualization Mode**

The default setting is **None**.

**NParEp Mode**

Select Enabled for NParEp Mode support. The default setting is **Disabled**.

**Shared Memory Features**

Select Enabled to disable usage of reserved memory regions to allow direct assignment of device to guest virtual machines. The options are **Enabled** and Disabled

**Active Physical Functions**

Use this feature to enable physical functions specified by the user. Disabled functions will not be exposed and their associated ports will be completely shut down. The options are PF0 only, PF0 and PF1, (PF0, PF1 and PF2), (PF0, PF1, PF2 and PF3), (PF0, PF1, PF2, PF3 and PF4), (PF0, PF1, PF2, PF3, PF4 and PF5), (PF0, PF1, PF2, PF3, PF4, PF5 and PF6), and **All**.

**Blink LEDs**

Use this feature to set the duration of blinking for the LAN LED indicators, ranged from 0 to 15 seconds.

The following information will be displayed as well:

- UEFI Driver
- Adapter PBA
- Device Name
- Chip Type
- PCI Device ID
- PCI Address
- Link Status
- MAC Address
- Virtual MAC Address

**Forward Error Correction**

Use this feature to specify the type of forward error correction to use on the link. The options are **Auto**, Fire Code (Clause 74), RS-FEC (Clause 91, 108, 134), and Disabled.

**Permit total Port Shutdown**

Select Enabled to allow the port to be completely disabled when a Port Down command is received from the Host OS or driver. The options are Enabled and **Disabled**.

## ► TLS Authenticate Configuration



When this submenu is selected, the following items will display:

## ► Server CA Configuration

This feature allows the user to configure the client certificate that is to be used by the server.

### ► Enroll Certification

This feature allows the user to enroll the certificate in the system.

#### ► Enroll Cert (Certification) Using File

This feature allows the user to enroll the security certificate in the system by using a file.

#### Certification GUID (Global Unique Identifier)

This feature displays the GUID for this system.

#### ► Commit Changes and Exit

Select this feature to save the changes you have made and exit from the system.

► **Discard Changes and Exit**

Select this feature to discard the changes you have made and exit from the system.

► **Delete Certification**

This feature is used to delete the certificate if a certificate has been enrolled in the system.

► **Client Certification Configuration**

This feature allows the user to configure the client certificate to be used by the server.

► **Enroll Certification**

This feature allows the user to enroll the certificate in the system.

► **Enroll Cert (Certification) Using File**

This feature allows the user to enroll the security certificate in the system by using a file.

**Cert (Certification) GUID (Global Unique Identifier)**

This feature displays the GUID for this system.

► **Commit Changes and Exit**

Select this feature to save the changes you have made and exit from the system.

► **Discard Changes and Exit**

Select this feature to discard the changes you have made and exit from the system.

► **Delete Certification**

If this feature is set to Enable, the certificate enrolled in the system will be deleted.  
The options are Enable and **Disable**.

► **All CPU Information**

This submenu displays the detailed information of each CPU as detected by the BIOS as shown below.

## ►RAM Disk Configuration

This feature allows the user to configure the settings for the RAM disks installed in the system. When you select this submenu and press <Enter>, the following items will display:

### Disk Memory Type

This feature specifies the type of memory that is available for you to create a RAM disk. The options are **Boot Service Data** and **Reserved**.

## ►Create Raw

This feature allows the user to create a raw RAM disk from all available memory modules in the system. When you select this submenu and press <Enter>, the following items will display:

- **Size (Hex):** Use this feature to set the size of the raw RAM disk. The default setting is 1.
- **Create & Exit:** Select this feature when you want to exit from this submenu after you've created a raw RAM disk.
- **Discard & Exit:** Select this feature when you want to abandon the changes you've made and to exit from the submenu.

## ►Create from File

This feature allows the user to create a RAM disk from a file specified by the user. Select this submenu and press <Enter>, the following items will display:

- **Create RAM Disk List:** Use this feature to create a RAM disk list.
- **Remove Selected RAM Disk(s):** Use this feature to delete the RAM disk(s) specified by the user.

### Attempt Priority

Use this feature to change the priority of iSCSI attempt using the + or - keys. The options are **Host Attempt**, **Redfish Attempt**, and **Rst Attempt**.

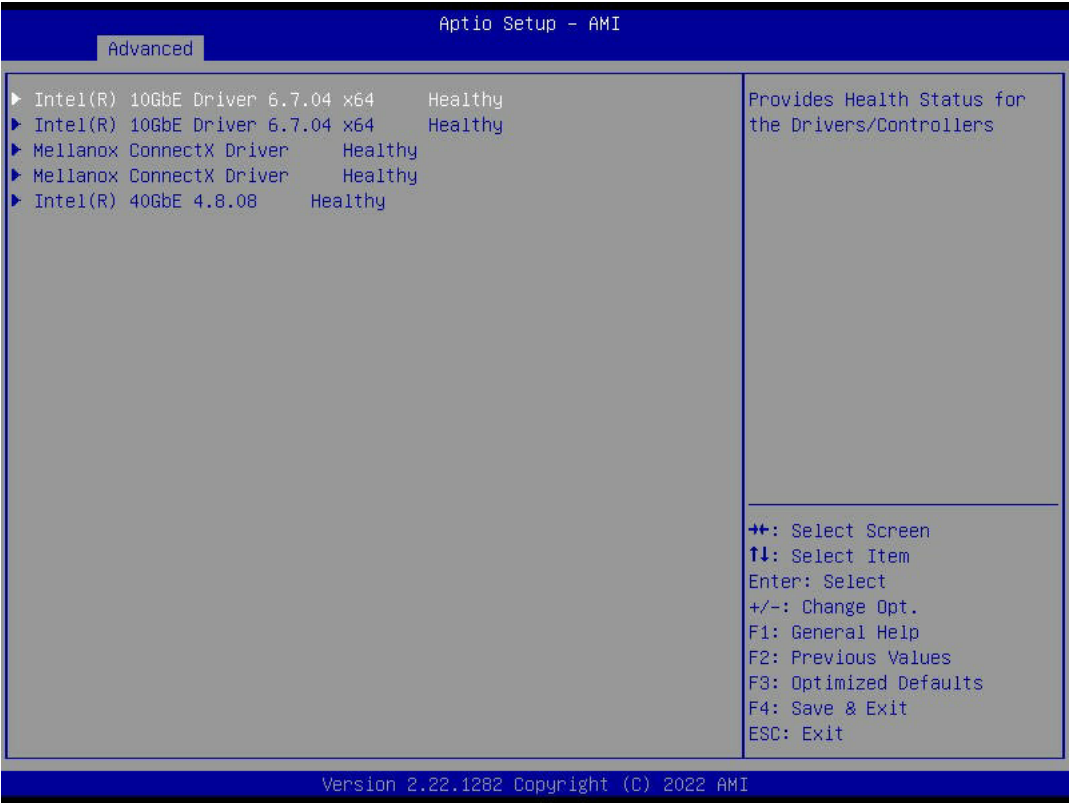
### Commit Changes and Exit

Select this feature to save the changes you've made and exit from the program.

►Driver Health

This feature displays the health information of the drivers installed in your system, including LAN controllers, as detected by the BIOS. Select one and press <Enter> to see the details.

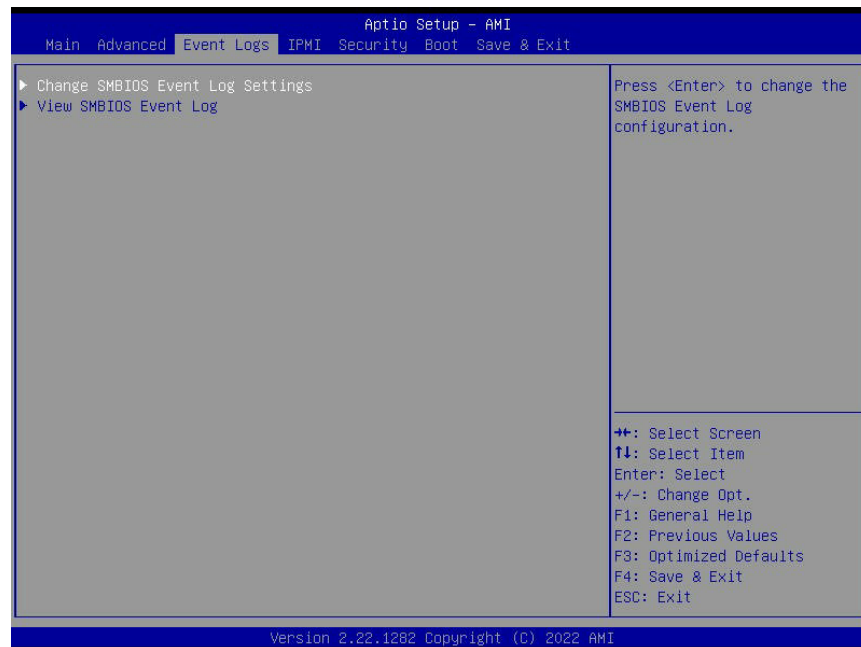
 **Note:** This section is provided for reference only, for the driver health status will differ depending on the drivers installed in your system. It's also based on your system configuration and the environment that your system is operating in.



## 4.4 Event Logs

Use this feature to configure Event Log settings.

 **Note:** After you've made any changes on a setting below, please reboot the system for the changes you've made to take effect.



### ► Change SMBIOS Event Log Settings

#### Enabling/Disabling Options

##### SMBIOS Event Log

Select Enabled to enable SMBIOS (System Management BIOS) Event Logging during system boot. The options are Disabled and **Enabled**.

#### Erasing Settings (Available when SMBIOS Event Log is set to Enabled)

##### Erase Event Log (Available when SMBIOS Event Log is set to Enabled)

Select "No" to keep the event log without erasing it upon next system bootup. Select "Yes, Next reset" to erase the event log upon next system reboot. The options are **"No"**, "Yes, Next reset", and "Yes, Every reset".

#### When Log is Full (Available when SMBIOS Event Log is set to Enabled)

Select Erase Immediately to immediately erase all errors in the SMBIOS event log when the event log is full. Select Do Nothing for the system to do nothing when the SMBIOS event log is full. The options are **Do Nothing** and Erase Immediately.

## ► SMBIOS Event Log Standard Settings

### Log System Boot Event

Select Enabled to log system boot events. The options are Enabled and **Disabled**.

### MECI (Multiple Event Count Increment)

Enter the increment value for the multiple event counter. Enter a number between 1 to 255. The default setting is **1**.

### METW (Multiple Event Count Time Window)

This feature is used to determine how long (in minutes) should the multiple event counter wait before generating a new event log. Enter a number between 0 to 99. The default setting is **60**.

## ► View System Event Log

This feature allows the user to view the event in the system event log. Select this feature and press <Enter> to view the status of an event in the log. The following categories will be displayed:

**Date/Time/Error Code/Severity**

## 4.5 BMC

This submenu displays the status of the Baseboard Management Controller (BMC), and allows the user to configure the following BMC settings.



- **BMC Firmware Revision:** This feature indicates the BMC firmware used in your system.
- **BMC Status:** This feature indicates the status of BMC (Baseboard Management Controller) used in your system.

### ► System Event Log

#### Enabling/Disabling Options

##### SEL Components

Select Enabled to enable all system event logging upon system boot. The options are Disabled and **Enabled**.

#### Erasing Settings

##### Erase SEL

Select "Yes, On next reset" to erase all system event logs upon next system boot. Select "Yes, On every reset" to erase all system event logs upon each system reboot. Select "No" to keep all system event logs after each system reboot. The options are **"No"**, "Yes, On next reset", and "Yes, On every reset".

##### When SEL is Full

This feature allows the user to determine what the BIOS should do when the system event log is full. Select Erase Immediately to erase all events in the log when the system event log is full. The options are **Do Nothing** and Erase Immediately.

## ►BMC Network Configuration

### Update BMC LAN Configuration

Select Yes for the BIOS to implement all IP/MAC address changes upon next system boot. The options are **No** and **Yes**.

\*\*\*\*\*

### Configure IPv4 Support

\*\*\*\*\*

- BMC LAN Selection: This feature allows the user to select the type of the BMC LAN. The manufacturer default setting is **Failover**.
- BMC Network Link Status: This feature displays the status of the BMC network link for this system. The manufacturer default setting is **Dedicated LAN**.
- Configuration Address Source

Use this feature to select the source of the IPv4 Connection. If Static is selected, you will need to know the IP address of IPv4 connection and enter it to the system manually in the field. If DHCP is selected, the BIOS will search for a DHCP (Dynamic Host Configuration Protocol) server in the network that is attached to and request the next available IP address for the IPv6 connection. The options are **DHCP** and **Static**.

- Station IP Address: This feature displays the Station IP address in decimal and in dotted quad form (i.e., 192.168.10.253).
- Subnet Mask: This feature displays the sub-network that this computer belongs to. The value of each three-digit number separated by dots should not exceed 255.
- Station MAC Address: This feature displays the Station MAC address for this computer.
- Gateway IP Address: This feature displays the Gateway IP address for this computer. This should be in decimal and in dotted quad form (i.e., 172.29.0.1).
- VLAN: This feature displays the status of VLAN support. The manufacturer default setting is **Disabled**.

\*\*\*\*\*

## Configure IPv6 Support

\*\*\*\*\*

- IPv6 Address Status: This feature displays the status of IPv6 addresses.
- IPv6 Support: Select Enabled for IPv6 support. The options are **Enabled** and Disabled.
- Configuration Address Source

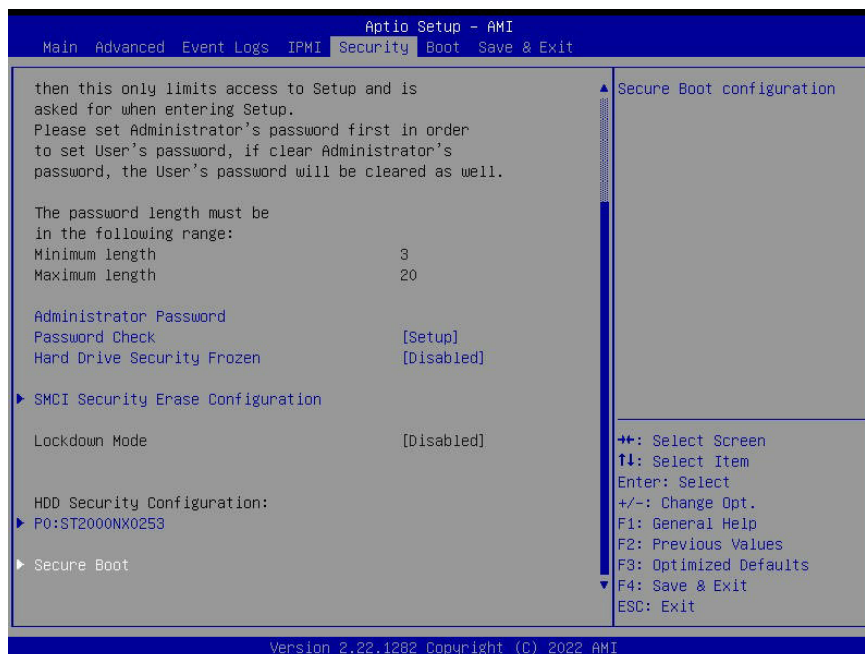
Use this feature to select the source of the IPv4 Connection. If Static is selected, you will need to know the IP address of IPv4 connection and enter it to the system manually in the field. If DHCP is selected, the BIOS will search for a DHCP (Dynamic Host Configuration Protocol) server in the network that is attached to and request the next available IP address for the IPv6 connection. The options are **DHCPv6 Stateless** and Static.

***\*If the item above is set to Static, the following items will become available for configuration:***

- Station IPV6 Address
- Prefix Length
- Gateway IP
- Advanced Settings
- Preferred DNS server IP
- Alternative DNS server IP

## 4.6 Security

This menu allows the user to configure the following security settings for the system.



### Administrator Password

This feature indicates if an administrator password has been installed. It also allows the user to set the administrator password which is required to enter the BIOS setup utility. The length of the password should be from 3 characters to 20 characters long.

### User Password (Available when an Administrator Password is entered)

This feature indicates if a user's password has been installed. It also allows the user to set the user's password which is required to enter the BIOS setup utility. This feature provides the description of the user's password. The length of the password should be from 3 characters to 20 characters long.

### Password Check

Select Setup for the system to check for a password at Setup. Select Always for the system to check for a password upon system boot and upon entering the BIOS Setup utility. The options are **Setup** and **Always**.

### Hard Drive Security Frozen

Select Enabled to freeze and lock hard drive security settings to protect important data stored in the hard drives from being altered. The options are Enabled and **Disabled**.

 **Note:** For detailed instructions on how to configure Secure Boot settings, please refer to the Secure Boot Configuration User's Guide posted on the web page under the link: <http://www.supernmicro.com/support/manuals/>.

## ►SMCI Security Erase Configuration

This section allows the user to configure the SMCI-proprietary Security Erase settings. When this section is selected, the following features will display:

- **HDD Name:** This feature displays the name of the HDD/SATA drive that is connected to the SMCI Security Erase Configuration submenu.
- **HDD Serial Number:** This feature displays the serial number of the HDD/SATA device that is connected to the SMCI Security Erase Configuration submenu.
- **Security Erase Mode:** This feature displays the security erase mode set for this system.
- **Estimated Time:** This feature displays the estimate time needed to perform the selected Security Erase features.
- **HDD User (Pwd) Password Status:** This feature indicates if a password has been set as a SATA user password which will allow the user to configure SMCI Security Erase settings on the HDD (SATA) device by using this HDD user password.

### Security Function

Select Password to set an HDD/SATA password which will allow the user to configure the security settings of the HDD/SATA device. Select Security Erase - Password to enter a SATA user password to allow the user to erase the password and the contents previously stored in the HDD/SATA device. Select Security Erase - Without Password to use the manufacturer default password "11111111" as the SATA user password and allow the user to erase the contents of the HDD/SATA device by using this default password. The options are **Disable**, Set Password, Security Erase - Password, and Security Erase - Without Password.

### Password

Use this feature to set the SATA user password which will allow the user to configure the SMCI Security Erase settings by using the SATA user password.

### Lockdown Mode

Select Enabled to support Lockdown Mode which will prevent existing data or keys stored in the system from being altered or changed in an effort to preserve system integrity and security. The options are Enabled and **Disabled**.

## ►Secure Boot



**Note:** For detailed instructions on how to configure Secure Boot settings, please refer to the Secure Boot Configuration User's Guide posted on the web page under the link: <http://www.supermicro.com/support/manuals/>.

When you select this submenu and press the <Enter> key, the following items will display:

- Secure Mode

### Secure Boot

Select Enabled to use Secure Boot settings. The options are **Disabled** and Enabled.

### Secure Boot Mode

Use this feature to select the desired secure boot mode for the system. The options are Standard and **Custom**.

### CMS Support

If this feature is set to Enabled, legacy devices will be supported by the system. The options are Disabled and **Enabled**.

## ►Enter Audit Mode

This feature allows the user to set the Audit Mode.

## ►Key Management (Available when "Secure Boot Mode" is set to Custom)

### Vendor Keys

#### ►Provisioning Factory Defaults

Select Enabled to install factory default Secure Boot keys after the platform reset while the system is in the Setup mode. The options are **Disabled** and Enabled.

#### ►Restore Factory Keys

Select Yes to restore manufacturer default keys used to ensure system security. The options are **Yes** and No.

#### ►Reset to Setup Mode

This feature resets the system to Setup Mode.

### ►Export Secure Boot Variables

This feature exports the NVRAM contents of Secure Boot variables to a storage device.

### ►Enroll Efi Image

This feature specifies which EFI (Extensible Firmware Interface) image should be used for the system when it operates in the Secure Boot mode.

### Device Guard Ready

### ►Remove 'UEFI CA' from DB

Select Yes to remove UEFI CA from the database. The options are **Yes** and No.

### ►Restore DB defaults

Select Yes to restore database variables to the manufacturer default settings. The options are **Yes** and No.

### Secure Boot Variable/Size/Keys/Key Source

### ►Platform Key (PK)

Use this feature to enter and configure a set of values to be used as platform firmware keys for the system. These values also indicate the sizes, keys numbers, and the sources of the authorized signatures. Select Update to update the platform key. The options are **Details**, Export, Update, and Delete.

### ►Key Exchange Keys

Use this feature to enter and configure a set of values to be used as Key-Exchange-Keys for the system. These values also indicate the sizes, keys numbers, and the sources of the authorized signatures. Select Update to update your "Key Exchange Keys". Select Append to append your "Key Exchange Keys". The options are **Details**, Export, Update, Append, and Delete.

### ►Authorized Signatures

Use this feature to enter and configure a set of values to be used as Authorized Signatures for the system. These values also indicate the sizes, keys numbers, and the sources of the authorized signatures. Select Update to update your "Authorized Signatures". Select Append to append your "Authorized Signatures". The options are **Details**, Export, Update, Append, and Delete.

### ►Forbidden Signatures

Use this feature to enter and configure a set of values to be used as Forbidden Signatures for the system. These values also indicate sizes, keys numbers, and key sources of the forbidden signatures. Select Update to update your "Forbidden Signatures". Select Append to append your "Forbidden Signatures". The options are **Details**, Export, Update, Append, and Delete.

### ►Authorized TimeStamps

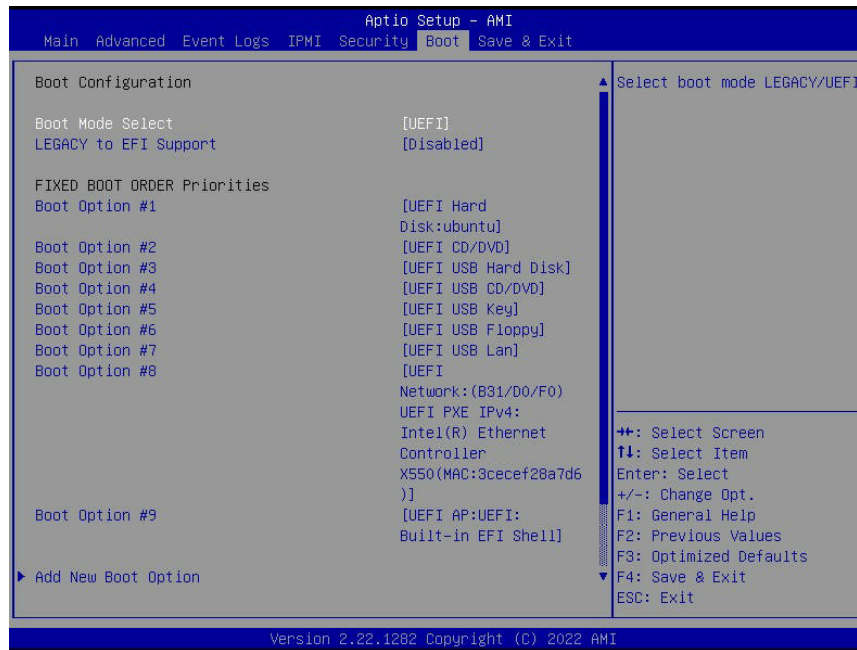
This feature allows the user to set and save the timestamps for the authorized signatures which will indicate the time when these signatures are entered into the system. These values also indicate sizes, keys, and key sources of the authorized timestamps. Select Update to update your "Authorized TimeStamps". Select Append to append your "Authorized TimeStamps". The settings are **Update** and Append.

### ►OsRecovery (OS Recovery) Signatures

This feature allows the user to set and save the authorized signatures used for OS recovery. Select Update to update your "OS Recovery Signatures". These values also indicate sizes, keys, and key sources of the OsRecovery signatures. Select Append to append your "OS Recovery Signatures". The settings are **Update** and Append.

## 4.7 Boot

Use this feature to configure Boot Settings:



### Boot Mode Select

Use this feature to select the type of devices from which the system will boot. The options are LEGACY, **UEFI (Unified Extensible Firmware Interface)**, and Dual.

 **Note:** When the Boot Mode Select feature above is set to Dual, be sure to set all OPRON-related settings to Legacy.

### Fixed Boot Order Priorities

This feature prioritizes the order of a bootable device from which the system will boot. Press <Enter> on each item sequentially to select devices.

When the feature "Boot Mode Select" is set to **Dual** (default), the following items will be displayed for the user to configure the boot settings:

- Boot Option #1 ~ Boot Option #17

When the feature "Boot Mode Select" is set to Legacy, the following items will be displayed for configuration:

- Boot Option #1 ~ Boot Option #8

When the feature "Boot Mode Select" is set to UEFI, the following items will be displayed for configuration:

- Boot Option #1 ~ Boot Option #9

### **LEGACY to EFI Support**

Select Enabled to enable the system to boot to EFI OS after boot failed from legacy boot order. The options are **Disabled** and Enabled.

## **Add New Boot Option**

This feature allows the user to add a new boot option to the boot priority features for system boot.

### **Add Boot Option Label**

This feature allows the user to create the label for the new boot option.

### **Path for Boot Option**

Use this feature to enter the path for the new boot option in the format fsx:\path\filename.efi.

### **Boot Option File Path**

This feature allows the user to specify the file path for the new boot option.

### **Create**

After you've created a new boot option, press <Enter> to save the changes.

## **►Delete Boot Option**

This feature allows the user to select a boot device to delete from the boot priority list.

### **Delete Boot Option**

This feature allows the user to remove an EFI boot option from the boot priority list.

## **►UEFI Hard Disk Drive BBS Priorities**

This feature allows the user to change the UEFI HDD priority boot list.

## **►UEFI Network Drive BBS Priorities**

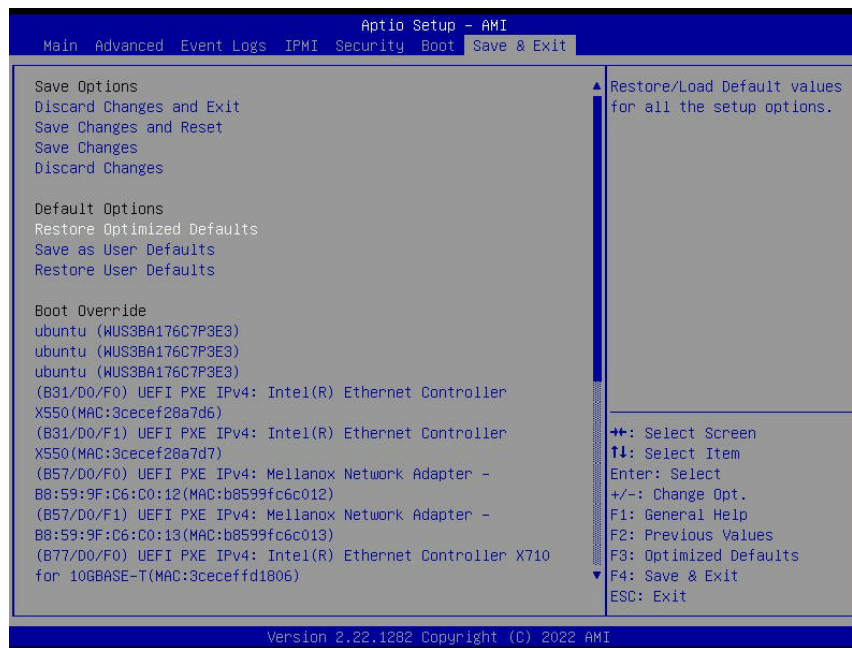
This feature allows the user to change the UEFI network drive priority boot list.

## **►UEFI Application Boot Priorities**

This feature allows the user to change the UEFI application boot drive priority boot list.

## 4.8 Save & Exit

Select the Save & Exit menu from the BIOS setup screen to configure the settings below.



### Save Options

#### Discard Changes and Exit

Select this option to exit from the BIOS setup utility without making any permanent changes to the system configuration and reboot the computer.

#### Save Changes and Reset

When you have completed the system configuration changes, select this option to leave the BIOS setup utility and reboot the computer for the new system configuration parameters to become effective.

#### Save Changes

When you have completed the system configuration changes, select this option to save all changes you've made. This will not reset (reboot) the system.

#### Discard Changes

Select this option and press <Enter> to discard all the changes you've made and return to the AMI BIOS setup utility.

## Default Options

### Restore Optimized Defaults

To set this feature, select Restore Optimized Defaults from the Exit menu and press <Enter> to load manufacturer optimized default settings which are intended for maximum system performance but not for maximum stability.

### Save As User Defaults

To set this feature, select this feature and press <Enter> to save all changes on the default values entered by the user to the BIOS setup utility for future use.

### Restore the User Default

To set this feature, select Restore the User Default Values from the Exit menu and press <Enter>. Use this feature to retrieve user-defined default settings that have been saved previously.

### Boot Override

This feature allows the user to override the Boot priorities sequence in the Boot menu, and immediately boot the system with a device specified by the user instead of the one specified in the boot list. This is a one-time override.

# Appendix A

## BIOS POST Codes

### A.1 BIOS POST Codes

The AMI BIOS supplies additional checkpoint codes, which are documented online at <http://www.supernmicro.com/support/manuals/> ("AMI BIOS POST Codes User's Guide").

For information on AMI updates, please refer to <http://www.ami.com/products/>.

## Appendix B

### Software

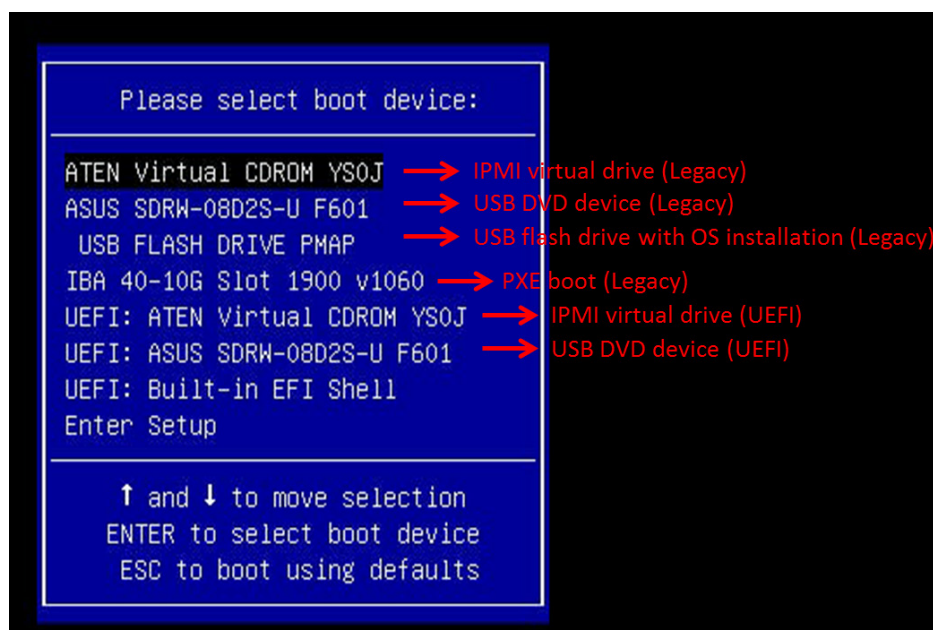
After the hardware has been installed, you can install the Operating System (OS), configure RAID settings, and install the drivers.

#### B.1 Microsoft Windows OS Installation

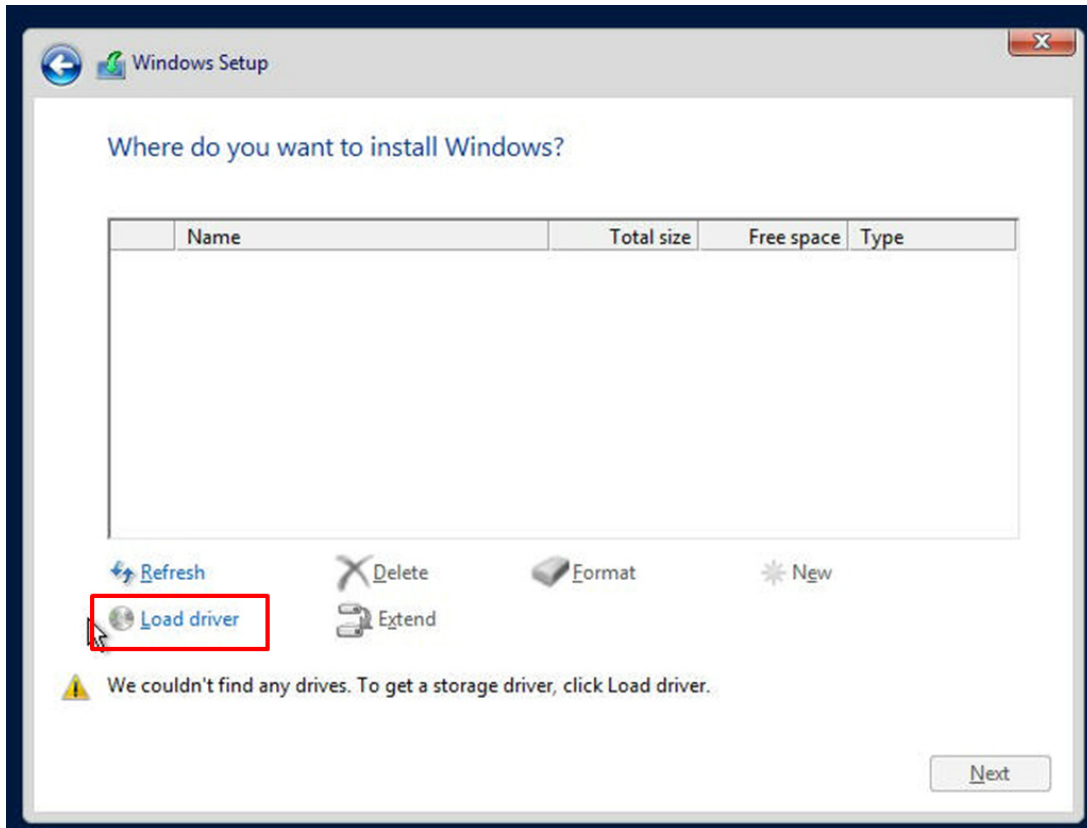
If you will be using RAID, you must configure RAID settings before installing the Windows OS and the RAID driver. Refer to the RAID Configuration User Guides posted on our website at [www.supermicro.com/support/manuals](http://www.supermicro.com/support/manuals).

##### Installing the OS

1. Create a method to access the MS Windows installation ISO file. That can be a USB flash drive or media drive.
2. Retrieve the proper RST/RSTe driver. Go to the Supermicro web page for your motherboard and click on "Download the Latest Drivers and Utilities", select the proper driver, and copy it to a USB flash drive.
3. Boot from a bootable device with Windows OS installation. You can see a bootable device list by pressing **F11** during the system startup.



4. During Windows Setup, continue to the dialog box where you select the drives on to install Windows. If the disk you want to use is not listed, click on “Load driver” link at the bottom left corner.



To load the driver, browse the USB flash drive for the proper driver files.

- For RAID, choose the SATA/sSATA RAID driver indicated then choose the storage drive on which you want to install it.
  - For non-RAID, choose the SATA/sSATA AHCI driver indicated then choose the storage drive on which you want to install it.
5. Once all devices are specified, continue with the installation.
  6. After the Windows OS installation has completed, the system will automatically reboot multiple times.

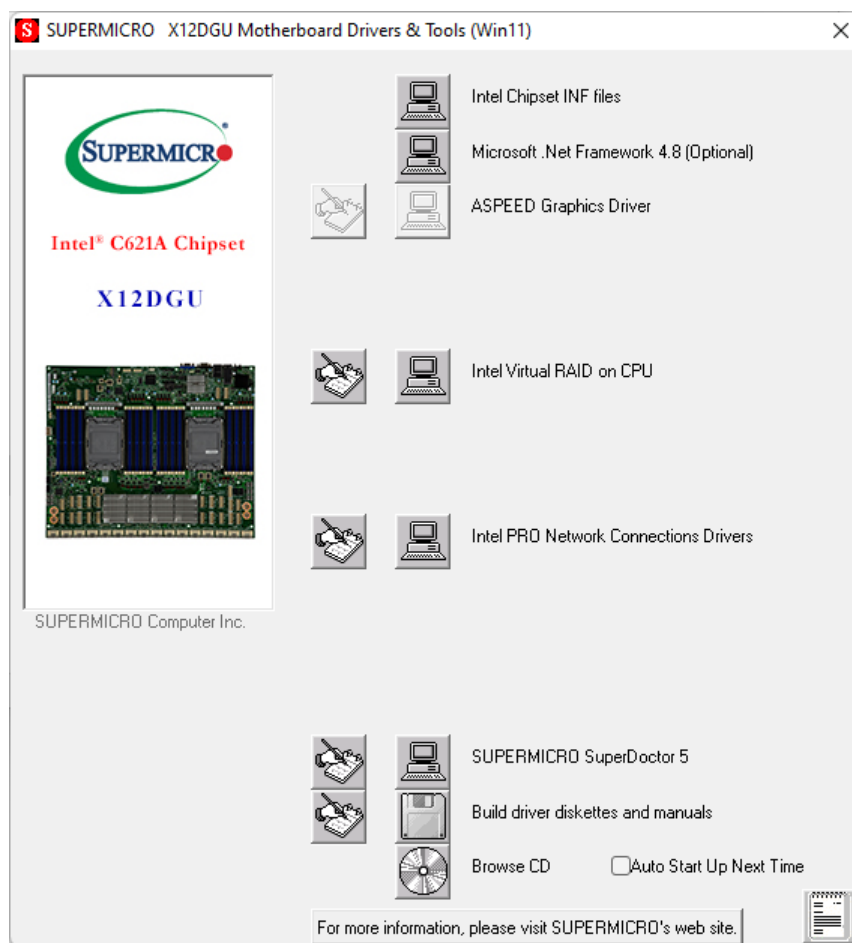
## B.2 Driver Installation

The Supermicro website contains drivers and utilities for your system at <https://www.supermicro.com/wdl/driver>. Some of these must be installed, such as the chipset driver.

After accessing the website, go into the CDR\_Images (in the parent directory of the above link) and locate the ISO file for your motherboard. Download this file to a USB flash drive or media drive. (You may also use a utility to extract the ISO file if preferred.)

Another option is to go to the Supermicro website at <http://www.supermicro.com/products/>. Find the product page for your motherboard, and "Download the Latest Drivers and Utilities".

Insert the flash drive or disk and the screenshot shown below should appear.

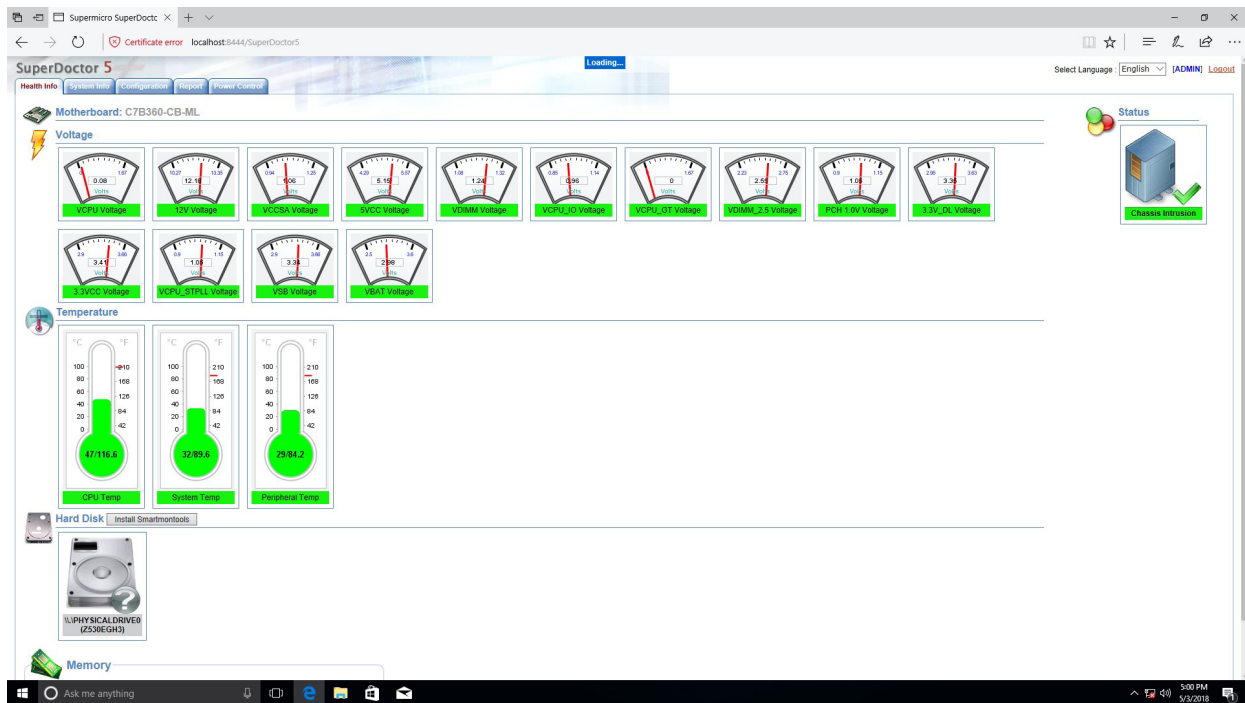


**Note:** Click the icons showing a hand writing on paper to view the readme files for each item. Click the computer icons to the right of these items to install each item (from top to the bottom) one at a time. **After installing each item, you must re-boot the system before moving on to the next item on the list.** The bottom icon with a CD on it allows you to view the entire contents.

## B.3 SuperDoctor® 5

The Supermicro SuperDoctor 5 is a program that functions in a command-line or web-based interface for Windows and Linux operating systems. The program monitors such system health information as CPU temperature, system voltages, system power consumption, fan speed, and provides alerts via email or Simple Network Management Protocol (SNMP).

SuperDoctor 5 comes in local and remote management versions and can be used with Nagios to maximize your system monitoring needs. With SuperDoctor 5 Management Server (SSM Server), you can remotely control power on/off and reset chassis intrusion for multiple systems with SuperDoctor 5 or BMC. SuperDoctor 5 Management Server monitors HTTP, FTP, and SMTP services to optimize the efficiency of your operation.



## B.4 BMC

The X12DGU supports the Baseboard Management Controller (BMC). BMC is used to provide remote access, monitoring and management. There are several BIOS settings that are related to BMC.

For general documentation and information on BMC, please visit our website at: <http://www.supermicro.com/products/nfo/BMC.cfm>.

## B.5 Logging into the BMC (Baseboard Management Controller)

Supermicro ships standard products with a unique password for the BMC ADMIN user. This password can be found on a label on the motherboard.

When logging in to the BMC for the first time, please use the unique password provided by Supermicro to log in. You can change the unique password to a user name and password of your choice for subsequent logins.

*For more information regarding BMC passwords, please visit our website at <http://www.supermicro.com/bmcpassword>.*

## Appendix C

### Standardized Warning Statements

The following statements are industry standard warnings, provided to warn the user of situations where a potential bodily injury may occur. Should you have questions or experience difficulty, contact Supermicro's Technical Support department for assistance. Only certified technicians should attempt to install or configure components.

Read this section in its entirety before installing or configuring components.

These warnings may also be found on our website at [http://www.supermicro.com/about/policies/safety\\_information.cfm](http://www.supermicro.com/about/policies/safety_information.cfm).

#### Battery Handling



**Warning!** There is the danger of explosion if the battery is replaced incorrectly. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions

#### 電池の取り扱い

電池交換が正しく行われなかった場合、破裂の危険性があります。交換する電池はメーカーが推奨する型、または同等のものを使用下さい。使用済電池は製造元の指示に従って処分して下さい。

#### 警告

電池更換不當會有爆炸危險。請只使用同類電池或制造商推薦的功能相當的電池更換原有電池。請按制造商的說明處理廢舊電池。

#### 警告

電池更換不當會有爆炸危險。請使用製造商建議之相同或功能相當的電池更換原有電池。請按照製造商的說明指示處理廢棄舊電池。

#### Warnung

Bei Einsetzen einer falschen Batterie besteht Explosionsgefahr. Ersetzen Sie die Batterie nur durch den gleichen oder vom Hersteller empfohlenen Batterietyp. Entsorgen Sie die benutzten Batterien nach den Anweisungen des Herstellers.

#### Attention

Danger d'explosion si la pile n'est pas remplacée correctement. Ne la remplacer que par une pile de type semblable ou équivalent, recommandée par le fabricant. Jeter les piles usagées conformément aux instructions du fabricant.

#### ¡Advertencia!

Existe peligro de explosión si la batería se reemplaza de manera incorrecta. Reemplazar la batería exclusivamente con el mismo tipo o el equivalente recomendado por el fabricante. Desechar las baterías gastadas según las instrucciones del fabricante.

#### אזהרה!

קיימת סכנת פיצוץ של הסוללה במידה והוחלפה בדרך לא תקינה. יש להחליף את הסוללה בסוג התואם מחברת יצרן מומלצת. סילוק הסוללות המשומשות יש לבצע לפי הוראות היצרן.

هناك خطر من انفجار في حالة اسبدال البطارية بطريقة غير صحيحة فاعلil

اسبدال البطارية

فقط بنفس النوع أو ما يعادلها مما أوصت به الشركة المصنعة

جخلص من البطاريات المسحمة وفقا لعليمات الشركة الصانعة

#### 경고!

배터리가 올바르게 교체되지 않으면 폭발의 위험이 있습니다. 기존 배터리와 동일하거나 제조사에서 권장하는 동등한 종류의 배터리로만 교체해야 합니다. 제조사의 안내에 따라 사용된 배터리를 처리하여 주십시오.

#### Waarschuwing

Er is ontplofingsgevaar indien de batterij verkeerd vervangen wordt. Vervang de batterij slechts met hetzelfde of een equivalent type die door de fabrikant aanbevolen wordt. Gebruikte batterijen dienen overeenkomstig fabrieksvoorschriften afgevoerd te worden.

## Product Disposal



**Warning!** Ultimate disposal of this product should be handled according to all national laws and regulations.

### 製品の廃棄

この製品を廃棄処分する場合、国の関係する全ての法律・条例に従い処理する必要があります。

### 警告

本产品的废弃处理应根据所有国家的法律和规章进行。

### 警告

本產品的廢棄處理應根據所有國家的法律和規章進行。

### Warnung

Die Entsorgung dieses Produkts sollte gemäß allen Bestimmungen und Gesetzen des Landes erfolgen.

### ¡Advertencia!

Al deshacerse por completo de este producto debe seguir todas las leyes y reglamentos nacionales.

### Attention

La mise au rebut ou le recyclage de ce produit sont généralement soumis à des lois et/ou directives de respect de l'environnement. Renseignez-vous auprès de l'organisme compétent.

סילוק המוצר

אזהרה!

סילוק סופי של מוצר זה חייב להיות בהתאם להנחיות וחוקי המדינה.

عند التخلص النهائي من هذا المنتج ينبغي التعامل معه وفقا لجميع القوانين واللوائح الوطنية

### 경고!

이 제품은 해당 국가의 관련 법규 및 규정에 따라 폐기되어야 합니다.

### Waarschuwing

De uiteindelijke verwijdering van dit product dient te geschieden in overeenstemming met alle nationale wetten en reglementen.