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California Best Management Practices Regulations for Perchlorate Materials: This Perchlorate warning applies only to products containing CR (Manganese Dioxide) Lithium coin cells. “Perchlorate Material-special handling may apply. See www.dtsc.ca.gov/hazardouswaste/perchlorate”

WARNING: Handling of lead solder materials used in this product may expose you to lead, a chemical known to the State of California to cause birth defects and other reproductive harm.

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Returning Merchandise for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton, and mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

For faster service, RMA authorizations may be requested online (http://www.supermicro.com/support/rma/).

Whenever possible, repack the backplane in the original Supermicro box, using the original packaging materials. If these are no longer available, be sure to pack the backplane in an anti-static bag and inside the box. Make sure that there is enough packaging material surrounding the backplane so that it does not become damaged during shipping.

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alteration, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.
This chapter offers guidelines for personal and equipment safety, and notes about the BPN-SAS3-116A-N2 version documented in this manual.

1-1 ESD Safety Guidelines

Electrostatic Discharge (ESD) can damage electronic components. To prevent damage to your system, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

• Use a grounded wrist strap designed to prevent static discharge.

• Touch a grounded metal object before removing a component from the antistatic bag.

• Handle the backplane by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.

• When handling chips or modules, avoid touching their pins.

• Put the card and peripherals back into their antistatic bags when not in use.

1-2 General Safety Guidelines

• Always disconnect power cables before installing or removing any components from the computer, including the backplane.

• Disconnect the power cable before installing or removing any cables from the backplane.

• Make sure that the backplane is securely and properly installed on the motherboard to prevent damage to the system due to power shortage.
1-3 Version Information

The BPN-SAS3-116A-N2 backplane has been designed to utilize the most up-to-date technology available, providing your system with reliable, high-quality performance.

This manual reflects BPN-SAS3-116A-N2, Revision 1.10, the most current release available at the time of publication. Refer to the Supermicro Web site at www.supermicro.com for the latest updates, compatible parts and supported configurations.
Chapter 2

Connectors, Jumpers and LEDs

This manual covers BPN-SAS3-116A-N2 with NVMe capabilities.

2-1 Rear Connector Locations

The following connectors are on the side of the backplane that faces the rear of the chassis. They are marked by silkscreen labels.

1. Power Connectors: JPW1, JPW2 (4-pin)
2. Power Connector: JPW3 (2x2 pin)
3. Sideband Connector: T-SGPIO1 (6-pin)
4. SAS2 Connectors: JS1 and JS2
5. SAS3 Connectors: JSM1, JSM2
6. NVMe Connectors: JSM4
7. NVMe Connectors: JSM3
8. CPLD programming port: J23 (6-pin)

Figure 2-1. Rear Connector Locations
2-2 Rear Connector Definitions

1. Backplane Main Power Connectors
   The 4-pin connectors, designated JPW1 and JPW2, provide power to the backplane. See the table on the right for pin definitions.

<table>
<thead>
<tr>
<th>Pin#</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>+12V</td>
</tr>
<tr>
<td>2</td>
<td>Ground</td>
</tr>
<tr>
<td>3</td>
<td>Ground</td>
</tr>
<tr>
<td>4</td>
<td>+5V</td>
</tr>
</tbody>
</table>

2. DC Power Input
   This connector is the +12V DC power input connector for the NVMe solid state drive, which requires more power than typical SAS/SATA drives.

3. Sideband Connector
   This is the sideband connector for SAS2 and is designated T-SGPIO1.

<table>
<thead>
<tr>
<th>Pin</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>N/C</td>
</tr>
<tr>
<td>2</td>
<td>SDataIn (SGPIO Data In)</td>
</tr>
<tr>
<td>3</td>
<td>GND</td>
</tr>
<tr>
<td>4</td>
<td>SDataOut (SGPIO Data Out)</td>
</tr>
<tr>
<td>5</td>
<td>SLoad (SGPIO Load)</td>
</tr>
<tr>
<td>6</td>
<td>SClock (SGPIO Clock)</td>
</tr>
</tbody>
</table>

4. SAS2 Connectors
   The SAS2 connectors are designated JS1 (SAS2 in #1) and JS2 (SAS2 in #2).

5. SAS3 Connectors
   JSM1 and JSM2 provide connections for the SAS drive cables. Pins are defined according the SAS standard.

6-7. NVMe Connectors
   Two NVMe connectors are used to connect the NVMe drive cables. Each connector controls one NVMe SSD. Pins are defined according the NVMe standard.

8. CPLD Upgrade Port
   The JP23 connector is used only by manufacturing to upgrade firmware.
2-3 Rear Jumpers

Explanation of Jumpers
To modify the operation of the backplane, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. Note: On two pin jumpers, "Closed" means the jumper is on and "Open" means the jumper is off the pins.

<table>
<thead>
<tr>
<th>Jumper</th>
<th>Settings</th>
<th>Note</th>
</tr>
</thead>
<tbody>
<tr>
<td>JP1</td>
<td>Pins 1-2 (default)</td>
<td>Used only by manufacturing</td>
</tr>
<tr>
<td>JP2</td>
<td>Closed: Connects NVMe#0 to CPU1, and NVMe#1 to CPU2</td>
<td>NVMe mapping to CPU</td>
</tr>
<tr>
<td></td>
<td>Open: NVMe#0 and #1 to CPU1</td>
<td></td>
</tr>
<tr>
<td>J11, J12</td>
<td>Open</td>
<td>Reserved for future</td>
</tr>
</tbody>
</table>
2-4 Front Connectors and LED Indicators

Connectors for SAS drive numbers #0 through #7 are SAS3. SAS #8 and #9 are hybrid ports that support SAS2 or NVMe.

Figure 2-3. Front Connectors and LEDs

<table>
<thead>
<tr>
<th>Drive Number</th>
<th>Label</th>
<th>HDD Activity LED (blue)</th>
<th>Failure LED (red)</th>
</tr>
</thead>
<tbody>
<tr>
<td>SAS #0</td>
<td>J1</td>
<td>LED11</td>
<td>LED1</td>
</tr>
<tr>
<td>SAS #1</td>
<td>J2</td>
<td>LED12</td>
<td>LED2</td>
</tr>
<tr>
<td>SAS #2</td>
<td>J3</td>
<td>LED13</td>
<td>LED3</td>
</tr>
<tr>
<td>SAS #3</td>
<td>J4</td>
<td>LED14</td>
<td>LED4</td>
</tr>
<tr>
<td>SAS #4</td>
<td>J5</td>
<td>LED15</td>
<td>LED5</td>
</tr>
<tr>
<td>SAS #5</td>
<td>J6</td>
<td>LED16</td>
<td>LED6</td>
</tr>
<tr>
<td>SAS #6</td>
<td>J7</td>
<td>LED17</td>
<td>LED7</td>
</tr>
<tr>
<td>SAS #7</td>
<td>J8</td>
<td>LED18</td>
<td>LED8</td>
</tr>
<tr>
<td>SAS #8/NVMe #0*</td>
<td>J9</td>
<td>LED19</td>
<td>LED9**</td>
</tr>
<tr>
<td>SAS #9/NVMe #1*</td>
<td>J10</td>
<td>LED20</td>
<td>LED10**</td>
</tr>
</tbody>
</table>

*Hybrid ports; NVMe or SAS

**The failure LED is bi-color. Red indicates Failure/Rebuild; green indicates NVMe SSD ready to remove.
Notes
Disclaimer (cont.)

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