BPN-SAS3-213A-N8 Backplane

USER'S GUIDE

1.0
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California Best Management Practices Regulations for Perchlorate Materials: This Perchlorate warning applies only to products containing CR (Manganese Dioxide) Lithium coin cells. “Perchlorate Material-special handling may apply. See www.dtsc.ca.gov/hazardouswaste/perchlorate”

WARNING: Handling of lead solder materials used in this product may expose you to lead, a chemical known to the State of California to cause birth defects and other reproductive harm.

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Returning Merchandise for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton, and mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

For faster service, RMA authorizations may be requested online (http://www.supermicro.com/support/rma/).

Whenever possible, repack the backplane in the original Supermicro box, using the original packaging materials. If these are no longer available, be sure to pack the backplane in an anti-static bag and inside the box. Make sure that there is enough packaging material surrounding the backplane so that it does not become damaged during shipping.

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alteration, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.
Notes
Chapter 1

Safety Guidelines

To avoid personal injury and property damage, carefully follow all the safety steps listed below when accessing your system or handling the components.

1-1 ESD Safety Guidelines

Electrostatic Discharge (ESD) can damage electronic components. To prevent damage to your system, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

• Use a grounded wrist strap designed to prevent static discharge.

• Touch a grounded metal object before removing a component from the antistatic bag.

• Handle the backplane by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.

• When handling chips or modules, avoid touching their pins.

• Put the card and peripherals back into their antistatic bags when not in use.

1-2 General Safety Guidelines

• Always disconnect power cables before installing or removing any components from the computer, including the backplane.

• Disconnect the power cable before installing or removing any cables from the backplane.

• Make sure that the backplane is securely and properly installed on the motherboard to prevent damage to the system due to power shortage.
1-3 A Note to Users

All images and layouts in this user’s guide are based upon the latest PCB revision available at the time of publishing. The card you have received may not look exactly the same as the graphics in this manual.

1-4 Introduction to the BPN-SAS3-213A-N8 Backplane

The BPN-SAS3-213A-N8 backplane has been designed to utilize the most up-to-date technology available, providing your system with reliable, high-quality performance.

Always refer to the Supermicro web site at www.supermicro.com for the latest updates, compatible parts, and supported configurations.
Front Connectors

1. Power Connectors (4-pin): JPW3, JPW2, JPW1, JPW6, JPW5, and JPW4.
2. Upgrade Connector: JP1
3. SAS IN #0-#3: JSM1
4. SAS IN #4-#7: JSM3
5. SAS IN #8-#11: JSM2
6. SAS IN #12-#15: JSM4
7. NVMe IN #15: CN8
8. NVMe IN #13: CN4
9. NVMe IN #11: CN7
10. NVMe IN #9: CN3
11. NVMe IN #8: CN1
12. NVMe IN #10: CN5
13. NVMe IN #12: CN2
14. NVMe IN #14: CN6
2-2  Front Connector and Pin Definitions

1. Backplane Main Power Connectors
The 4-pin connectors, designated JPW3, JPW2, JPW1, JPW6, JPW5, and JPW4, provide power to the backplane. See the table on the right for pin definitions.

<table>
<thead>
<tr>
<th>Pin#</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>+12V</td>
</tr>
<tr>
<td>2 and 3</td>
<td>Ground</td>
</tr>
<tr>
<td>4</td>
<td>+5V</td>
</tr>
</tbody>
</table>

2. Upgrade Connector
The upgrade connector is used for manufacturer diagnostic purposes only.

3. - 6. SAS IN Ports (Sideband included)
The SAS ports are used to connect the SAS3 drive cables. The four SAS IN ports are designated JSM1, JSM3, JSM2, and JSM4. Each port is also compatible with SATA drives. However, mixing SAS3 and SATA drives is not recommended.

<table>
<thead>
<tr>
<th>Pin #</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>A0</td>
<td>SB0</td>
</tr>
<tr>
<td>B2</td>
<td>SB1</td>
</tr>
<tr>
<td>C2</td>
<td>SB2</td>
</tr>
<tr>
<td>B1</td>
<td>SB3</td>
</tr>
</tbody>
</table>

3. - 6. - Sideband Definitions
(JSM1 - JSM4)

7. - 10. NVMe IN Ports
The NVMe ports are used to connect the NVMe drive cables. OCuLink cable CBL-SAST-0820 should be used to connect these ports to the corresponding ports on add-on card AOC-SLG3-4E2P. The add-on card in slot 1 of the motherboard should have its ports CN1, CN2, CN3, and CN4 connected to the backplane's ports CN1, CN3, CN5, and CN7, respectively. The add-on card in slot 2 of the motherboard should have its ports CN1, CN2, CN3, and CN4 connected to CN2, CN4, CN6, and CN8, respectively.
2-3 Front Jumpers

Figure 2-2. Front Jumpers

Explanation of Jumpers

To modify the operation of the backplane, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. Note: On two pin jumpers, "Closed" means the jumper is on and "Open" means the jumper is off the pins.
### Jumper Settings

<table>
<thead>
<tr>
<th>Jumper</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>J17 CPU_SEL0</td>
<td>NVMe drive connection 8+0</td>
</tr>
<tr>
<td>J18 CPU_SEL1</td>
<td>NVMe drive connection 4+4 (default for two 4-NVMe AOC interface)</td>
</tr>
<tr>
<td>J19 CPU_SEL2</td>
<td>NVMe drive connection 2+6</td>
</tr>
<tr>
<td>J20 I2C_SEL0</td>
<td>NVMe drive connection 6+2</td>
</tr>
<tr>
<td>J21 I2C_SEL1</td>
<td>Reserved for future use. Open by default.</td>
</tr>
</tbody>
</table>

- **0 0**  
  - BMC I2C address is 66h (default)
- **0 1**  
  - BMC I2C address is 76h
- **1 0**  
  - BMC I2C address is 6Eh
- **1 1**  
  - BMC I2C address is 7Eh
### 2-4 Rear Components, Connectors, and LED Indicators

#### SAS/SATA/NVMe Connectors and LED Indicators

<table>
<thead>
<tr>
<th>Rear Connector</th>
<th>SAS Drive Number</th>
<th>Failure LED</th>
<th>Activity LED</th>
</tr>
</thead>
<tbody>
<tr>
<td>#0</td>
<td>SAS3/SATA HDD #0</td>
<td>LED1</td>
<td>LED2</td>
</tr>
<tr>
<td>#1</td>
<td>SAS3/SATA HDD #1</td>
<td>LED5</td>
<td>LED6</td>
</tr>
<tr>
<td>#2</td>
<td>SAS3/SATA HDD #2</td>
<td>LED8</td>
<td>LED3</td>
</tr>
<tr>
<td>#3</td>
<td>SAS3/SATA HDD #3</td>
<td>LED10</td>
<td>LED7</td>
</tr>
<tr>
<td>#4</td>
<td>SAS3/SATA HDD #4</td>
<td>LED13</td>
<td>LED11</td>
</tr>
<tr>
<td>#5</td>
<td>SAS3/SATA HDD #5</td>
<td>LED14</td>
<td>LED4</td>
</tr>
<tr>
<td>#6</td>
<td>SAS3/SATA HDD #6</td>
<td>LED15</td>
<td>LED9</td>
</tr>
<tr>
<td>#7</td>
<td>SAS3/SATA HDD #7</td>
<td>LED16</td>
<td>LED12</td>
</tr>
<tr>
<td>#8</td>
<td>SAS3/SATA/NVMe HDD #8</td>
<td>D1</td>
<td>LED18</td>
</tr>
<tr>
<td>#9</td>
<td>SAS3/SATA/NVMe HDD #9</td>
<td>D2</td>
<td>LED21</td>
</tr>
<tr>
<td>#10</td>
<td>SAS3/SATA/NVMe HDD #10</td>
<td>D3</td>
<td>LED19</td>
</tr>
<tr>
<td>#11</td>
<td>SAS3/SATA/NVMe HDD #11</td>
<td>D4</td>
<td>LED22</td>
</tr>
<tr>
<td>#12</td>
<td>SAS3/SATA/NVMe HDD #12</td>
<td>D5</td>
<td>LED23</td>
</tr>
<tr>
<td>#13</td>
<td>SAS3/SATA/NVMe HDD #13</td>
<td>D6</td>
<td>LED17</td>
</tr>
<tr>
<td>#14</td>
<td>SAS3/SATA/NVMe HDD #14</td>
<td>D7</td>
<td>LED20</td>
</tr>
<tr>
<td>#15</td>
<td>SAS3/SATA/NVMe HDD #15</td>
<td>D8</td>
<td>LED24</td>
</tr>
</tbody>
</table>

![Diagram of Rear SAS/SATA Connectors and LED Indicators](image)

*Figure 2-3. Rear SAS/SATA Connectors and LED Indicators*
Disclaimer (cont.)

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