

INSTRUCTIONS ON HOW TO ENABLE INTEL® SGX SUPPORT ON SUPERMICRO X12DP SERIES/X12SP SERIES MOTHERBOARDS

USER'S GUIDE

Revision 1.0

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Preface

Target Audience and Writing Purpose

This user's guide is written for system integrators, IT technicians, and knowledgeable end users. It provides information on how to enable Intel® SGX support on Supermicro X12DP Series/X12SP Series motherboards.

About This User's Guide

This user's guide provides detailed instructions for the user to enable Intel® Software Guard Extensions (SGX) support on Supermicro X12DP Series/X12SP Series motherboards that are based on the 3rd Gen Intel® Xeon® Scalable Processors (in Socket P+ (LGA-4189)). Intel SGX is a set of instruction codes built into certain high-level processors to allow the user and the operating system (OS) to define particular regions of memory as private regions and encrypt the contents which reside in these regions to protect the data and instruction codes therein from any exposure to outside threats. SGX, essentially, creates a private, protected, and safe environment for the processor to operate by denying access to a perceived outsider, effectively eliminating the threats caused by any outside intrusions and encroachments. It is an effective measure to achieve data protection and security. However, for SGX to function properly, please ensure that the processor(s) installed on the motherboard and the BIOS utility used in the system are capable of supporting Intel SGX and then follow the instructions provided in this user's guide to enable SGX support for your system.

Please also note that all Supermicro's products are intended to be installed, configured, and serviced by professional technicians only. For processor/memory updates, please refer to our website at http://www.supermicro.com/products/.

Conventions Used in the Manual

Special attention should be given to the symbol below for proper BIOS configuration and for prevention of accidental damage to your system components:



Note: Important Information given for proper system setup or for proper firmware configuration.

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Chapter 1

Steps to Enable Intel® SGX in the UEFI BIOS Setup Utility

This section provides detailed instructions on how to enable Intel® Software Guide Extensions support on the UEFI BIOS. Please follow the instructions carefully to configure your BIOS settings for Intel SGX to work properly.

1.1 Pre-Requirements

To ensure that Intel SGX is supported by your system, please be sure to meet the following requirements first before enabling Intel SGX in the BIOS utility:

- 1. Be sure to use the correct type of processors that support Intel SGX.
- 2. Please be sure to use BIOS Rev. 1.1a or a newer version.
- 3. Install DIMM modules based on the memory configuration supported by Intel SGX as listed in the tables below.

Memory Configuration Tables for SGX Support

a.Memory Configuration Table for X12DP Series Motherboards

	X12 DP SGX-Supported Memory Configuration															
DDR4	F1	F2	E1	E2	H1	H2	G1	G2	C2	C1	D2	D1	A2	A1	B2	B1
8 DIMMs	DDR4		DDR4		DDR4		DDR4			DDR4		DDR4		DDR4		DDR4
	DDR4	DDR4	DDR4		DDR4	DDR4	DDR4		DDR4	DDR4	DDR4		DDR4	DDR4	DDR4	
12 DIMMe	DDR4	DDR4	DDR4		DDR4		DDR4	DDR4	DDR4	DDR4	DDR4		DDR4		DDR4	DDR4
12 DIMINIS	DDR4		DDR4	DDR4	DDR4		DDR4	DDR4	DDR4		DDR4	DDR4	DDR4		DDR4	DDR4
	DDR4		DDR4	DDR4	DDR4	DDR4	DDR4		DDR4		DDR4	DDR4	DDR4	DDR4	DDR4	
16 DIMMs	DDR4	DDR4	DDR4	DDR4	DDR4	DDR4	DDR4	DDR4	DDR4	DDR4	DDR4	DDR4	DDR4	DDR4	DDR4	DDR4

b Memory Configuration Table for X12SP Series Motherboards

X12 UP SGX-Supported Memory Configuration								
DDR4	F1	E1	H1	G1	C1	D1	A1	B1
8 DIMMs	DDR4							

Processor Requirements

• 3rd Gen Intel® Xeon® Scalable Processors (in Socket P+ (LGA-4189))

OS Requirements

- Windows Server 2019
- Linux: Ubuntu 20.04, Ubuntu 18.04, Red Hat Enterprise Linux Server 8.2
- For more information on OS requirements for Intel's SGX support, please refer to Intel's website.

Software Requirements

Intel® SGX Platform Software

- For Intel® SGX application to work properly in a system, Intel® SGX PSW is required to be pre-installed before shipping.
- A standalone Intel® SGX PSW for Windows OS is also available. (Please refer to Intel's website.)

Note: Depending on Windows version, PSW and drivers may already be automatically installed.

Supermicro Platform Support

- X12DP Series motherboards
- X12SP Series motherboards
- Supermicro servers/systems based on X12DP Series/X12SP Series motherboards

1.2 Step 1: Entering the UEFI BIOS Utility to Enable Intel SGX Support

To enable Intel SGX support on the BIOS setting, you will need to enter the BIOS Setup utility by following the instructions below:

1. Press during system boot to enter the BIOS Setup utility.

Aptio Setup – AMI Main <mark>Advanced</mark> Event Logs IPMI Security Boot Save & Exit	
 Boot Feature CPU Configuration Chipset Configuration Server ME Information PCH SATA Configuration PCH SATA Configuration PCLE/PCL/PAP Configuration Super ID Configuration Serial Port Console Redirection ACPI Settings Trusted Computing HTTP Boot Configuration SMC-KMS Server Configuration Intel(R) Ethernet Controller X550 - 3C:EC:EF:30:52:3E 	CPU Configuration
 Intel(R) Ethernet Controller X550 - 3C:EC:EF:30:52:3F Intel(R) I350 Gigabit Network Connection - 3C:EC:EF:2D:32:C4 Intel(R) I350 Gigabit Network Connection - 3C:EC:EF:2D:32:C5 Intel(R) I350 Gigabit Network Connection - 3C:EC:EF:2D:32:C6 Intel(R) I350 Gigabit Network Connection - 3C:EC:EF:2D:32:C7 TLS Authenticate Configuration Driver Health 	★+: Select Screen 11: Select Item Enter: Select ★/-: Change Opt, F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
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- 2. Select the Advanced tab from the menu bar on the top of the screen. Using the down arrow key, select *CPU Configuration* and press <Enter> as shown below.
- 3. When the CPU Configuration submenu displays, scroll down to select *Total Memory Encryption (TME)*.
- 4. Once Total Memory Encryption (TME) is highlighted, press <Enter>. The TME option dialogue box will display as shown below.



5. From the option dialogue box, select *Enabled* and press <Enter> to enable TME support for your system.

1.3 Step 2: Disabling Mirror Mode, ADDDC Sparing*, and Patrol Scrub Support in the Memory-RAS Configuration Submenu

For Intel SGX to function properly, please disable the following features in the Memory-RAS submenu first:

- 1. Mirror Mode
- 2. ADDDC (Adaptive Double Device Data Correction) Sparing* (See the notes below.)
- 3. Patrol Scrub
 - **Note 1**: The feature "*ADDDC Sparing*" will only be activated and displayed on the BIOS screen when x4 DRAM DIMMs, which support ADDDC(+1), are installed in the system. Without required DRAMs present, this feature will not be activated but remains dormant, hidden from the user's view. For ADDDC memory support, please refer to the Memory RAS Configuration User's Guide posted on our website at: https://www.supermicro.com/manuals/other/Memory_RAS_Configuration_User_Guide.pdf.

Note 2: If ADDDC Sparing does not appear on your BIOS screen, this feature is inactive and masked off by default, and you will not need to disable it manually.

To properly disable Mirror Mode, Patrol Scrub, and ADDDC Sparing (if needed), please follow the instructions listed below:

- 4. From the Advanced menu, select Chipset Configuration and press < Enter>.
- 5. From the Chipset Configuration submenu, select North Bridge and press < Enter>.
- 6. From the North Bridge submenu, select *Memory Configuration* and press <Enter> as shown below.

Uncore Configuration	Displays and provides
Memory Configuration	option to change the Memor
IIO Configuration	Settings
	++: Select Screen
	Enter: Select
	+/-: Change Opt.
	F1: General Help
	F2: Previous Values
	F3: Uptimized Defaults

7. When the Memory Configuration submenu displays, scroll down to select the Memory RAS Configuration submenu as shown below.

Advanced	Aptio Setup – AMI	
Advanced Integrated Memory Controller (IMC) Enhanced PPR Operation Mode Enforce POR PPR Type Memory Frequency Data Scrambling for DDR4 2% Refresh Enable Memory RAS Configuration	(Disable) (Test and Repair) (POR) (Hard PPR) (Auto) (Enable) (Auto)	Displays and provides option to change the Memory RAS Settings ++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help
		F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
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8. When *Memory RAS Configuration* is highlighted as shown above, press <Enter>. The Memory RAS Configuration submenu will display. Please check to ensure that Mirror Mode is **Disabled** as shown below.

Memory RAS Configuration	Aptio Setup – AMI	
Memory RAS Configuration Setup Enable Pcode WA for SAI PG Mirror Mode UEFI ARM Mirror Correctable Error Threshold Partial Cache Line Sparing PCLS ADDOC Sparing Patrol Scrub	[Disabled] [Disabled] [Disabled] 512 [Enabled] DISabled] DISabled Enabled	<pre>#*: Select Screen 14: Select Item Enter: Select 4/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>
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- 9. With Mirror Mode disabled, scroll down to check if the feature ADDDC Sparing displays on your screen. If ADDDC Sparing does not appear on your screen, this feature is not activated, and you will not need to disable it manually. Please skip this step. (See the notes on the previous page.)
- 10. If ADDDC Sparing is displayed on your screen, use the arrow keys to select it by highlighting it and press <Enter>.
- 11. The ADDDC Sparing option dialogue box will display. Select *Disabled* from the option box and press <Enter> to disable ADDDC Sparing as shown above.

- 12. Using the down arrow key, select the feature "Patrol Scrub" by highlighting it.
- With Patrol Scrub highlighted, press <Enter>. From the option dialogue box, select Disabled and press <Enter> to disable Patrol Scrub support for your system as shown below.

Advanced	Aptio Setup – AMI	
Memory RAS Configuration Setup Enable Poode WA for SAI PG Mirror Mode UEFI ARM Mirror Correctable Error Threshold Partial Cache Line Sparing PCLS Patrol Scrub	(Disabled) (Disabled) (Disabled) Si2 (Enabled) Patrol Scrub Coisabled Enabled Enable at End of POST	Enable/Disable Patrol Scrub
		<pre>++: Select Screen f1: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>
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14. Press <F4> to save the settings and reboot the system for the changes you've made to take effect.

1.4 Step 3: Enabling NUMA and Disabling UMA-Based Clustering Support in the ACPI Submenu

For Intel SGX to function properly, please enable NUMA (Non-Uniform Memory Access) and disable UMA-Based Clustering support in the ACPI submenu by following the instructions below.

1. From the Advanced menu, scroll down to select *ACPI Settings* and press <Enter> as shown below.

Aptio Setup – AMI Main <mark>Advanced</mark> Event Logs IPMI Security Boot Save & Exit						
 Boot Feature CPU Configuration Chipset Configuration Server ME Information PCH SATA Configuration PCH SATA Configuration PCE SATA Configuration Network Configuration Serial Port Console Redirection ACPI Settings Trusted Computing HTTP Boot Configuration SMC-KMS Server Configuration SMC-KMS Server Configuration Intel(R) Ethernet Controller X550 - 3C:EC:EF:30:52:3E Intel(R) Ethernet Controller X550 - 3C:EC:EF:30:52:3F Intel(R) I350 Gigabit Network Connection - 3C:EC:EF:2D:32:06 Intel(R) I350 Gigabit Network Connection - 3C:EC:EF:2D:32:06 Intel(R) I350 Gigabit Network Connection - 3C:EC:EF:2D:32:06 Intel(R) I350 Gigabit Network Connection - 3C:EC:EF:2D:32:07 TLS Authenticate Configuration 	System ACPI Parameters. ++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit					
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2. From the ACPI submenu, select NUMA and press <Enter>.



3. From the NUMA option dialogue box as shown above, select *Enabled*, and press <Enter> to enable NUMA support.

4. After NUMA is enabled, scroll down to select UMA-Based Clustering and press <Enter>. When the option dialogue box displays, select **Disable (ALL2ALL)** and press <Enter> to disable UMA-Based Clustering support as show below.

Advanced	Aptio Setup – AMI	
ACPI Settings NUMA UMA-Based Clustering NHEA Support High Precision Event Timer	[Enabled] [Disable (A112A11) [Enabled] [Enabled] [Enabled] Disable (A112A11) Hemisphere (2-clusters)	UMA Based Clustering options include Disable (ALL2ALL), Hemisphere (2 cluster), and Quadrant (4 cluster, not supported on ICX). These option are only valid when SNC is disabled. If SNC is enabled, UMA-Based Clustering is automatically disabled by BIOS.
		++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
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5. After you've enabled NUMA and disabled UMA-Based Clustering support in the ACPI settings, press <F4> to save the settings and reboot the system for the changes you've made to take effect.

1.5 Step 4: Enabling SGX Support in the CPU Configuration Settings

After you've properly configured memory-related features in the UEFI BIOS utility as instructed above, your system is ready to support Intel Software Guard Extensions. To use SGX, please follow the instructions below.

- 1. After you've saved the changes made from Step 1 to Step 3 by pressing <F4> as instructed in the previous sections, your system will reboot.
- 2. Press to enter the BIOS Setup Utility during system boot.
- 3. Select *Advanced* from the menu bar on top of the screen. From the Advanced menu, select *CPU Configuration* and press <Enter>.
- 4. When the CPU Configuration submenu displays, scroll down to select *SW Guard Extensions (SGX)* and press <Enter>.
- 5. From the option dialogue box, select *Enabled* to enable SGX support for your system as shown below.

Advanced	Aptio Setup – AMI	
 CPU1 Core Disable Bitmap Hyper-Threading [ALL] Hardware Prefetcher Adjacent Cache Prefetch DCU Streamer Prefetcher DCU IP Prefetcher LLC Prefetch Extended APIC VMX Enable SMX PPIN Control AES-NI TME, TME-MT, TDX 	[Enable] [Enable] [Enable] [Enable] [Enable] [Enable] [Disable] [Enable] SH Guard Extensions (SGX)	Enable/Disable Software Guard Extensions (SGX). Will disable and grayed out ADDDC, UMA-Based Clustering SGX, mirror and enable NUMA. SGX cannot co-exists PMem(Persistent Memory), ADDDC, MCA Recovery-Execution Path, Run Sune, Memory Mirroing, Address Range Mirroing(in the same memory regison), dynamice change CPU/memory/lio, static/Hand Partitioning
Total Memory Encryption (TME) Total Memory Encryption Multi-Tenant(TME-MT) Max TME-MT Keys	(Enabled) (Disabled) 0x0	
Software Guard Extension (SGX SGX Factory Reset SW Guard Extensions (SGX)) [Disabled] [Enabled]	+/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults • F4: Save & Exit ESC: Exit
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6. After enabling SGX, press <F4> to save all settings and reboot the system. All the changes you've made will take effect after system reboot.