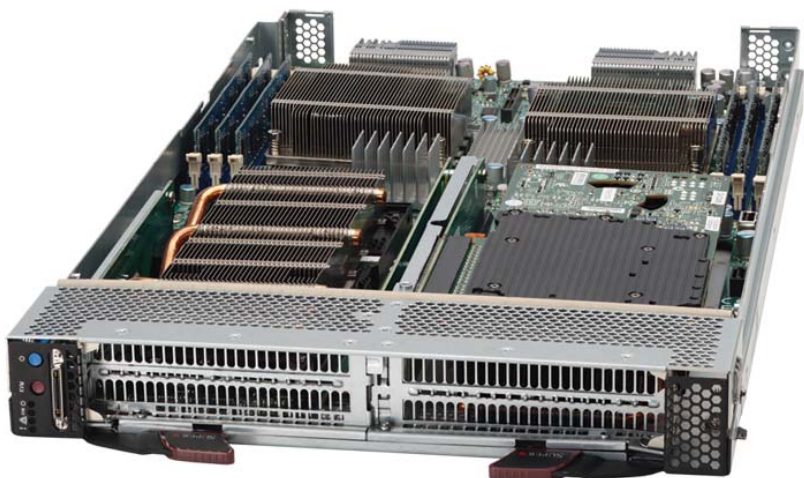




## **SBI-7126TG Blade Module**



## **User's Manual**

Revision 1.0a

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Manual Revision 1.0a

Release Date: May 18, 2011

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# Preface

## About this Manual

This manual is written for professional system integrators, Information Technology professionals, service personnel and technicians. It provides information for the installation and use of Supermicro's SBI-7126TG blade module. Installation and maintenance should be performed by experienced professionals only.

## Manual Organization

### Chapter 1: Introduction

The first chapter provides a checklist of the main components included with the SBI-7126TG blade module and describes its main features.

### Chapter 2: System Safety

You should familiarize yourself with this chapter for a general overview of safety precautions that should be followed when installing and servicing the SBI-7126TG blade module.

### Chapter 3: Setup and Installation

Refer to this chapter for details on installing the SBI-7126TG blade module into the Superblade chassis. Other sections cover the installation and placement of memory modules and the installation of hard disk drives into the blade module.

### Chapter 4: Blade Module Features

This chapter covers features and component information about the SBI-7126TG blade module. Included here are descriptions and information for mainboard components, connectors, LEDs and other features of the blade module.

### Chapter 5: BIOS

BIOS setup is covered in this chapter for the SBI-7126TG blade module.

### Appendix A: BIOS POST Codes

BIOS POST Codes for the SBI-7126TG blade module are explained in this appendix.

## Notes

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# Chapter 1

## Introduction

### 1-1 Overview

The SBI-7126TG blade module is a compact self-contained server that connects into a pre-cabled enclosure that provides power, cooling, management and networking functions. One enclosure for the SBI-7126TG blade module can hold ten blade units.

In this manual, “blade system” refers to the entire system (including the enclosure and blades units), “blade” or “blade unit” refers to a single blade module and “blade enclosure” is the chassis that the blades, power supplies and modules are housed in.

Please refer to our web site for information on operating systems that have been certified for use with the SuperBlade ([www.supermicro.com/products/superblade/](http://www.supermicro.com/products/superblade/)).

### 1-2 Product Checklist of Typical Components

Your blade module ships with its mainboard already installed in its chassis. Memory, hard disk drives and the CPU must all be installed by the user after shipment. See [Chapter 3: "Setup and Installation" on page 3-1](#) for details on installation of these components.

Aside from the blade module unit itself, the following optional Mezzanine add-on cards (with InfiniBand Switch) may be ordered for your blade module:

- AOC-IBH-XDD
- AOC-IBH-XDS
- AOC-IBH-XQS
- AOC-XEH-iN2

See the [Supermicro website](#) and the *Superblade Network Modules User's Manual* on your Superblade system's CD-ROM for more details on these add-on cards including instructions on how to install them.



**NOTE:** Some of these add-on cards may not be available at the time of this manual's publication. Please refer to the [Supermicro website](#) for their availability.

## 1-3 Blade Module Features

**Table 1-1** lists the main features of the SBI-7126TG blade module. See the proceeding section for components typically included in a blade system and other optional components. Specific details on the SBI-7126TG blade module is found in [Chapter 4: "Blade Module Features" on page 4-1](#).

**Table 1-1. SBI-7126TG Blade Specification Features**

Mainboard	B8DTG (proprietary form factor) Chassis Dimensions (HxWxD): 11.32" x 1.67" x 18.9"
Processors	Single or dual Intel™ Xeon® 5500/5600 Sequence processors. Please refer to our web site for a complete listing of supported processors.
FSB Speed	QPI 4.8/6.4G bus speed
Chipset	Intel IOH36D + ICH10/Intel ICH10 Northbridge/Southbridge
Graphics Controller	Onboard Matrox G200eW Graphics chip with 16MB of SDRAM
BIOS	32 Mb AMI® SPI Flash EPROM BIOS
Memory Capacity	Six 240-pin DIMM sockets supporting up to 96 GBECC Registered DDR3-1333/1066/800 SDRAM.
Expansion Slots	Two (2) PCI-E 2.0 x16 expansion slots

### Processors

The SBI-7126TG blade module supports up to dual 1366-pin Intel Xeon 5500/5600 series processors.

Refer to the Supermicro web site for a complete listing of supported processors (<http://www.supermicro.com/products/superblade>). Please note that you will need to check the detailed specifications of a particular blade module for a list of the CPUs it supports.

Details on installation of the processor into the SBI-7126TG blade module are found in [Chapter 3: "Setup and Installation" on page 3-1](#).

## Memory

The SBI-7126TG blade module has six 240-pin DIMM sockets that can support up to 96 GB ECC Registered DDR3-1333/1066/800 SDRAM. Both interleaved and non-interleaved memory are supported, so you may populate any number of DIMM slots.

Please refer to the Supermicro web site for a list of supported memory ([www.supermicro.com/products/superblade](http://www.supermicro.com/products/superblade)). The detailed specifications for a blade module will contain a link to a list of recommended memory sizes and manufacturers.

Details on installation of memory modules into the SBI-7126TG blade module are found in [Chapter 3: "Setup and Installation" on page 3-1](#).

## Storage

The SBI-7126TG blade module does not contain any hard disk drives or drive carriers.

## Density

A maximum of ten blade modules may be installed into a single blade enclosure. Each blade enclosure is a 7U form factor, so a standard 42U rack may accommodate up to six enclosures with sixty blade modules, or the equivalent of sixty 1U servers. With the inclusion of six CMM modules, twelve Gigabit Ethernet switches and six InfiniBand switches, this would occupy up to 84U space in a conventional 1U server configuration.

[Figure 1-1](#) displays a view of a full rack with six blade enclosures in it, each with ten blades to an enclosure.

**Figure 1-1. Full Rack of Blade Enclosures and Blade Servers**



## 1-4 Contacting Supermicro

### Headquarters

Address: Super Micro Computer, Inc.  
980 Rock Ave.  
San Jose, CA 95131 U.S.A.

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Fax: +1 (408) 503-8008

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Web Site: [www.supermicro.com.tw](http://www.supermicro.com.tw)

Technical Support:

Email: [support@supermicro.com.tw](mailto:support@supermicro.com.tw)

Tel: +886-2-8228-1366, ext. 132 or 139

# Notes



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# Chapter 2

## System Safety

### 2-1 Electrical Safety Precautions

Basic electrical safety precautions should be followed to protect yourself from harm and the SuperBlade from damage:

- Be aware of how to power on/off the enclosure power supplies and the individual blades as well as the room's emergency power-off switch, disconnection switch or electrical outlet. If an electrical accident occurs, you can then quickly remove power from the system.
- Do not work alone when working with high voltage components.
- Power should always be disconnected from the blade module when removing or installing such system components as the mainboard, memory modules and processors.
- When working around exposed electrical circuits, another person who is familiar with the power-off controls should be nearby to switch off the power if necessary.
- Use only one hand when working with powered-on electrical equipment. This is to avoid making a complete circuit, which will cause electrical shock. Use extreme caution when using metal tools, which can easily damage any electrical components or circuit boards they come into contact with.
- Do not use mats designed to decrease electrostatic discharge as protection from electrical shock. Instead, use rubber mats that have been specifically designed as electrical insulators.
- The power supply power cords must include a grounding plug and must be plugged into grounded electrical outlets. Power input requires 110-240 VAC, depending upon your power supply module.
- Mainboard Battery: This battery must be replaced only with the same or an equivalent type recommended by the manufacturer (CR2032 Lithium 3V battery). Dispose of used batteries according to the manufacturer's instructions.



**WARNING:** There is a danger of explosion if the onboard battery is installed upside down, which will reverse its polarities.

- Mainboard replaceable soldered-in fuses: Self-resetting PTC (Positive Temperature Coefficient) fuses on the mainboard must be replaced by trained service technicians only. The new fuse must be the same or equivalent as the one replaced. Contact technical support for details and support.

## 2-2 General Safety Precautions

Follow these rules to ensure general safety:

- Keep the area around the SuperBlade clean and free of clutter.
- Place the blade module cover and any system components that have been removed away from the system or on a table so that they won't accidentally be stepped on.
- While working on the system, do not wear loose clothing such as neckties and unbuttoned shirt sleeves, which can come into contact with electrical circuits or be pulled into a cooling fan.
- Remove any jewelry or metal objects from your body, which are excellent metal conductors that can create short circuits and harm you if they come into contact with printed circuit boards or areas where power is present.
- After accessing the inside of the system, replace the blade module's cover before installing it back into the blade enclosure.

## 2-3 Electrostatic Discharge Precautions

Electrostatic discharge (ESD) is generated by two objects with different electrical charges coming into contact with each other. An electrical discharge is created to neutralize this difference, which can damage electronic components and printed circuit boards.

The following measures are generally sufficient to neutralize this difference **before** contact is made to protect your equipment from ESD:

- Use a grounded wrist strap designed to prevent static discharge.
- Keep all components and printed circuit boards (PCBs) in their antistatic bags until ready for use.
- Touch a grounded metal object before removing the board from the antistatic bag.
- Do not let components or PCBs come into contact with your clothing, which may retain a charge even if you are wearing a wrist strap.
- Handle a board by its edges only; do not touch its components, peripheral chips, memory modules or contacts.
- When handling chips or modules, avoid touching their pins.
- Put the mainboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure the blade enclosure provides excellent conductivity between the power supplies, the blade modules and the mainboard.

## 2-4 Operating Precautions

Care must be taken to assure that the cover of the blade unit is in place when the blade is operating to assure proper cooling. Out of warranty damage to the blade can occur if this practice is not strictly followed.

Any drive carrier without a hard drive installed must remain fully installed in the drive bay when the blade module is operating to ensure proper airflow.

# Chapter 3

## Setup and Installation

### 3-1 Overview

This chapter covers the setup and installation of the blade module and its components.

### 3-2 Installing Blade Modules

Up to ten SBI-7126TG blade modules may be installed into a single blade enclosure (depending upon your enclosure and blade). Blade modules with Windows and Linux operating systems may be mixed together in the same blade enclosure.

#### Powering Up a Blade Unit

Each blade unit may be powered on and off independently from the rest of the blades installed in the same enclosure. A blade unit may be powered up in two ways:

- Press the power button on the blade unit.
- Use IPMIView or the web-browser based management utility to apply power using either a CMM module, or by the use of the onboard BMC chip in the blade module.

#### Powering Down a Blade Unit

A blade unit may be powered down in either of five ways:

- Press the power button on the blade unit.
- Use IPMIView or the web-browser based management utility to power down (if you have Operator or Admin privileges on the CMM).
- Use IPMITool when connected to the CMM to power down (if you have Operator or Admin privileges on the CMM).
- Use IPMIview or a browser connected to the onboard BMC chip attached to the blade to power down.
- Use IPMITool to use a Command Line Interface (CLI) to the onboard BMC chip (if you have Operator or Admin privileges).

#### Removing a Blade Unit from the Enclosure

Although the blade system may continue to run, individual blades should always be powered down before removing them from the enclosure.

##### ***Removing a Blade Unit from the Enclosure***

1. Power down the blade unit (see ["Powering Down a Blade Unit"](#) above).
2. Squeeze both handles to depress the red sections then pull out both handles completely and use them to pull the blade unit from the enclosure.



**NOTE:** Blade Modules can be Hot-Plugged from the enclosure.

## Removing/Replacing the Blade Cover

The blade cover must be removed to access the mainboard when you need to install or remove processors, memory units, the onboard battery and so on.

### *Removing/Replacing the Blade Cover*

1. Remove the blade unit from the enclosure (see ["Removing a Blade Unit from the Enclosure"](#) above).
2. Remove the retaining screw from the top of the blade cover, then depress the two buttons on the cover while pushing the cover toward the rear of the blade unit. When it stops, lift the cover off the blade unit.
3. To replace the cover, fit the six grooves in the cover into the studs in the sides of the blade, then slide the cover toward the front of the blade to lock it into place. Rescrew the blade cover retaining screw back into place.

## Installing a Blade Unit into the Enclosure

Make sure the cover of the blade unit has been replaced first before installing a blade unit in the enclosure.

### *Installing a Blade Unit into the Enclosure*

1. Slowly push the blade unit into its bay with the handles fully pulled out (see [Figure 3-1](#)).
2. When the blade stops, push the handles back in to their locked position, making sure the notches in both handles catch the lip of the enclosure (see [Figure 3-2](#)).



**NOTE:** Blade Modules can be Hot-Plugged into the enclosure.

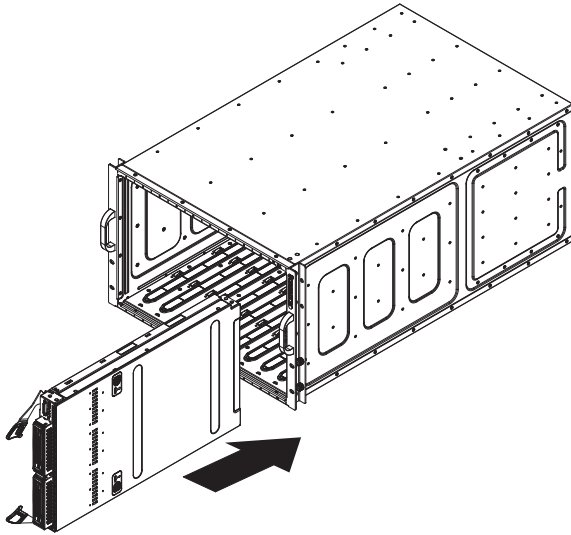


**WARNING:** Use extreme caution when inserting a blade module into the enclosure. If the blade's power connector becomes damaged, it can damage pins on other blade bays that it is inserted into.

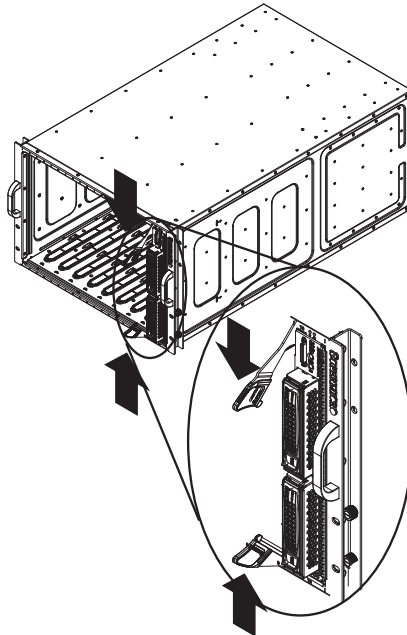


**NOTE:** The below illustrations in [Figure 3-1](#) and [Figure 3-2](#) demonstrate the removal and insertion for all Supermicro blade modules. Your blade module or enclosure may appear different than illustrated but uses the same handles and method of insertion.

**Figure 3-1. Inserting a Blade into the Enclosure**



**Figure 3-2. Locking the Blade into Position**



## 3-3 Processor Installation

One or two processors may be installed to the mainboard of each blade unit. See [Chapter 1](#) for general information on the features of the blade unit and the [Supermicro web site](#) for further details including processor, memory and operating system support.



**WARNING:** This action should only be performed by a trained service technician. Allow the processor heatsink to cool before removing it.

### *Removing a Processor*

1. Power down and remove the blade unit from the enclosure (see [Section 3-2: Installing Blade Modules on page 3-1](#) for details).
2. Remove the cover of the blade unit (see ["Removing/Replacing the Blade Cover" on page 3-2](#)).
3. Loosen the four screws that secure the heatsink to the mainboard.
4. Remove the heatsink by *gently* rotating it back-and-forth sideways with your fingers to release it from the processor. Set the heatsink aside and upside-down so that nothing comes into contact with the thermal grease on its underside.
5. Raise the lever of the processor socket up until the processor is released from the socket, then lift the silver cover plate and remove the processor.



**WARNING:** This action should only be performed by a trained service technician.

### *Installing a Processor*

1. If present, remove the protective black PnP cap from the processor socket.
2. Raise the lever of the processor socket until it reaches its upper limit.
3. Lift the silver cover plate completely up and out of the way.



**NOTE:** Be careful not to damage the pins protruding from the CPU socket.

4. Align the CPU key (the semi-circle cutout) with the socket key (the notch below the gold color dot on the side of the socket) ([Figure 3-3](#)).
5. Once the CPU and the socket are aligned, carefully lower the CPU straight down into the socket. Do not rub the CPU against the surface of the socket or its pins to avoid damaging the CPU or the socket.
6. With the CPU in the socket, inspect the four corners of the CPU to make sure that it sits level and is properly installed.

7. Once the CPU is securely seated in the socket, lower the CPU load plate to the socket.
8. To install the heatsink, apply thermal grease to the top of the processor. (If reinstalling a heatsink, first clean off the old thermal grease with a clean, lint-free cloth.)
9. Place the heatsink on the processor then tighten two diagonal screws until snug, then the other two screws.
10. When all four screws are snug, tighten them all to secure the heatsink to the mainboard.

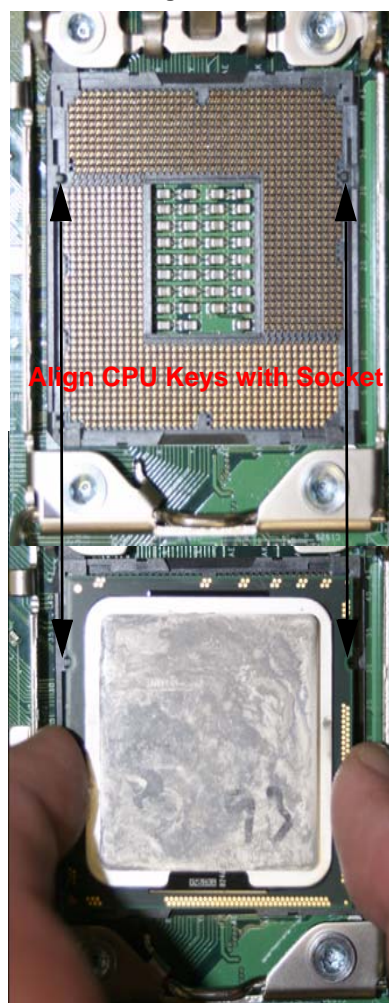


**NOTE:** Do not overtighten the screws as this may damage the processor or the heatsink.

11. Replace the cover on the blade unit and finish by installing the unit back into the blade enclosure.



**Figure 3-3. Installing a Processor in a Socket**



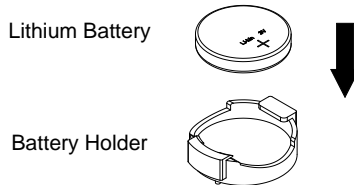
### 3-4 Onboard Battery Installation

A battery is included on the mainboard to supply certain volatile memory components with power when power has been removed from the blade module. If this battery dies, it must be replaced with an equivalent CR2032 Lithium 3V battery. Dispose of used batteries according to the manufacturer's instructions. See [Figure 3-4](#) for a diagram of installing a new onboard battery.



**WARNING:** There is a danger of explosion if the onboard battery is installed upside down, which reverses its polarities.

**Figure 3-4. Installing the Onboard CR2032 Battery**



### 3-5 Memory Installation

The mainboard of each blade unit must be populated with DIMMs (Dual In-line Memory Modules) to provide system memory. **The DIMMs should all be of the same size and speed and from the same manufacturer due to compatibility issues.** See details below on supported memory and our web site ([www.supermicro.com/products/superblade](http://www.supermicro.com/products/superblade)) for recommended memory.



**WARNING:** For all SBI-7426 series blades, **ONLY** VLP (Very low profile) memory can be used.

#### Populating Memory Slots

The mainboard of a SBI-7126TG blade module has 6 memory slots, depending upon the blade model. Both interleaved and non-interleaved memory are supported, so you may populate any number of DIMM slots.

Populating three slots at a time (DIMM1A + DIMM2A + DIMM3A, etc.) with memory modules of the same size and of the same type will result in dual-channel, interleaved memory, which is faster than single-channel, non-interleaved memory. See [Table 3-1](#) and [Figure 3-5](#) for details.

**For an interleaved configuration, memory modules of the same size and speed must be installed in pairs. You should not mix DIMMs of different sizes and speeds.**

Table 3-1. Populating Twelve Memory Slots for Interleaved Operation

Number of DIMMs	Processor 1						Processor 2					
	Channel 0		Channel 1		Channel 2		Channel 0		Channel 1		Channel 2	
2 DIMMs	1A						1A					
4 DIMMs	1A		2A				1A		2A			
6 DIMMs	1A		2A		3A		1A		2A		3A	

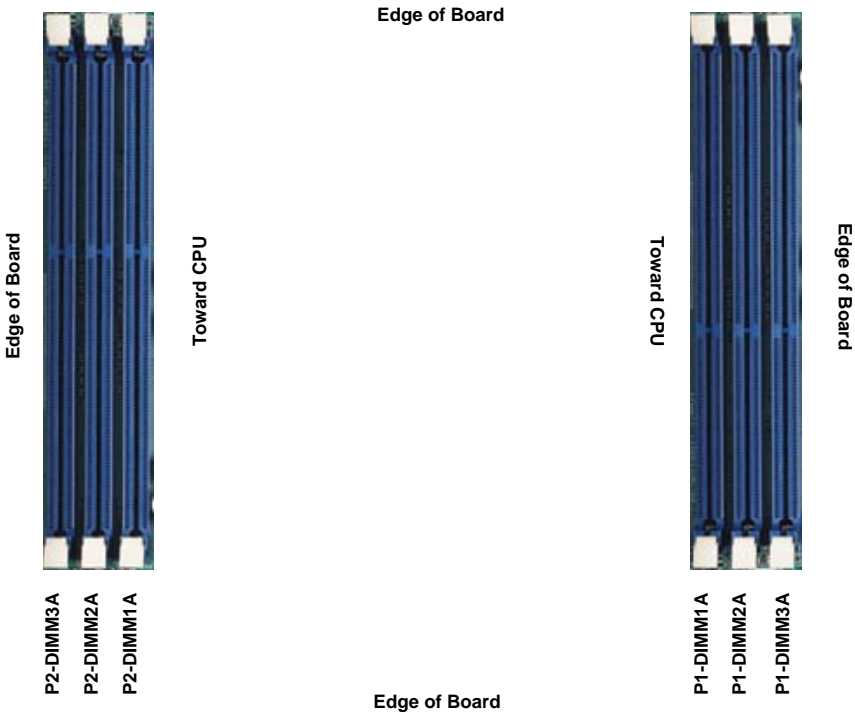


**NOTE:** The DIMM slot number specified in [Table 3-1](#) equals the DIMM slot to be populated. A blank space indicates that the DIMM slot should be left unpopulated.



**NOTE:** Though multiple DIMM memory module types and speeds may be supported, you need to use DIMM memory modules of the same speed and type.

Figure 3-5. 6-Slot DIMM Numbering



## DIMM Installation



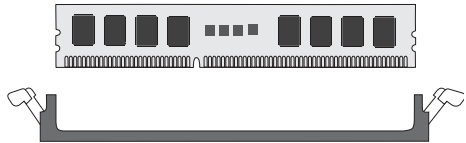
**WARNING:** Exercise extreme care when installing or removing DIMM modules to prevent any possible damage.

### Installing DIMM Memory Modules

1. Power down the blade module (see ["Powering Down a Blade Unit" on page 3-1](#)).
2. Remove the blade from the enclosure and the cover from the blade (see ["Removing/Replacing the Blade Cover" on page 3-2](#)).
3. Remove the air shroud that covers the DIMM slots.
4. Insert each DIMM vertically into its slot, starting with slots 1A and 2A. Pay attention to the notch along the bottom of the module to prevent inserting the DIMM incorrectly (see [Figure 3-6](#)).

**Figure 3-6. Installing a DIMM into a Memory Slot**

**To Install:** Insert module vertically and press down until it snaps into place. Pay attention to the bottom notch.



**To Remove:** Use your thumbs to gently push each release tab outward to free the DIMM from the slot.



5. Gently press down on the DIMM until it snaps into place in the slot. Repeat for all modules (see [Table 3-1](#) for installing DIMMs into the slots in the correct order).
6. Replace the air shroud and the blade cover and install the blade module back into the enclosure.
7. Power up the blade unit (see ["Powering Up a Blade Unit" on page 3-1](#)).

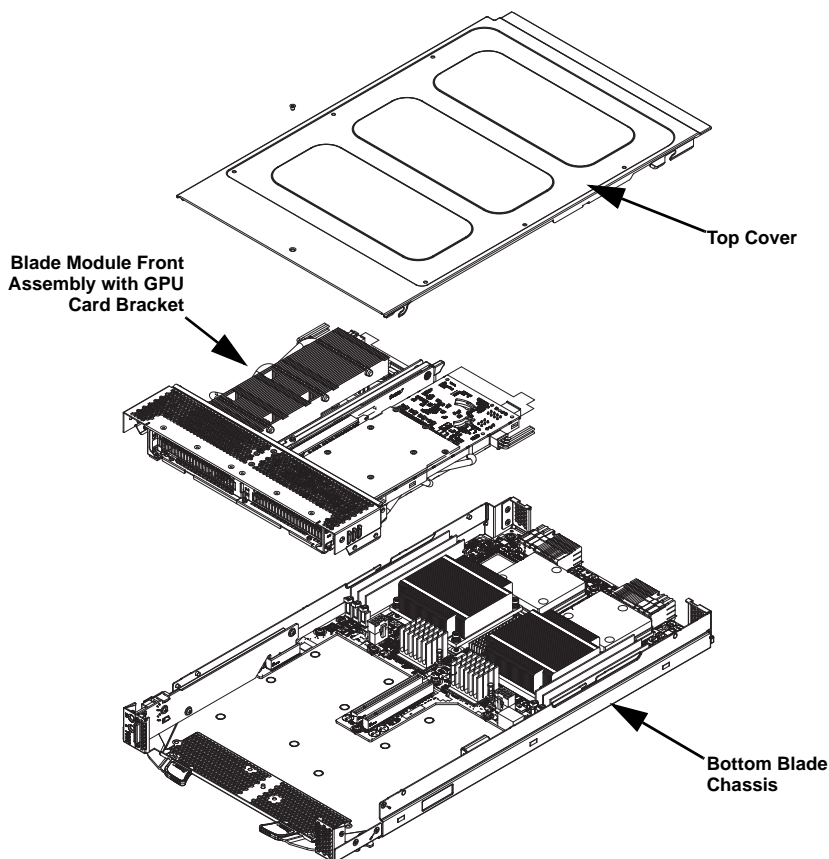
## Hard Drive Installation

The SBI-7126TG blade module does not mount a hard disk drive system. However you may install an optional SATA DOM module for file or operating system storage. See ["Hard Disk Drives" on page 4-8](#) for details.

## 3-6 Installing GPU Expansion Cards

If you need to install GPU extension cards into the SBI-7126TG blade module, then you must first remove the top cover, and then the Blade Module Front Assembly and GPU PCI-E Expansion Card Bracket first (see [Figure 3-7](#)). Use the procedure below to install GPU cards to the blade module.

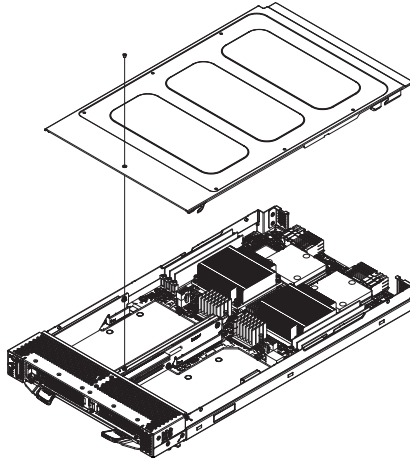
**Figure 3-7. Reassembling the Front Blade Assembly and Top Cover**



***Installing GPU Cards in the Blade Module***

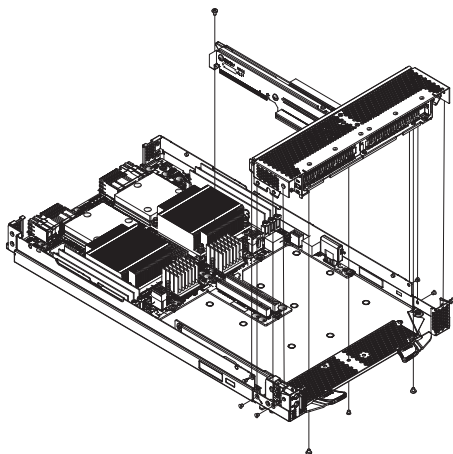
1. Unscrew the retaining screw on the top cover of the blade module and remove the top cover from the module (see [Figure 3-8](#)).

**Figure 3-8. Remove the Top Blade Cover**

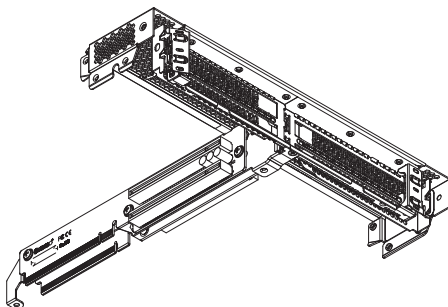


2. Remove the fourteen (14) retaining screws for the module's riser card and assembly front and lift off the front assembly (see [Figure 3-9](#) and [Figure 3-10](#)).

**Figure 3-9. Remove Front Blade Assembly**



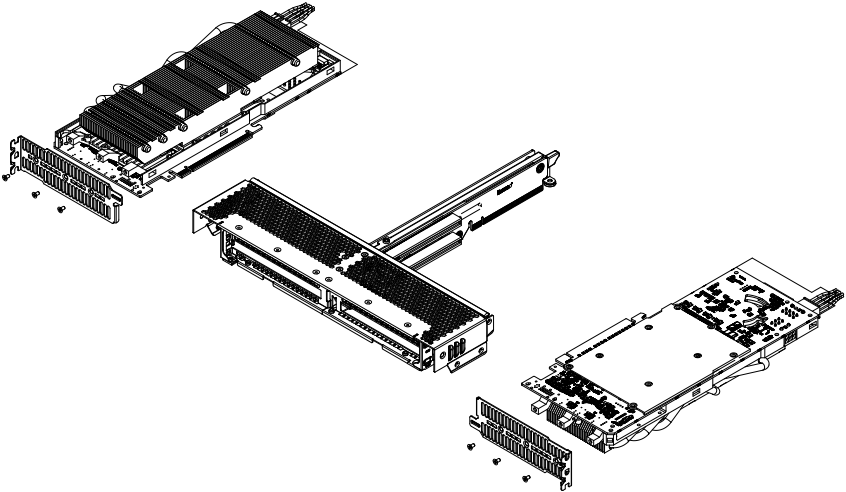
**Figure 3-10. Front Assembly**





3. Remove the two I/O plates from the front assembly by removing their retaining screws (four screws per GPU card) and detaching them.
4. Replace with the new front I/O plates (part # MCP-120-00056-0N) shipped in the blade accessory kit, with three front screws (see [Figure 3-11](#)).

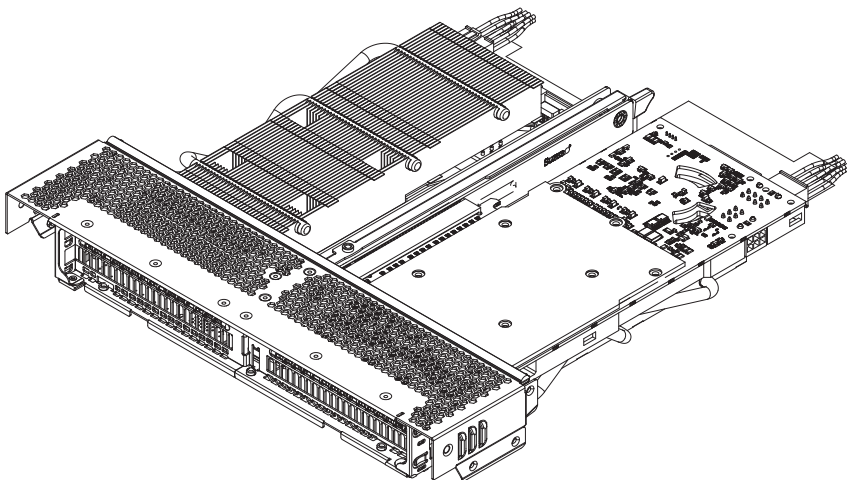
**Figure 3-11. Remove I/O Plates from GPU Front Assembly**



5. Slide the end of the front I/O plate into the reserved I/O slot while inserting the GPU card into the PCI-E slot.

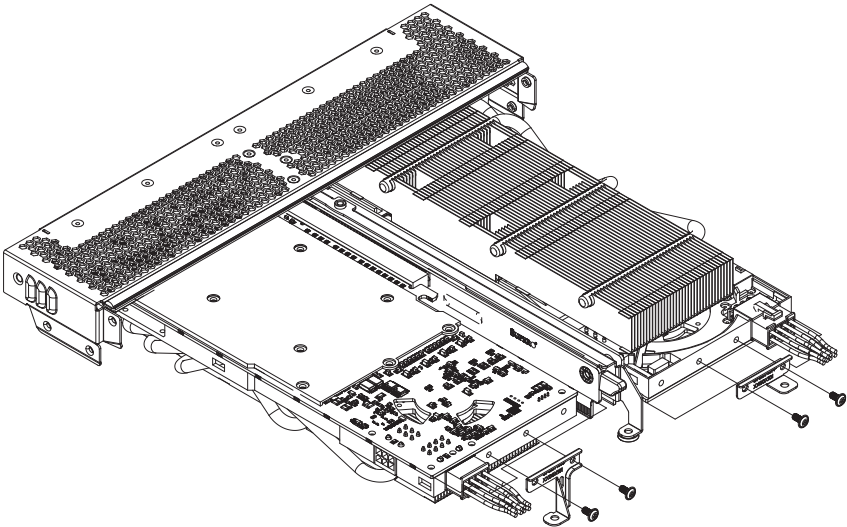
6. The completed assembly will have the GPU cards connected to the Supermicro I/O plates and the I/O cards connected to the riser card's PCI-E connectors (see [Figure 3-12](#) for completed assembly).

**Figure 3-12. Re-attach I/O Plates**



7. Attach retention brackets to the back of each of the GPU cards with two (2) screws each (see [Figure 3-13](#)). Also unscrew the retention bracket screws from the module serverboard for assembly installation into the bottom blade chassis.

**Figure 3-13. Attaching Retention Brackets to GPU Cards**



**NOTE:** The SBI-7126TG blade module uses a different size and type of brackets for each of the GPUs installed in the module. Use (part # MCP-640-00062-0N) for the right GPU and (part # MCP-640-00062-0N) for the left GPU (for the orientation as shown in [Figure 3-13](#) above).

8. (Optional) Install the SATA DOM module and its associated power cable before replacing the front assembly back on the blade module at this time. See ["Hard Disk Drives"](#) on [page 4-8](#) in [Chapter 4](#) for details.
9. Reattach the Riser Card/Front Blade Assembly back into the blade module. Resecure it with its fourteen (14) retaining screws and screw the two retention bracket screws to the GPUs and the serverboard (see [step 7](#)).
10. Install the two GPU card power cables for each GPU card. One end attaches to the GPU card's 8-pin socket, the other end attaches to the power socket on the blade module's serverboard.
11. Replace the top cover and secure it with its one retention screw (see [Figure 3-8](#)).

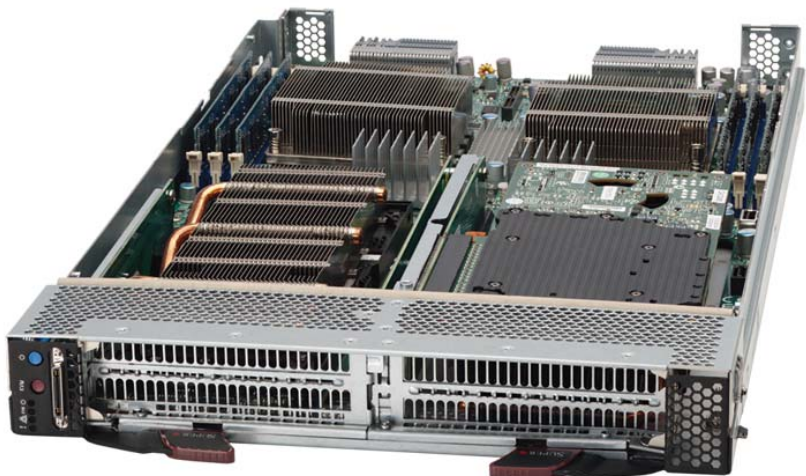
## Notes

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# Chapter 4

## Blade Module Features

Figure 4-1. SBI-7126TG Blade Module Front View



This chapter describes the SBI-7126TG blade module. Installation and maintenance should be performed by experienced technicians only.

See [Figure 4-1](#) for a front view of the blade unit and [Table 4-1](#) for its features.

Table 4-1. SBI-7126TG Blade Module Features

Feature	Description
Processors	Supports single or dual 1366-pin Intel Xeon 5500/5600 series processors
Memory	Supports up to 96 GB ECC Registered DDR3-1333/1066/800 SDRAM.
Expansion Slots	Two (2) PCI-E 2.0 x16 expansion slots
Ports	KVM port (1)
Features	Onboard Matrox G200eW graphics chip, IPMI 2.0, ATA/100, Plug and Play, APM 1.2, DMI 2.3, PCI 2.2, ACPI 1.0/2.0, SMBIOS 2.3, Real Time Clock, Watch Dog,
Power Consumption	Base Power Draw (~35W) / Power per CPU (60W/80W/95W) / Power per DIMM (typically 14.5W)

# 4-1 Control Panel

Each blade has a similar control panel (Figure 4-2) with power on/off button, a KVM connector, a KVM button and four LEDs on the top front of the unit. The numbers mentioned in Figure 4-2 are described in Table 4-2.

Figure 4-2. Blade Control Panel

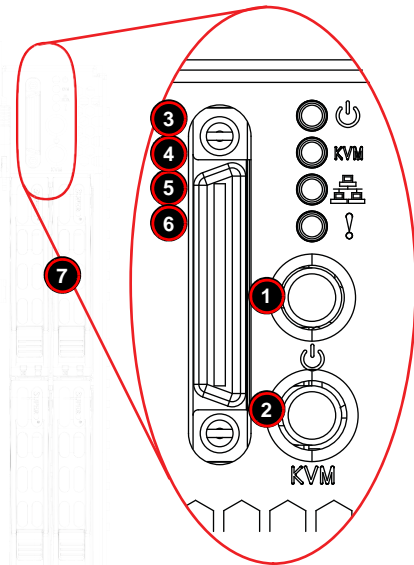


Table 4-2. Blade Control Panel

Item	Function	State	Description
1	Power Button	N/A	Turns blade module on and off
2	KVM Button	N/A	Initiates KVM function
3	Power LED	Green/Amber/ Red	See Table 4-3 for details.
4	KVM/UID LED	Blue/Flashing Blue	See Table 4-3 for details.
5	Network/IB LED	Flashing Green/ Flashing Orange	See Table 4-3 for details.
6	System Fault LED	Red	See Table 4-3 for details.
7	KVM Connector	N/A	Connector for SUV/KVM cable

## Power Button

Each blade has its own power button so that individual blade units within the enclosure may be turned on or off independently of the others. Press the power button (#1) to turn on the blade server. The power LED (#3) will turn green. To turn off, press and hold the power button for >4 seconds and the power LED will turn orange.

## KVM Button

KVM stands for Keyboard/Video/Mouse. With KVM, a user can control multiple blades with a single keyboard/video/mouse setup. Connect your keyboard, mouse and monitor to the USB and VGA connectors on the CMM module, then push the KVM button on the control panel of the blade module you wish to access.

## LED Indicators

Blade module LEDs are described below in [Table 4-3](#).

**Table 4-3. Blade Module LED Indicators**

LED	State	Description
Power LED	Green	Power On
	Amber	Standby
	Red	Power Failure <sup>a</sup>
KVM/UID LED (Blue)	Steady On	Indicates that KVM has been initialized on this blade module
	Flashing	Serves as a UID indicator (the UID function is activated with a management program)
Network LED (Green)	Flashing Green	Flashes on and off to indicate traffic (Tx and Rx data) on the LAN connection to this blade module.
Network LED (Orange)	Flashing Orange	Flashes on and off to indicate traffic over the InfiniBand module (when present in the system).
System Fault LED (Red)	Steady On	This LED illuminates red when a fatal error occurs. This may be the result of a memory error, a VGA error or any other fatal error that prevents the operating system from booting up.

- a. In the event of a power failure, the N+1 Redundant Power Supply (if included in your system's configuration) picks up the system load to provide uninterrupted operation. The failed power supply should be replaced with a new one as soon as possible.

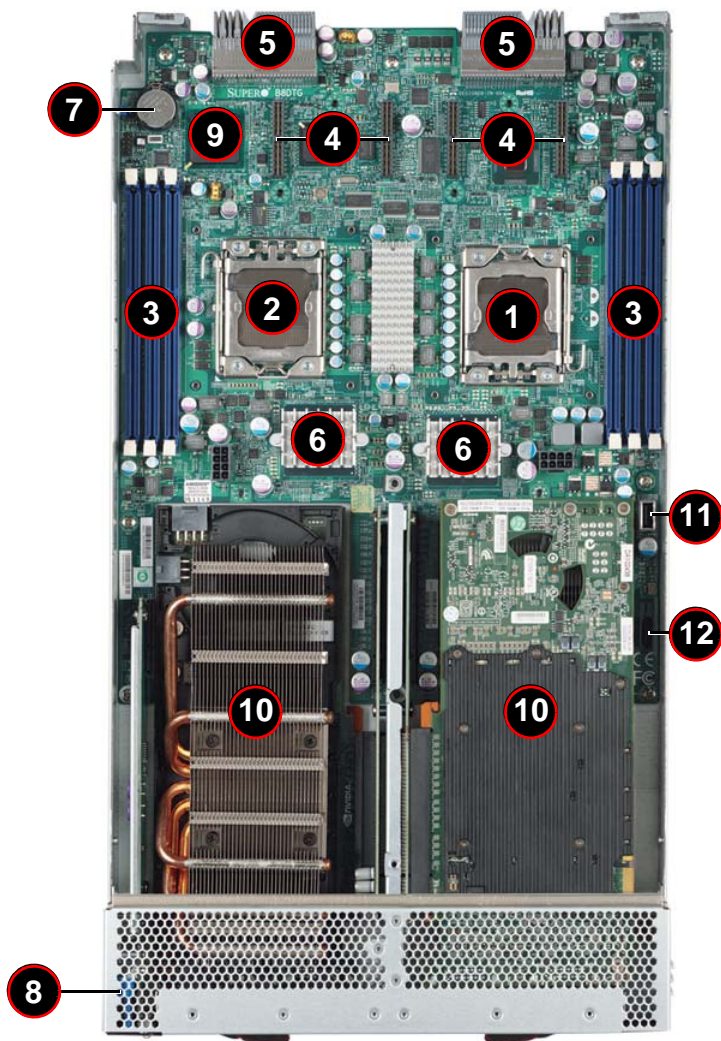
## KVM Connector

Alternatively, you may connect a KVM cable (CBL-0218L, with a keyboard/video/mouse attached) to the KVM connector (#7) of the blade you wish to access. To switch to another blade, disconnect the cable then reconnect it to the new blade. See the *Web-based Management Utility User's Manual* on your Superblade system CD-ROM for further details on using the KVM function remotely.

## 4-2 Mainboard

The mainboard of the SBI-7126TG blade unit is a proprietary design, which is based on the Intel IOH36D + ICH10 chipset. See [Figure 4-4](#) for a block diagram of this chipset, [Figure 4-3](#) for a view of the B8DTG Mainboard and [Figure 4-5](#) for an exploded view diagram of the blade unit.

Figure 4-3. B8DTG Mainboard







## Jumpers

The jumpers present on the mainboard are used by the manufacturer only; there are no jumpers used to configure the operation of the mainboard.

## CMOS Clear

JBT1 is used to clear CMOS and will also clear any passwords. JBT1 consists of two contact pads located near the BIOS chip.

### *Clearing CMOS*

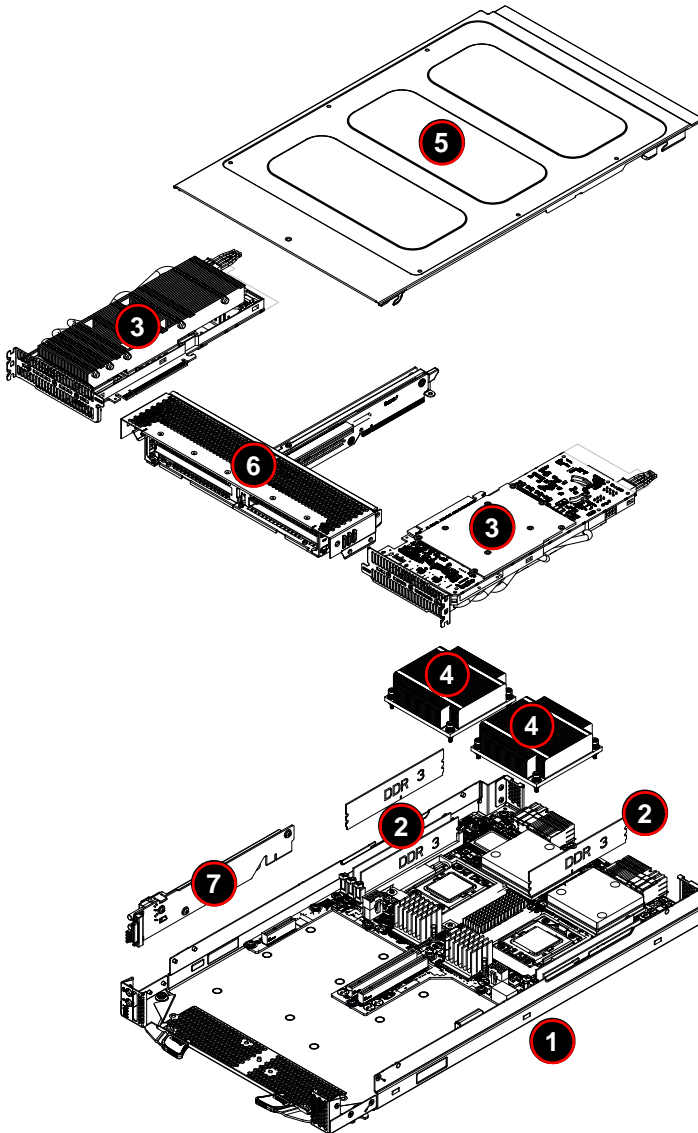
1. First power down the blade and remove it from the enclosure.
2. Remove the blade cover to access the mainboard (see ["Removing/Replacing the Blade Cover" on page 3-2](#) for further details). Short the CMOS pads with a metal object such as a small screwdriver.
3. Replace the cover, install the blade back into the enclosure and power it on.

## USB Port Connector

The SBI-7126TG blade module includes an internal USB Type A port that you may use for connecting USB devices to the blade module when the top blade cover is removed.

## 4-3 Blade Unit Components

Figure 4-5. Exploded View of SBI-7126TG Blade Module



Main components of the SBI-7126TG blade module are shown in [Figure 4-5](#) and described in [Table 4-5](#).

**Table 4-5. Main Components of SBI-7126TG Blade Module**

Item	Description
1	Blade Unit/Module
2	DIMMs (system memory)
3	PCI-E 2.0 x16 Expansion Card
4	CPU Heatsinks
5	Top Cover
6	PCI-E 2.0 x16 GPU Expansion Card Bracket and Blade Module Front Assembly
7	Adapter Card Bracket

## Memory Support

The SBI-7126TG blade module supports up to 96 GB of ECC Registered DDR3-1333/1066/800 SDRAM in twelve DIMM sockets. See [Section 3-5](#) for further details on mainboard memory installation.

## Hard Disk Drives

The SBI-7126TG blade module contains no hard disk drives or drive carriers. The SBI-7126TG blade module however does include a SATA port for an optional SATA DOM module which you may use instead as a storage device for files or an operating system. Please note that you must first disassemble the Blade Module Front Assembly and remove the GPU PCI-E Expansion Card Bracket in order to insert the SATA DOM module into its SATA port and attach its associated power cable. See [Section 3-6](#) in [Chapter 3](#) for details.

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# Chapter 5

## BIOS

### 5-1 Introduction

This chapter describes the BIOS for Intel SuperBlade modules. The Intel Blade modules use a AMI™ ROM BIOS that is stored in a flash chip. This BIOS can be easily upgraded using a floppy disk-based program.



**NOTE:** Due to periodic changes to the BIOS, some settings may have been added or deleted and might not yet be recorded in this manual. Please refer to the <http://www.supermicro.com/products/SuperBlade/module/> web site for further details on BIOS setup and the BIOS menus for your SuperBlade blade module.

#### System BIOS

BIOS stands for Basic Input Output System. The AMI BIOS flash chip stores the system parameters, types of disk drives, video displays, etc. in the CMOS. The CMOS memory requires very little electrical power. When the blade unit is turned off, a backup battery provides power to the BIOS flash chip, enabling it to retain system parameters. Each time the blade is powered on it is configured with the values stored in the BIOS ROM by the system BIOS, which gains control at boot up.

#### How To Change the Configuration Data

The CMOS information that determines the system parameters may be changed by entering the BIOS Setup utility. This Setup utility can be accessed by pressing the <DELETE> key at the appropriate time during system boot. (See "[Starting the Setup Utility](#)" below.)

#### Starting the Setup Utility

Normally, the only visible POST (Power-On Self-Test) routine is the memory test. As the memory is being tested, press the <DELETE> key to enter the main menu of the BIOS Setup utility. From the main menu, you can access the other setup screens, such as the Security and Power menus.



**WARNING:** To prevent possible boot failure, do not shut down or reset the system while updating the BIOS.

## 5-2 BIOS Updates

It may be necessary to update the BIOS used in the blade modules on occasion. However, it is recommended that you not update BIOS if you are not experiencing problems with a blade module.

Updated BIOS files are located on our web site([www.supermicro.com/products/superblade/](http://www.supermicro.com/products/superblade/)). Please check the current BIOS revision and make sure it is newer than your current BIOS before downloading.

There are several methods you may use to upgrade (flash) your BIOS. After downloading the appropriate BIOS file (in a zip file format), follow one of the methods described below to flash the new BIOS.

### Flashing BIOS

Use the procedures below to “Flash” your BIOS with a new update using the KVM dongle, USB ports on the CMM module or by use of a Floppy disk.

#### ***Flashing a BIOS using the KVM Dongle:***

For this method, you must use a KVM “dongle” cable (CBL-0218L, included with the system).

1. Copy the contents of the zip file to a bootable USB pen drive.
2. Connect the KVM dongle (CBL-0218L) to the KVM connector at the front of the blade you will be flashing the BIOS to.
3. Connect your bootable USB pen drive to one of the two USB slots on the KVM dongle.
4. Boot to the USB pen drive and go to the directory where you saved the contents of the zip file.
5. Type **flash filename.rom** (replace *filename.rom* by the actual ROM file name).

#### ***Flashing a BIOS using the USB Ports on the CMM:***

1. Copy the contents of the zip file to a bootable USB pen drive.
2. Connect your bootable USB pen drive to one of the two USB slots on the CMM (located on the back side of the enclosure).
3. Boot to the USB pen drive and go to the directory where you saved the contents of the zip file.
4. Type **flash filename.rom** (replace *filename.rom* by the actual ROM file name).

#### ***Flashing a BIOS using a Floppy Image File***

This method must be performed remotely.

1. Copy the image file from the zip file to your desktop.
2. Use the web browser or IPMIView to access your CMM remotely using its IP Address.

3. Go to the VIRTUAL MEDIA menu and select FLOPPY IMAGE UPLOAD.
4. BROWSE or OPEN to locate the \*.img file on your desktop and select it.
5. Press the UPLOAD button and wait a few seconds for the image to upload to the CMM.
6. Once the upload finishes, turn on the blade module and press <DEL> to enter the BIOS setup utility.
7. In the BOOT MENU, bring **USB LS120: PEPPCMM VIRTUAL DISC 1** to the top of the boot priority list.
8. Exit while saving the changes. The blade module will boot to the virtual media (floppy image) **A:\>**.
9. Type **flash filename.rom**.



**NOTE:** Replace *filename.rom* by the actual ROM file name (such as **B8DTE142.rom** for example) in the command.

## 5-3 Running Setup



**NOTE:** Default settings are in **bold** text unless otherwise noted.

The BIOS setup options described in this section are selected by choosing the appropriate text from the MAIN BIOS SETUP screen. All displayed text is described in this section, although the screen display is often all you need to understand how to set the options.

When you first power on the computer, the BIOS is immediately activated.

While the BIOS is in control, the Setup program can be activated in one of two ways:

1. By pressing <DELETE> immediately after turning the system on, or
2. When the message **Press the <Delete> key to enter Setup** appears briefly at the bottom of the screen during the POST, press the <DELETE> key to activate the main SETUP menu:

## 5-4 Main BIOS Setup

All Main Setup options are described in this section.

Use the UP/DOWN arrow keys to move among the different settings in each menu. Use the LEFT/RIGHT arrow keys to change the options for each setting.

Press the <ESC> key to exit the CMOS SETUP menu. The next section describes in detail how to navigate through the menus.

Items that use sub-menus are indicated with the ► icon. With the item highlighted, press the <ENTER> key to access the submenu.

Menu options found in the MAIN BIOS SETUP menu are described in [Table 5-1](#).

**Table 5-1. Main BIOS Setup Menu Options**

Menu Option	Description
System Time	To set the system date and time, key in the correct information in the appropriate fields. Then press the <Enter> key to save the data.
System Date	Using the arrow keys, highlight the month, day and year fields, and enter the correct data for the system date. Press the <Enter> key to save the data.
BIOS Date	The BIOS Date field displays the date when this version of the BIOS was built. This option is not configurable.

## 5-5 Advanced Setup

Choose **Advanced** from the BIOS Setup Utility main menu with the arrow keys to display the ADVANCED SETUP menu. The items with a triangle beside them are sub-menus that can be accessed by highlighting the item and pressing <ENTER>. Options for PIR settings are displayed by highlighting the setting option using the arrow keys and pressing <ENTER>.

[Table 5-2](#) describes all sub-menus found in the ADVANCED SETUP menu.

**Table 5-2. Advanced Setup Menu Options**

Sub-menu	Description
►Boot Feature	See <a href="#">Table 5-3</a> for a description of BIOS setup menu options in this sub-menu.
►Processor & Clock Configuration	See <a href="#">Table 5-4</a> for a description of BIOS setup menu options in this sub-menu.
►Advanced Chipset Control	See <a href="#">Table 5-5</a> for a description of BIOS setup menu options in this sub-menu.
►IDE/SATA Configuration	See <a href="#">Table 5-6</a> for a description of BIOS setup menu options in this sub-menu.
►PCI/PnP Configuration	See <a href="#">Table 5-7</a> for a description of BIOS setup menu options in this sub-menu.



Table 5-2. Advanced Setup Menu Options (Continued)

Sub-menu	Description
► SuperIO Device Configuration	See <a href="#">Table 5-8</a> for a description of BIOS setup menu options in this sub-menu.
► Remote Access Configuration	See <a href="#">Table 5-9</a> for a description of BIOS setup menu options in this sub-menu.
► System Health Monitor	See <a href="#">Table 5-10</a> for a description of BIOS setup menu options in this sub-menu.
► ACPI Configuration	See <a href="#">Table 5-11</a> for a description of BIOS setup menu options in this sub-menu.
► IPMI Configuration	See <a href="#">Table 5-12</a> for a description of BIOS setup menu options in this sub-menu.
► Event Log Configuration	See <a href="#">Table 5-13</a> for a description of BIOS setup menu options in this sub-menu.

Table 5-3. Boot Feature Sub-menu

Menu Option	Description
Quick Boot	This setting allows the BIOS to skip certain tests while booting. This will decrease the time needed to boot the system. Options include <b>Enabled</b> and <b>Disabled</b> .
Quiet Boot	When <b>Disabled</b> the BIOS displays normal POST messages. When <b>Enabled</b> the BIOS displays an OEM Logo instead of POST messages.
AddOn ROM Display Mode	This setting sets the display mode for Option ROM. Options include <b>Force BIOS</b> or <b>Keep Current</b> .
Bootup NUM-Lock	This setting selects the Power-On state for Numlock. Options include <b>On</b> or <b>Off</b> .
Wait for 'F1' If Error	When enabled, the system will wait for the F1 key to be pressed if an error occurs. Options are <b>Enabled</b> or <b>Disabled</b> .
Hit 'DEL' Message Display	This setting displays "Press DEL to run Setup" in POST. Options include <b>Enabled</b> or <b>Disabled</b> .
Interrupt 19 Capture	When enabled this setting allows option ROMs to trap Interrupt 19. Options include <b>Enabled</b> or <b>Disabled</b> .
Watch Dog Function	This setting allows the system to restart when it is not active more than 5 minutes. Option include <b>Enabled</b> and <b>Disabled</b> .
Power Button Function	This setting specifies the power button's function when pressed. Option include 4 Seconds Override or <b>Instant Off</b> .
Restore on AC Power Loss	This setting specifies what the system will do when power is restored after an AC power loss to the system. Options include <b>Power Off</b> (system power remains off after power loss), <b>Power-On</b> (system power turns on after power loss) or <b>Last State</b> (allows the system to resume its last state before the power loss).

Table 5-4. Processor &amp; Clock Options Sub-menu

Menu Option	Description
CPU Ratio	This setting allows you to set the CPU Ratio to either <b>Auto</b> (Maximum Ratio) or Manual (Manual Ratio).
Clock Spread Spectrum	This setting enables or disables spread spectrum modulation. Options are <b>Enabled</b> and Disabled.
Hardware Prefetcher	If set to <b>Enabled</b> , the hardware prefetcher will prefetch streams of data and instructions from the main memory to the L2 cache in the forward or backward manner to improve CPU performance. Options are <b>Enabled</b> and Disabled. For UP platforms leave it enabled. for DP/MP servers, this setting may be used to tune performance to the specific application.
Adjacent Cache Line Prefetch	The CPU fetches the cache line for 64-bytes if this option is set to Disabled. The CPU fetches both cache lines for 128-bytes as comprised if <b>Enabled</b> . For UP platforms leave it enabled. for DP/MP servers, this setting may be used to tune performance to the specific application.
DCU Prefetcher	For UP platforms, leave this option <b>Enabled</b> . For DP/MP servers, this option may be used to tune performance to the specific application. Options are <b>Enabled</b> or Disabled.
Data Reuse Optimization	For UP platforms, leave this option <b>Enabled</b> . For DP/MP servers, this option may be used to tune performance to the specific application. Options are <b>Enabled</b> or Disabled.
MPS and ACPI MDAT Ordering	This setting allows you to specify MPS and ACPI MADT ordering. Options include <b>Modern Ordering</b> (for Windows XP or later OSes) or Legacy Ordering (for Windows 2000 or earlier OSes).
Machine Check Feature	This setting enables or disables the machine check feature in the system. Options include <b>Enabled</b> or Disabled.
Intel® Virtualization Tech	Select <b>Enabled</b> to use this Virtualization Technology feature to allow one platform to run multiple operating systems and applications in independent partitions, creating multiple “virtual” systems in one physical computer system. The options are <b>Enabled</b> and Disabled. Please refer to the <a href="#">Intel website</a> for further detailed information. <b>NOTE:</b> A full reset of the system is required when you change this setting.
Execute-Disable Bit Capability	The Execute Disable Bit allows the processor to designate areas in the system memory where an application code can be executed, and where it cannot. This prevents a worm or virus from flooding illegal codes into the system to overwhelm the processor or damage the system during an attack. The default setting is <b>Enabled</b> . Please refer to the <a href="#">Intel</a> and <a href="#">Microsoft</a> websites for more information.
Intel(R) AES-NI	This setting allows you to Enable or <b>Disable</b> Intel AES-NI in the system.
Simultaneous Multi-Threading	When Disabled, only one thread per enabled core is enabled. Options include <b>Enabled</b> or Disabled.
Active Processor Cores	This setting sets the number of cores to enable in each processor package. Options include <b>All</b> , 1, 2, 3, 4 and 5.
Intel (R) EIST Technology	This setting allows the processor to dynamically transition speed and voltage state in C0. Options include <b>Enabled</b> or Disabled.

Table 5-4. Processor &amp; Clock Options Sub-menu (Continued)

Menu Option	Description
Intel (R) TurboMode Tech	This setting allows the processor cores to run faster than the marked frequency in specific conditions. Options are either <b>Enabled</b> or Disabled.
C1E Support	This setting should be enabled in order to enable or disable the "Enhanced Halt State" in the system. Options include <b>Enabled</b> or Disabled.
Intel (R) C-STATE tech	This setting allows the processor to set the idle state for power savings. Options include <b>Enabled</b> or Disabled.
C State package limit setting	The selected option for this setting will program into the C State package limit register. Options include <b>Auto</b> , C1, C3, C6 and C7.
C1 Auto Demotion	When enabled, the CPU will conditionally demote C3/C6/C7 requests to C1 based on uncore auto-demote information. Options include <b>Enabled</b> and Disabled.
C3 Auto Demotion	When enabled, the CPU will conditionally demote C6/C7 requests to C3 based on uncore auto-demote information. Options include <b>Enabled</b> and Disabled.
ACPI T State	Enabling this setting reports processor throttling in ACPI. Options include Enabled or <b>Disabled</b> .

Table 5-5. Advanced Chipset Control Sub-menu

Menu Option	Description
► CPU Bridge Configuration	This sub-menu configures CPU Bridge features as well as showing Configuration information.
CPU Bridge Information	The top of this sub-menu contains non-configurable CPU Bridge Information including CPU Revision, Current QPI Frequency, Current CPU1 Memory Frequency, Current CPU2 Memory Frequency, Memory Reference Code Revision and QPI Reference Code Revision
Request Transaction ID	This setting allows you to specify options for the Request Transaction ID. Each QPI transaction initiated by an agent has an associated RTID. Options include Balanced or <b>IO Bias</b> .
QPI Links Speed	This setting allows you to transition QPI links to <b>Full-Speed</b> or leave them in Slow-Mode for the QPI data transfer speed.
QPI Frequency	This setting selects the desired QPI frequency. Option include <b>Auto</b> , 4.800 GT, 5.866 GT and 6.400 GT.
QPI L0s and L1	This setting enables the QPI power state to low power with L0s and L1 automatically selected by the mainboard. The options are <b>Disabled</b> and Enabled.
Memory Frequency	This feature forces a DDR3 frequency slower than what the system has detected. The available options are <b>Auto</b> , DDR-800, Force DDR-1066, Force DDR-1333 and Force SPD.

Table 5-5. Advanced Chipset Control Sub-menu (Continued)

Menu Option	Description
Memory Mode	<p>This setting sets the system memory mode. Options include the following:</p> <ul style="list-style-type: none"> <li>• <b>Independent</b> (default) – All DIMMs are available to the operating system.</li> <li>• Channel Mirror – The mainboard maintains two identical copies of all data in memory for redundancy.</li> <li>• Lockstep – The mainboard uses two areas of memory to run the same set of operations in parallel.</li> <li>• Sparing – A preset threshold of correctable errors is used to trigger fail-over. The spare memory is put online and used as active memory in place of the failed memory.</li> </ul>
Demand Scrubbing	This setting is a memory error-correction scheme whereby the processor writes corrected data back into the memory block from where it was read by the processor. The options include Enabled or <b>Disabled</b> .
Patrol Scrubbing	This setting is a memory error-correction scheme that works in the background looking for and correcting resident errors. The options are <b>Enabled</b> or Disabled.
Channel Interleave	This setting allows you to set the channel interleave in your system. Options include 1 Way, 2 Way, 3 Way, 4 Way and <b>6 Way</b> .
Bank Interleave	This setting allows you to set Bank Interleave in your system. Options include 1 Way, 2 Way and <b>4 Way</b> .
Throttling – Closed Loop	This setting allows BIOS to program Closed Loop Throttling for memory components. Options include <b>Enabled</b> or Disabled.
►North Bridge Configuration	This sub-menu configures North Bridge features and shows configuration information.
Intel I/OAT	This setting enables or disables the Crystal Beach DMA configuration. Options include <b>Enabled</b> or Disabled.
DCA Technology	This setting enables or disables the Crystal Beach DCA configuration. Options include <b>Enabled</b> or Disabled.
DCA Prefetch Delay	This setting programs the time delay in the bus clocks from snoop to prefetch. Options include 8, 16, 24, <b>32</b> , 40, 48, 56, 64, 72, 80, 88, 96, 104, 112 and 120.
Intel VT-d	This setting allows you to Enable or <b>Disable</b> Intel VT-d in your system.
Active State Power Management	This setting allows you to Enable or <b>Disable</b> PCI Express L0s and L1 link power states.
IOH PCIe Max Payload Size	This setting allows you to specify IOH PCIe Max Payload size as either 128B or <b>256B</b> .
►South Bridge Configuration	This sub-menu configures South Bridge features.
USB Functions	This setting allows you to enable USB functions in your system. Options include <b>Enabled</b> or Disabled.

Table 5-5. Advanced Chipset Control Sub-menu (Continued)

Menu Option	Description
USB 2.0 Controller	This setting is only active if USB Functions above it is Disabled. This setting allows you to <b>Enable</b> or Disable the USB 2.0 controller in your system.
Legacy USB Support	This option allows you to enable the use of Legacy USB devices. If this option is set to Auto, legacy USB support will be automatically enabled if a legacy USB device is installed on the mainboard, and disabled if no USB devices are connected. The options include Disabled, Enabled and <b>Auto</b> .

Table 5-6. IDE/SATA Configuration Sub-menu

Menu Option	Description
SATA#1 Configuration	If Compatible is selected, this sets SATA#1 to legacy compatibility mode. Selecting Enhanced sets SATA#1 to native SATA mode. The options are Disabled, Compatible and <b>Enhanced</b> .
Configure SATA#1 as	This setting allows you to select the drive type for SATA#1. The options are <b>IDE</b> , and AHCI.
IDE Detect Time Out (Sec)	This setting allows you to select the time out value for detecting ATA/ATAPI devices in your system. Options include values for 0, 5, 10, 15, 20, 25, 30 and <b>35</b> seconds.
►Primary/Secondary/Third/Fourth IDE Master/Slave	This submenu displays the type of drive and contains IDE Master/Slave settings.
Type	This setting specifies the type of device connected to the system. Options include <b>Auto</b> , CD/DVD and ARMD.
LBA/Large Mode	This setting allows you to enable/disable LBA Mode. Options include Disabled and <b>Auto</b> , which enables LBA mode if the device supports it and the device is not already formatted with LBA mode disabled.
Block (Multi-Sector Transfer)	Block mode boosts IDE drive performance by increasing the amount of data transferred. Only 512 bytes of data can be transferred per interrupt if block mode is not used. Block mode allows transfers of up to 64 KB per interrupt. Select "Disabled" to allow the data to be transferred from and to the device one sector at a time. Select "Auto" to allows the data transfer from and to the device occur multiple sectors at a time if the device supports it. The options are <b>Auto</b> and Disabled.
PIO Mode	PIO (Programmable I/O) mode programs timing cycles between the IDE drive and the programmable IDE controller. As the PIO mode increases, the cycle time decreases. The options are <b>Auto</b> , 0, 1, 2, 3, and 4. Select Auto to allow BIOS to auto detect the PIO mode. Use this value if the IDE disk drive support cannot be determined. Select 0 to allow BIOS to use PIO mode 0, which has a data transfer rate of 3.3 MBs. Select 1 to allow BIOS to use PIO mode 1, which has a data transfer rate of 5.2 MBs. Select 2 to allow BIOS to use PIO mode 2, which has a data transfer rate of 8.3 MBs. Select 3 to allow BIOS to use PIO mode 3, which has a data transfer rate of 11.1 MBs. Select 4 to allow BIOS to use PIO mode 4, which has a data transfer rate of 16.6 MBs. This setting generally works with all hard disk drives manufactured after 1999. For other disk drives, such as IDE CD-ROM drives, check the specifications of the drive.
DMA Mode	Selects the DMA Mode. The only option is <b>Auto</b>

Table 5-6. IDE/SATA Configuration Sub-menu (Continued)

Menu Option	Description
S.M.A.R.T.	Self-Monitoring Analysis and Reporting Technology (SMART) can help predict impending drive failures. Select "Auto" to allow BIOS to auto detect hard disk drive support. Select "Disabled" to prevent AMI BIOS from using the S.M.A.R.T. Select "Enabled" to allow AMI BIOS to use the S.M.A.R.T. to support hard drive disk. The options are Disabled, Enabled, and <b>Auto</b> .
32-bit Data Transfer	Select "Enabled" to activate the function of 32-Bit data transfer. Select "Disabled" to deactivate the function. The options are <b>Enabled</b> and Disabled.

Table 5-7. PCI/PnP Configuration Sub-menu

Menu Option	Description
Clear NVRAM	This setting allows you to clear NVRAM during a system boot. The options are <b>No</b> and Yes.
Plug & Play O/S	Selecting Yes allows the OS to configure Plug & Play devices. Select <b>No</b> to allow the AMI BIOS to configure all devices in your system. <b>NOTE:</b> This is not required for system boot (select <b>No</b> ) if your system has an OS that supports Plug & Play.
PCI Latency Timer	This sets the latency timer of each PCI device installed on a PCI bus. For example, select <b>64</b> to set the PCI latency to 64 PCI clock cycles. The options are 32, <b>64</b> , 96, 128, 160, 192, 224 and 248.
PCI Cache Line Size	This sets the size of the PCI Cache Dwords/Line. The options are 16, 32, <b>64</b> and 128.
PCI IDE BusMaster	When enabled, the BIOS uses PCI bus mastering for reading/writing to IDE devices. The options are DISABLED and <b>Enabled</b> .
SR-IOV Support	This setting Enables or <b>Disables</b> SR-IOV Support.
Onboard LAN Option ROM Select	This setting allows you to select the option for onboard LAN option ROM to either <b>PXE</b> or iSCSI. Note: If iSCSI is selected, LAN1 Option ROM select will cover all other Oboard LANs.
Load Onboard LAN 1 Option ROM	This setting <b>Enables</b> or Disables onboard option ROM for LAN1.
Load Onboard LAN 2 Option ROM	This setting <b>Enables</b> or Disables onboard option ROM for LAN2.
Boots Graphic Adapter Priority	This setting allows you to select which graphics controller to use as the primary boot device. Options include Offboard VGA, <b>Onboard VGA</b> or PCI-E Slot 7.

Table 5-8. SuperIO Device Configuration Sub-menu

Menu Option	Description
Serial Port1 Address	This option specifies the base I/O port address and the Interrupt Request address of Serial Port 1. Select Disabled to prevent the serial port from accessing any system resources. When this option is Disabled, the serial port physically becomes unavailable. Select <b>3F8/IRQ4</b> to allow the serial port to use 3F8 as its I/O port address and IRQ 4 for its interrupt address. Options are Disabled, <b>3F8/IRQ4</b> , , 2F8/IRQ3, 3E8/IRQ4 and 2E8/IRQ3.
Serial Port2 Address	Same as above, but the options are Disabled, 3F8/IRQ4, <b>2F8/IRQ3</b> , 3E8/IRQ4 and 2E8/IRQ3.
Serial Port2 Attribute	This setting allows you to set COM2 as a normal serial port or as a virtual COM port for SOL. Options are <b>SOL</b> or COM.

Table 5-9. Remote Access Configuration Sub-menu

Menu Option	Description
Remote Access	This option allows you to select your remote access type. Options include <b>Enabled</b> and Disabled.
Serial Port Number	This option allows you to select a serial port for console redirection if the selected port is enabled. Options include COM1 and <b>COM2*</b> . Note: SOL will be enabled on this port when a BMC is present on the motherboard.
Serial Port Mode	Use this option to select the serial port mode setting. Options include <b>115200 8,n,1/ 57600 8,n,1/ 38400 8,n,1 / 19200 8,n,1 / 09600 8,n,1</b>
Flow Control	This option allows you to select Flow Control for redirection. Options include <b>None</b> , Hardware or Software.
Redirection After BIOS POST	<p>This option allows you to specify redirection after BIOS POST. Options include the following:</p> <ul style="list-style-type: none"> <li>• Disable – Turns off the redirection after POST</li> <li>• Boot Loader – Redirection is active during Boot Loader</li> <li>• <b>Always</b> – Redirection is always active.</li> </ul> <p><b>NOTE:</b> Some operating systems may not work if set to <b>Always</b>.</p>
Terminal Type	This option allows you to select the Terminal Type for your system. Options include ANSI, <b>VT100</b> and VT-UTF8.
VT-UTF8 Combo Key Support	Use this option to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. Options include <b>Enabled</b> and Disabled.
Sredir Memory Display Delay	This option gives the delay in seconds to display memory information. Options include <b>No Delay</b> , Delay in 1 Sec, Delay in 2 Sec and Delay in 4 Sec.

**Table 5-10. System Health Monitor Sub-menu**

Menu Option	Description
System Health Monitor Information	<p>This sub-menu contains system health information for the following:</p> <ul style="list-style-type: none"><li>• CPU1 Temperature</li><li>• CPU2 Temperature</li><li>• System Temperature</li><li>• CPU1 Vcore</li><li>• CPU2 Vcore</li><li>• 3.3V Vcc (V)</li><li>• +5Vin</li><li>• +12V Vcc (V)</li><li>• CPU1 DDR3 VTT</li><li>• CPU2 DDR3 VTT</li><li>• 5V Standby</li><li>• Battery Voltage</li></ul>

**Table 5-11. ACPI Configuration Sub-menu**

Menu Option	Description
ACPI Aware O/S	This setting Enables or Disables ACPI support for the Operating System. Options include No (if OS does not support ACPI) or <b>Yes</b> (if OS supports ACPI).
ACPI Version Features	This setting allows you to enable RSDP pointers to 64-bit fixed system description tables. Options include ACPI v1.0, <b>ACPI v2.0</b> and ACPI v3.0.
ACPI APIC Support	This setting allows you to include an ACPI APIC table pointer in the Root System Description Table (RSDT) pointer list. Options include <b>Enabled</b> and Disabled.
APIC ACPI SCI IRQ	This option allows you to enable the APIC ACPI SCI IRQ in your system. Options include Enabled and <b>Disabled</b> .
Headless Mode	This option allows you to enable your system to function without a keyboard, monitor or mouse attached. Options include <b>Disabled</b> or Enabled.
High Performance Event Timer	Select Enabled to activate the High Performance Event Timer (HPET). This produces periodic interrupts at a much higher frequency than a Real-time Clock (RTC) does in synchronizing multimedia streams, providing smooth playback and reducing the dependency on other timestamp calculation devices, such as an x86 RDTSC Instruction embedded in the CPU. The HPET is used to replace the 8254 Programmable Interval Timer. Options include Enabled and <b>Disabled</b> .
NUMA Support	When <b>Enabled</b> this setting allows the system to efficiently execute software for a NUMA aware OS. When Disabled, this setting allows better memory access performance for a non-NUMA OS.
WHEA Support	This setting allows you to <b>Enable</b> or Disable Windows Hardware Error Architecture.



Table 5-12. IPMI Configuration Sub-menu

Menu Option	Description
BMC Information	The Baseboard Management Controller (BMC) manages the interface between the system management software and platform hardware. The top of this menu contains non-configurable information that shows the IPMI Firmware Revision and the status of the BMC micro controller.
►View BMC System Event Log	<p>This submenu when selected displays the BMC System Event Log (SEL). This log shows the total number of entries of BMC system events. To view an event, select an entry number and press &lt;ENTER&gt; to display the information as shown in the screen.</p> <p>This screen contains the following information:</p> <ul style="list-style-type: none"> <li>• Total Number of Entries</li> <li>• SEL Entry Number</li> <li>• SEL Record ID</li> <li>• SEL Record Type</li> <li>• Event Timestamp</li> <li>• Generator ID</li> <li>• Event Message Format Version</li> <li>• Event Sensor Type</li> <li>• Event Sensor Number</li> <li>• Event Dir Type</li> <li>• Event Data</li> </ul>
Clear BMC System Event Log	<p>This setting allows you to clear the BMC system log. Select Cancel to keep the BMC system log and Ok with the &lt;ENTER&gt; key to clear the BMC system log.</p> <p><b>WARNING:</b> Any cleared information is unrecoverable. Make absolutely sure that you no longer need any data stored in the log before clearing the BMC Event Log.</p>
►Set LAN Configuration	This sub-menu is used to configure the IPMI LAN adapter with a network address.
LAN Configuration Information	This static display shows the Channel Number for the SET LAN Config command and the Channel Number Status for the channel number. It will either display "Channel number is OK" or "WRONG CHANNEL NUMBER".
IP Address Source	Use this setting for selecting the IP Address source. Options include Static or <b>DHCP</b> .
►IP Address	This sub-menu contains static displays and options for specifying the IP address for your system. This should be in decimal and in dotted quad form (such as 192.168.10.253). The value of each three-digit number separated by dots should not exceed 255.
Parameter Selector	This static display shows the parameter of your IP Address configuration.
IP Address Source	Use this setting for selecting the IP Address source. Options include <b>STATIC</b> or <b>DHCP</b> .
IP Address	The BIOS will automatically enter the IP address of your system; however it may be over-ridden. IP addresses are 6 two-digit hexadecimal numbers (base 16, 0-9, A, B, C, D, E and F) separated by dots (such as 00.30.48.D0.D4.60).

**Table 5-12. IPMI Configuration Sub-menu (Continued)**

Menu Option	Description
Current IP Address in BMC	This static display shows the current IP address used for your IPMI connection.
► Subnet Mask	Subnet masks tell the network which subnet this system belongs. The value of each three-digit number separated by dots should not exceed 255.
Parameter Selector	This static display shows the parameter of your Subnet Masks configuration.
Subnet Mask	This static display shows the current Subnet Mask setting for your IPMI connection.
Current Subnet Mask in BMC	This static display shows the current Subnet Mask used for your IPMI connection.
► Gateway Address	Enter the Gateway address for your system using this sub-menu. This should be in decimal and dotted quad form (such as 192.168.10.253). The value of each three-digit number separated by dots should not exceed 255.
Parameter Selector	This static display shows the parameter of your Gateway Address configuration.
Gateway Address	The BIOS will automatically enter the Gateway address of your system; however it may be over-ridden. MAC addresses are 6 two-digit hexadecimal numbers (base 16, 0-9, A, B, C, D, E and F) separated by dots (such as 00.30.48.D0.D4.60).
Current IP Address in BMC	This static display shows the current Gateway address used for your IPMI connection.
► MAC Address	Enter the MAC address for your system using this sub-menu. This should be in decimal and dotted quad form (such as 192.168.10.253). The value of each three-digit number separated by dots should not exceed 255.
Parameter Selector	This static display shows the parameter of your MAC Address configuration.
MAC Address	The BIOS will automatically enter the MAC address of your system; however it may be over-ridden. MAC addresses are 6 two-digit hexadecimal numbers (base 16, 0-9, A, B, C, D, E and F) separated by dots (such as 00.30.48.D0.D4.60).
Current MAC Address in BMC	This static display shows the current MAC address used for your IPMI connection.
BMC Watch Dog Timer Action	This setting allows the BMC to reset or power down the system if the operating system crashes or hangs. Options include <b>Disabled</b> , Reset System, Power Down or Power Cycle.

**Table 5-13. Event Log Configuration Sub-menu**

Menu Option	Description
View Event Log	This setting allows you to view all unread events on the Event Log.
Mark All Events as Read	This setting marks all events as read. Options are OK and <b>Cancel</b> when selected.

Table 5-13. Event Log Configuration Sub-menu (Continued)

Menu Option	Description
Clear Event Log	This setting clears the Event Log memory of all messages. Options are <b>OK</b> and <b>Cancel</b> .
PCIe Error Log	This setting allows you to specify whether or not you wish to record a PCIe Error Log. Options include <b>Yes</b> or <b>No</b> .
Memory ECC Error Log	This setting allows you to specify whether or not you wish to record a Memory ECC Error Log. Options include <b>Yes</b> or <b>No</b> .

## 5-6 Security

Choose **Security** from the AMI BIOS Setup Utility main menu with the arrow keys to bring up the **SECURITY SETUP** menu. Security setting options are displayed by highlighting the setting using the arrow keys and pressing <ENTER>. All Security BIOS settings are described in [Table 5-14](#) below.

Table 5-14. Security Menu Options

Menu Option	Description
Supervisor Password	This displays whether a supervisor password has been entered for the system. "Not Installed" means that a Supervisor password has not been used.
User Password	This displays whether a user password has been entered for the system. "Not Installed" means that a User password has not been used.
Change Supervisor Password	Select this setting and hit the <ENTER> key to access the sub-menu and then type in the Supervisor's password in the dialogue box to set or change the Supervisor password, which allows access to the System's BIOS.
Change User Password	<p>Select this setting and hit the &lt;ENTER&gt; key to access a sub-menu with the following options;</p> <ul style="list-style-type: none"> <li>• <b>Full Access</b> (default) – This grants full user read and write access to the BIOS Setup Utility.</li> <li>• <b>VIEW ONLY</b> – This allows access to the BIOS Setup Utility, but does not allow the fields to be changed.</li> <li>• <b>LIMITED</b> – This allows only limited fields to be changed such as <b>DATE</b> and <b>TIME</b>.</li> <li>• <b>No Access</b> – This prevents User access to the BIOS Setup Utility.</li> </ul> <p><b>NOTE:</b> This option is only available when the Supervisor Password has been set above.</p>
Boot Sector Virus Protection	When Enabled, the AMI BIOS displays a warning when any program or virus issues a Disk Format command or attempts to write to the boot sector of the hard disk drive. Options include <b>Enabled</b> and <b>Disabled</b> .

## 5-7 Boot

Choose **Boot** from the AMI BIOS Setup Utility main menu with the arrow keys to bring up the **BOOT SETUP** menu. Security setting options are displayed by highlighting the setting using the arrow keys and pressing <ENTER>. All Security BIOS settings are described in [Table 5-15](#) below.

**Table 5-15. Boot Setup Menu Options**

Menu Option	Description
►Boot Device Priority	This sub-menu feature allows you to specify the sequence of priority for the boot device (such as hard disk drives, USB devices, CD-ROM drives, Network drives and so on). The menu options are for 1st Boot Device, 2nd Boot Device and 3rd Boot device, 4th Boot Device and 5th Boot Device. Each numbered boot device can be set to a specific device installed in your system or to Disabled. <b>NOTE:</b> A device enclosed in parenthesis has been disabled in the corresponding type menu.
►Hard Disk Drives	This sub-menu feature allows you to specify the boot sequence from all available hard disk drives installed on your system. The settings for each are list of all available hard disk drives in your system that have been detected or Disabled.
►Network Drives	This sub-menu feature allows you to specify the boot sequence from all available Network drives installed on your system. The settings for each are list of all available Network drives in your system that have been detected or Disabled.
Retry Boot Devices	This setting, when enabled, will retry reading boot devices when there is an error in booting. Options are Enabled or <b>Disabled</b> .

## 5-8 Exit

Choose EXIT from the AMI BIOS Setup Utility main menu with the arrow keys to display the EXIT SETUP menu. All Exit BIOS settings are described in [Table 5-16](#) below.

**Table 5-16. Exit Menu Options**

Menu Option	Description
Save Changes and Exit	Highlight this item and hit <ENTER> to save any changes you made and to exit the BIOS Setup utility. The system will reboot and implement the changes you have made to the BIOS Setup.
Discard Changes and Exit	Highlight this item and hit <ENTER> to exit the BIOS Setup utility without saving any changes you may have made. Any changes you have made to the BIOS Setup will not take effect upon system bootup.
Discard Changes	Highlight this item and hit <ENTER> to discard (cancel) any changes you made. You will remain in the Setup utility.
Load Optimal Defaults	Highlight this item and hit <ENTER> to load the default settings for all items in the BIOS Setup. These are the safest settings to use and are designed for maximum system performance, but may not work best for all computer applications.
Load Fail Safe Defaults	Highlight this item and hit <ENTER> to load fail-safe settings that are designed for maximum system stability, but not for maximum performance.

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# Appendix A

## BIOS POST Codes

### A-1 BIOS POST Messages

During the Power-On Self-Test (POST), the BIOS will check for problems. If a problem is found, the BIOS will activate an alarm or display a message. The following is a list of such BIOS messages.

**Table A-1. BIOS POST Messages**

BIOS Message	Description
Failure Fixed Disk	Fixed disk is not working or not configured properly. Check to see if fixed disk is attached properly. Run Setup. Find out if the fixed-disk type is correctly identified.
Stuck key	Stuck key on keyboard.
Keyboard error	Keyboard not working.
Keyboard Controller Failed	Keyboard controller failed test. May require replacing keyboard controller.
Keyboard locked - Unlock key switch	Unlock the system to proceed.
Monitor type does not match CMOS - Run SETUP	Monitor type not correctly identified in Setup
Shadow Ram Failed at offset: nnnn	Shadow RAM failed at offset <b>nnnn</b> of the 64k block at which the error was detected.
System RAM Failed at offset: nnnn	System RAM failed at offset <b>nnnn</b> of in the 64k block at which the error was detected.
Extended RAM Failed at offset: nnnn	Extended memory not working or not configured properly at offset <b>nnnn</b> .
System battery is dead - Replace and run SETUP	The CMOS clock battery indicator shows the battery is dead. Replace the battery and run Setup to reconfigure the system.
System CMOS checksum bad - Default configuration used	System CMOS has been corrupted or modified incorrectly, perhaps by an application program that changes data stored in CMOS. The BIOS installed Default Setup Values. If you do not want these values, enter Setup and enter your own values. If the error persists, check the system battery or contact your dealer.
System timer error	The timer test failed. Requires repair of system board.
Real time clock error	Real-Time Clock fails BIOS hardware test. May require board repair.
Check date and time settings	BIOS found date or time out of range and reset the Real-Time Clock. May require setting legal date (1991-2099).

Table A-1. BIOS POST Messages (Continued)

BIOS Message	Description
Previous boot incomplete - Default configuration used	Previous POST did not complete successfully. POST loads default values and offers to run Setup. If the failure was caused by incorrect values and they are not corrected, the next boot will likely fail. On systems with control of <b>wait states</b> , improper Setup settings can also terminate POST and cause this error on the next boot. Run Setup and verify that the waitstate configuration is correct. This error is cleared the next time the system is booted.
Memory Size found by POST differed from CMOS	Memory size found by POST differed from CMOS.
Diskette drive A error Diskette drive B error	Drive A: or B: is present but fails the BIOS POST diskette tests. Check to see that the drive is defined with the proper diskette type in Setup and that the diskette drive is attached correctly.
Incorrect Drive A type - run SETUP	Type of floppy drive A: not correctly identified in Setup.
Incorrect Drive B type - run SETUP	Type of floppy drive B: not correctly identified in Setup.
System cache error - Cache disabled	RAM cache failed and BIOS disabled the cache. On older boards, check the cache jumpers. You may have to replace the cache. See your dealer. A disabled cache slows system performance considerably.
CPU ID:	CPU socket number for Multi-Processor error.
EISA CMOS not writeable	ServerBIOS2 test error: Cannot write to EISA CMOS.
DMA Test Failed	ServerBIOS2 test error: Cannot write to extended <b>DMA</b> (Direct Memory Access) registers.
Software NMI Failed	ServerBIOS2 test error: Cannot generate software NMI (Non-Maskable Interrupt).
Fail-Safe Timer NMI Failed	ServerBIOS2 test error: Fail-Safe Timer takes too long.
device Address Conflict	Address conflict for specified <b>device</b> .
Allocation Error for: device	Run ISA or EISA Configuration Utility to resolve resource conflict for the specified <b>device</b> .
CD ROM Drive	CD ROM Drive identified.
Entering SETUP...	Starting Setup program
Failing Bits: nnnn	The hex number <b>nnnn</b> is a map of the bits at the RAM address which failed the memory test. Each 1 (one) in the map indicates a failed bit. See errors 230, 231, or 232 above for offset address of the failure in System, Extended, or Shadow memory.
Fixed Disk n	Fixed disk <b>n</b> (0-3) identified.
Invalid System Configuration Data	Problem with NVRAM (CMOS) data.
I/O device IRQ conflict	I/O device IRQ conflict error.
PS/2 Mouse Boot Summary Screen:	PS/2 Mouse installed.

Table A-1. BIOS POST Messages (Continued)

BIOS Message	Description
nnnn kB Extended RAM Passed	Where <b>nnnn</b> is the amount of RAM in kilobytes successfully tested.
nnnn Cache SRAM Passed	Where <b>nnnn</b> is the amount of system cache in kilobytes successfully tested.
nnnn kB Shadow RAM Passed	Where <b>nnnn</b> is the amount of shadow RAM in kilobytes successfully tested.
nnnn kB System RAM Passed	Where <b>nnnn</b> is the amount of system RAM in kilobytes successfully tested.
One or more I2O Block Storage Devices were excluded from the Setup Boot Menu	There was not enough room in the IPL table to display all installed I2O block-storage devices.
Operating system not found	Operating system cannot be located on either drive A: or drive C:. Enter Setup and see if fixed disk and drive A: are properly identified.
Parity Check 1 nnnn	Parity error found in the system bus. BIOS attempts to locate the address and display it on the screen. If it cannot locate the address, it displays ?????. Parity is a method for checking errors in binary data. A parity error indicates that some data has been corrupted.
Parity Check 2 nnnn	Parity error found in the I/O bus. BIOS attempts to locate the address and display it on the screen. If it cannot locate the address, it displays ????.
Press <F1> to resume, <F2> to Setup, <F3> for previous	Displayed after any recoverable error message. Press <F1> to start the boot process or <F2> to enter Setup and change the settings. Press <F3> to display the previous screen (usually an initialization error of an <b>Option ROM</b> , i.e., an add-on card). Write down and follow the information shown on the screen.
Press <F2> to enter Setup	Optional message displayed during POST. Can be turned off in Setup.
PS/2 Mouse:	PS/2 mouse identified.
Run the I2O Configuration Utility	One or more unclaimed block storage devices have the Configuration Request bit set in the LCT. Run an I2O Configuration Utility (e.g. the SAC utility).
System BIOS shadowed	System BIOS copied to shadow RAM.
UMB upper limit segment address: nnnn	Displays the address <b>nnnn</b> of the upper limit of <b>Upper Memory Blocks</b> , indicating released segments of the BIOS which can be reclaimed by a virtual memory manager.
Video BIOS shadowed	Video BIOS successfully copied to shadow RAM.

## A-2 BIOS POST Codes

This section lists the POST (Power-On Self-Test) codes for the AMI BIOS. POST codes are divided into two categories: recoverable and terminal.

### Recoverable POST Errors

When a recoverable type of error occurs during POST, the BIOS will display an POST code that describes the problem. BIOS may also issue one of the following beep codes:

- **One long and two short beeps** – video configuration error

### Terminal POST Errors

If a terminal type of error occurs, BIOS will shut down the system. Before doing so, BIOS will write the error to *port 80h*, attempt to initialize video and write the error in the top left corner of the screen.

The following is a list of codes that may be written to *port 80h*.

**Table A-2. Terminal POST Errors**

Post Code	Description
02h	Verify Real Mode
03h	Disable Non-Maskable Interrupt (NMI)
04h	Get CPU type
06h	Initialize system hardware
07h	Disable shadow and execute code from the ROM.
08h	Initialize chipset with initial POST values
09h	Set IN POST flag
0Ah	Initialize CPU registers
0Bh	Enable CPU cache
0Ch	Initialize caches to initial POST values
0Eh	Initialize I/O component
0Fh	Initialize the local bus IDE
10h	Initialize Power Management
11h	Load alternate registers with initial POST values
12h	Restore CPU control word during warm boot
13h	Initialize PCI Bus Mastering devices
14h	Initialize keyboard controller
16h	1-2-2-3 BIOS ROM checksum
17h	Initialize cache before memory Auto size
18h	8254 timer initialization



Table A-2. Terminal POST Errors (Continued)

Post Code	Description
1Ah	8237 DMA controller initialization
1Ch	Reset Programmable Interrupt Controller
20h	1-3-1-1 Test DRAM refresh
22h	1-3-1-3 Test 8742 Keyboard Controller
28h	Auto size DRAM
29h	Initialize POST Memory Manager
2Ah	Clear 512 kB base RAM
2Ch	1-3-4-1 RAM failure on address line <b>xxxx</b> *
2Eh	1-3-4-3 RAM failure on data bits <b>xxxx</b> * of low byte of memory bus
2Fh	Enable cache before system BIOS shadow
32h	Test CPU bus-clock frequency
33h	Initialize Phoenix Dispatch Manager
36h	Warm start shut down
38h	Shadow system BIOS ROM
3Ah	Auto size cache
3Ch	Advanced configuration of chipset registers
3Dh	Load alternate registers with CMOS values
41h	Initialize extended memory for RomPilot
42h	Initialize interrupt vectors
45h	POST device initialization
46h	2-1-2-3 Check ROM copyright notice
47h	Initialize I20 support
48h	Check video configuration against CMOS
49h	Initialize PCI bus and devices
4Ah	Initialize all video adapters in system
4Bh	QuietBoot start (optional)
4Ch	Shadow video BIOS ROM
4Eh	Display BIOS copyright notice
4Fh	Initialize MultiBoot
50h	Display CPU type and speed
51h	Initialize EISA board
52h	Test keyboard
54h	Set key click if enabled
55h	Enable USB devices
58h	2-2-3-1 Test for unexpected interrupts

**Table A-2. Terminal POST Errors (Continued)**

<b>Post Code</b>	<b>Description</b>
59h	Initialize POST display service
5Ah	Display prompt "Press F2 to enter SETUP"
5Bh	Disable CPU cache
5Ch	Test RAM between 512 and 640 kB
60h	Test extended memory
62h	Test extended memory address lines
64h	Jump to UserPatch1
66h	Configure advanced cache registers
67h	Initialize Multi Processor APIC
68h	Enable external and CPU caches
69h	Setup System Management Mode (SMM) area
6Ah	Display external L2 cache size
6Bh	Load custom defaults (optional)
6Ch	Display shadow-area message
6Eh	Display possible high address for UMB recovery
70h	Display error messages
72h	Check for configuration errors
76h	Check for keyboard errors
7Ch	Set up hardware interrupt vectors
7Dh	Initialize Intelligent System Monitoring
7Eh	Initialize coprocessor if present
80h	Disable onboard Super I/O ports and IRQs
81h	Late POST device initialization
82h	Detect and install external RS232 ports
83h	Configure non-MCD IDE controllers
84h	Detect and install external parallel ports
85h	Initialize PC-compatible PnP ISA devices
86h	Re-initialize onboard I/O ports.
87h	Configure Motherboard Configurable Devices (optional)
88h	Initialize BIOS Data Area
89h	Enable Non-Maskable Interrupts (NMIs)
8Ah	Initialize Extended BIOS Data Area
8Bh	Test and initialize PS/2 mouse
8Ch	Initialize floppy controller
8Fh	Determine number of ATA drives (optional)

**Table A-2. Terminal POST Errors (Continued)**

<b>Post Code</b>	<b>Description</b>
90h	Initialize hard-disk controllers
91h	Initialize local-bus hard-disk controllers
92h	Jump to UserPatch2
93h	Build MPTABLE for multi-processor boards
95h	Install CD ROM for boot
96h	Clear huge ES segment register
98h	1-2 Search for option ROMs. One long, two short beeps on check-sum failure
99h	Check for SMART Drive (optional)
9Ah	Shadow option ROMs
9Ch	Set up Power Management
9Dh	Initialize security engine (optional)
9Eh	Enable hardware interrupts
9Fh	Determine number of ATA and SCSI drives
A0h	Set time of day
A2h	Check key lock
A4h	Initialize typematic rate
A8h	Erase F2 prompt
AAh	Scan for F2 key stroke
ACh	Enter SETUP
A Eh	Clear Boot flag
B0h	Check for errors
B1h	Inform RomPilot about the end of POST.
B2h	POST done - prepare to boot operating system
B4h	1 One short beep before boot
B5h	Terminate QuietBoot (optional)
B6h	Check password (optional)
B7h	Initialize ACPI BIOS
B9h	Prepare Boot
BAh	Initialize SMBIOS
BBh	Initialize PnP Option ROMs
BCh	Clear parity checkers
BDh	Display MultiBoot menu
BEh	Clear screen (optional)
BFh	Check virus and backup reminders
C0h	Try to boot with INT 19

**Table A-2. Terminal POST Errors (Continued)**

<b>Post Code</b>	<b>Description</b>
C1h	Initialize POST Error Manager (PEM)
C2h	Initialize error logging
C3h	Initialize error display function
C4h	Initialize system error handler
C5h	PnPnd dual CMOS (optional)
C6h	Initialize note dock (optional)
C7h	Initialize note dock late
C8h	Force check (optional)
C9h	Extended checksum (optional)
CAh	Redirect Int 15h to enable remote keyboard
CBh	Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk
CCh	Redirect Int 10h to enable remote serial video
CDh	Re-map I/O and memory for PCMCIA
CEh	Initialize digitizer and display message
D2h	Unknown interrupt

The following are for the boot block in Flash ROM:

**Table A-3. Boot Block Flash ROM Terminal POST Errors**

<b>Post Code</b>	<b>Description</b>
E0h	Initialize the chipset
E1h	Initialize the bridge
E2h	Initialize the CPU
E3h	Initialize system timer
E4h	Initialize system I/O
E5h	Check force recovery boot
E6h	Checksum BIOS ROM
E7h	Go to BIOS
E8h	Set Huge Segment
E9h	Initialize Multi Processor
EAh	Initialize OEM special code
EBh	Initialize PIC and DMA
ECh	Initialize Memory type
EDh	Initialize Memory size
EEh	Shadow Boot Block

**Table A-3. Boot Block Flash ROM Terminal POST Errors (Continued)**

Post Code	Description
EFh	System memory test
F0h	Initialize interrupt vectors
F1h	Initialize Run Time Clock
F2h	Initialize video
F3h	Initialize System Management Manager
F4h	Output one beep
F5h	Clear Huge Segment
F6h	Boot to Mini DOS
F7h	Boot to Full DOS

If the BIOS detects error 2C, 2E, or 30 (base 512K RAM error), it displays an additional word-bitmap (**xxxx**) indicating the address line or bits that failed. For example, "2C 0002" means address line 1 (bit one set) has failed. "2E 1020" means data bits 12 and 5 (bits 12 and 5 set) have failed in the lower 16 bits. The BIOS also sends the bitmap to the port-80 LED display. It first displays the checkpoint code, followed by a delay, the high-order byte, another delay, and then the low order byte of the error. It repeats this sequence continuously.

## Notes

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## Notes