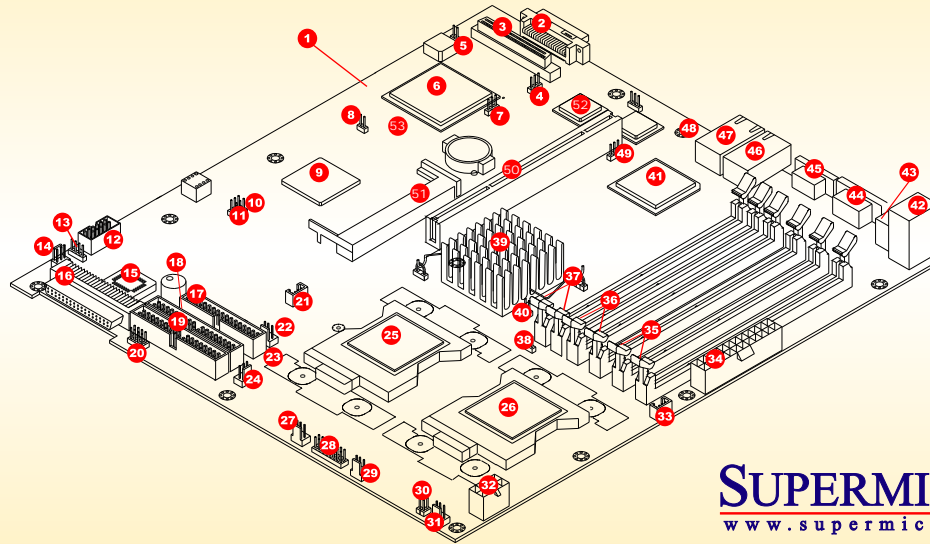


# 6013L-8 Serverboard Components



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1 X5DLR-8G2+ Serverboard	19 J19/J18: IDE#2, IDE#1	37 Bank 0B/0A
2 JA5: Ultra III LVD/SE ChB	20 USB 2/3	38 JP12: System bus speed
3 JA1: Ultra III LVD/SE ChB	21 J21: SMB header	39 North bridge
4 JP56: VGA enable/disable	22 CPU2 fan	40 J35: Spread spectrum
5 JPA1: SCSI channel A termination	23 JP58: Fan detection select	41 CIOB-E chip
6 AIC-7902 SCSI chip	24 CPU2/Chassis fan	42 J11: Keyboard/mouse
7 JA4: SCSI enable/disable	25 CPU2 socket	43 USB 0/1 port
8 JPA2: SCSI channel B termination	26 CPU1 socket	44 J66: COM1 port
9 South bridge	27 CPU1 fan	45 VGA port
10 JBT1: CMOS clear	28 JF1: Front control panel connector	46 Gigabit LAN1
11 JP3: Watch dog	29 CPU1/Chassis fan	47 Gigabit LAN2
12 COM2	30 JP48: Chassis/overheat fan select	48 D1-D8: Debug LEDs
13 JP57: Chassis Intrusion Header	31 OH/chassis Fan	49 P2: PCI-X speed settings
14 Chassis fan	32 J56: Processor power connector	50 PCI-X slot
15 BIOS	33 JP46: Third power supply fail header	51 SCSI RAID socket
16 JA2: Ultra III LVD/SE ChA	34 ATX power conn.	52 ATI Rage XL SVGA chip
17 J12: Floppy conn.	35 Bank 2B/2A	53 Battery
18 Speaker enable/disable	36 Bank 1B/1A	

\* Note: Interleaved ECC registered memory requires DDR DIMMs to be installed in pairs

## X5DLR-8G2+ Quik Reference

Jumpers	Description	Default Setting
J35	Spread spectrum	Open (Disabled)
JA4	SCSI enable/disable	Pins 1-2 (Enabled)
JBT1	CMOSE Clear	Pad
JP2	Speaker Enable/Disable	Close (Enabled)
JP3	Watch Dog	Pins 2-3 (NMI)
JP12	System Bus Speed	Pins 1-2 (Auto)
JP48	Chassis/Overheat Fan Select	Open (Overheat)
JP56	VGA Enable/Disable	Pins 1-2 (Enabled)
JP58	Fan Detection Select	Open (CPU fan)
JPA1/A2	SCSI channel A/B Termination	Open (Terminated)

## Rear Panel Functions



1. AC power connector
2. PS/2 mouse and keyboard ports
3. 2 USB ports
4. COM port
5. VGA port
6. 2 Gigabit LAN ports
7. 1 Standard PCI/X expansion slot
8. PCI/X card release latch
9. Ext. SCSI port