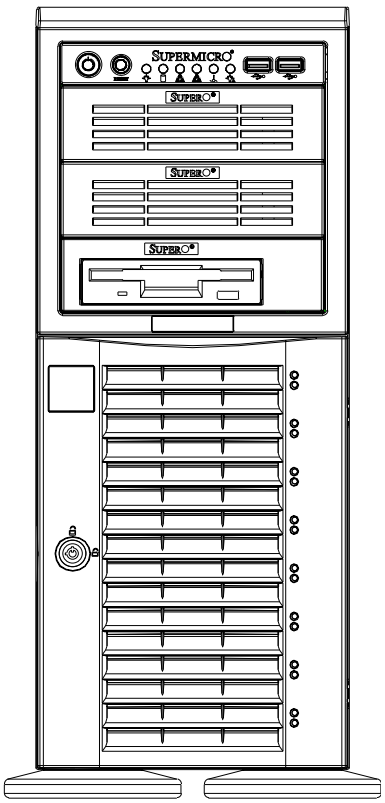
SUPERO®

A+ Workstation 4021A-T2



USER'S MANUAL

1.0

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Manual Revision 1.0
Release Date: January 31, 2008

Printed in the United States of America

Preface

About This Manual

This manual is written for professional system integrators and PC technicians. It provides information for the installation and use of the A+ Workstation 4021A-T2. Installation and maintainance should be performed by experienced technicians only.

The 4021A-T2 is a high-end, dual processor workstation based on the SC743T-645 4U tower/rackmount chassis and the H8DAE-2, a dual processor serverboard that supports single or dual AMD 64-bit Socket F, Opteron 2000 type processors.

Manual Organization

Chapter 1: Introduction

The first chapter provides a checklist of the main components included with the system and describes the main features of the H8DAE-2 serverboard and the SC743T-645 chassis, which comprise the 4021A-T2.

Chapter 2: Installation

This chapter describes the steps necessary to install the system into a rack and check out the workstation configuration prior to powering up the system. If your workstation was ordered without processor and memory components, this chapter will refer you to the appropriate sections of the manual for their installation.

Chapter 3: System Interface

Refer here for details on the system interface, which includes the functions and information provided by the control panel on the chassis as well as other LEDs located throughout the system.

Chapter 4: System Safety

You should thoroughly familiarize yourself with this chapter for a general overview of safety precautions that should be followed when installing and servicing the 4021A-T2.

Chapter 6: Advanced Chassis Setup

Refer to Chapter 6 for detailed information on the SC743T-645 chassis. You should follow the procedures given in this chapter when installing, removing or reconfiguring Serial ATA or peripheral drives and when replacing system power supply units and cooling fans.

Chapter 7: BIOS

The BIOS chapter includes an introduction to BIOS and provides detailed information on running the CMOS Setup Utility.

Appendix A: BIOS Error Beep Codes

Appendix B: BIOS POST Checkpoint Codes

Appendix C: System Specifications

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Chapter 1

Introduction

1-1 Overview

The A+ Workstation 4021A-T2 is a high-end workstation that is comprised of two main subsystems: the SC743T-645 tower/4U chassis and the H8DAE-2 serverboard, which supports single or dual AMD Opteron™ 2000 series processors and up to 64 GB of registered ECC DDR667/533/400 SDRAM memory.

In addition to the serverboard and chassis, various hardware components have been included with the 4021A-T2, as listed below:

- Four (4) 8-cm hot-swap chassis fans (FAN-0074)
- Two (2) 5.25" dummy drive trays [CSE-PT41(B)]
- One (1) front control panel cable (CBL-0087)
- Two (2) IEEE 1394 (Firewire) cables (CBL-0173L)
- Serial ATA Accessories
  One (1) Serial ATA backplane (CSE-SAS-743TQ)
  Six (6) Serial ATA data cables (CBL-0061L)
  Two (2) SGPIO cables (CBL-0157L)
  Eight (8) Serial ATA hot-swap drive carriers [CSE-PT17(B)]
- Optional:
  Two (2) active CPU heatsinks with PWM fan (SNK-P0024AP4)
  One (1) 9-cm PWM cooling fan (FAN-0091L)
  One (1) 3.5" floppy drive [FPD-PNSC-02(1)]

1-2 Serverboard Features

At the heart of the 4021A-T2 lies the H8DAE-2, a dual processor serverboard based on the nVidia MCP55 Pro/IO-55 and NEC uPD720400 chipset. Below are the main features of the H8DAE-2. (See Figure 1-1 for a block diagram of the chipset).

Processors

The H8DAE-2 supports single or dual 64-bit AMD Socket F, Opteron 2000 type processors.

Memory

The H8DAE-2 has eight 240-pin DIMM slots that can support up to 64 GB of registered ECC DDR2-667/533/400. (The maximum memory capacity is halved if only one processor is installed.) Memory in supported in both interleaved and non-interleaved configurations. See Section 5-6 for details.

Serial ATA

A Serial ATA controller is integrated into the nVidia MCP55 Pro chipset to provide a six-port Serial ATA subsystem, which is RAID 0, 1, 0+1 and 5 supported. The Serial ATA drives are hot-swappable units.

Note: The operating system you use must have RAID support to enable the hot-swap capability and RAID functions of the Serial ATA drives.

PCI Expansion Slots

The H8DAE-2 has six PCI expansion slots, which includes two PCI-Express x16 slots, one PCI-Express x8 slot, one PCI-Express x4 slot, one 133 MHz PCI-X slot and one 100 MHz PCI-X slot.

Onboard Controllers/Ports

One floppy drive controller and an onboard ATA/133 controller is provided to support up to two IDE hard drives or ATAPI devices. The I/O ports include two COM ports, a parallel port, two USB ports, PS/2 mouse and keyboard ports and two gigabit Ethernet ports. Two front side USB ports are included on the front of the chassis. Audio jacks are also included for high-definition audio.

Other Features

Other onboard features that promote system health include onboard voltage monitors, a chassis intrusion header, auto-switching voltage regulators, chassis and CPU overheat sensors, virus protection and BIOS rescue.

1-3 Chassis Features

The following is a general outline of the main features of the SC743T-645 chassis.

System Power

The SC743T-645 features a single 645W power supply. The system must be shut down and the AC power cord removed before replacing or performing any service on the power supply unit.

Serial ATA Subsystem

The Serial ATA subsystem supports up to six Serial ATA drives. The Serial ATA drives are hot-swappable units and are connected to a backplane that provides power and control.

Note: The operating system you use must have RAID support to enable the hot-swap capability of the Serial ATA drives.

Front Control Panel

The chassis' control panel provides you with system monitoring and control. LEDs indicate system power, HDD activity, network activity, system overheat and power supply failure. A main power button and a system reset button are also included.

I/O Backplane

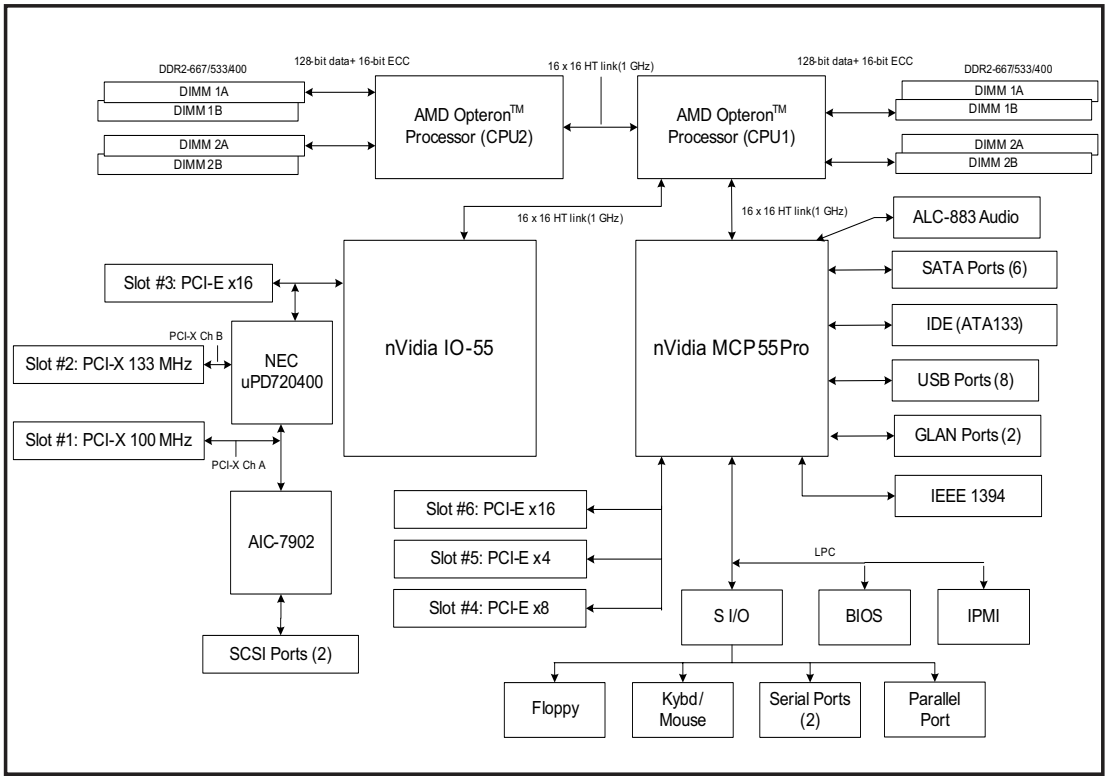
The SC743T-645 is an ATX form factor chassis that may be used in either a tower or a 4U rackmount configuration. Up to seven PCI expansion slots can be accommodated in the chassis.

Cooling System

The SC743T-645 chassis has an innovative cooling design that includes four 8-cm hot-plug system cooling fans located in the middle section of the chassis. These are PWM (Pulse Width Modulated) fans, which allows their speed to be set with an option in BIOS. The power supply module also includes a cooling fan.

Figure 1-1. nVidia MCP55 Pro/IO-55, uPD720400 Chipset: System Block Diagram

Note: This is a general block diagram. Please see Chapter 5 for details.



1-4 Contacting Supermicro

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Notes

Chapter 2

Server Installation

2-1 Overview

This chapter provides a quick setup checklist to get your A+ Workstation 4021A-T2 up and running. Following these steps in the order given should enable you to have the system operational within a minimum amount of time. This quick setup assumes that your system has come to you with the processors and memory preinstalled. If your system is not already fully integrated with a serverboard, processors, memory etc., please turn to the chapter or section noted in each step for details on installing specific components.

Although the 4021A-T2 was designed to be used in a tower (workstation) configuration, it may also be mounted in a rack as a 4U rackmount server system. If using it as a tower unit, please read the precautions in the next section and then skip ahead to Section 2-5.

2-2 Unpacking the System

You should inspect the box the system was shipped in and note if it was damaged in any way. If the workstation itself shows damage you should file a damage claim with the carrier who delivered it.

Decide on a suitable location for the 4021A-T2. It should be situated in a clean, dust-free area that is well ventilated. Avoid areas where heat, electrical noise and electromagnetic fields are generated. You will also need it placed near a grounded power outlet. Be sure to read the Rack and Server Precautions in the next section.

2-3 Preparing for Setup

The box the workstation was shipped in may include two sets of rail assemblies, two rail mounting brackets and mounting screws needed for installing the system into a rack (optional kit). Follow the steps in the order given to complete the installation process in a minimum amount of time. Please read this section in its entirety before you begin the installation procedure outlined in the sections that follow.

Choosing a Setup Location

- Leave enough clearance in front of the rack to enable you to open the front door completely (~25 inches) and approximately 30 inches of clearance in the back of the rack to allow for sufficient airflow and ease in servicing.This product is for installation only in a Restricted Access Location (dedicated equipment rooms, service closets and the like).

- This product is not suitable for use with visual display work place devices acccording to §2 of the the German Ordinance for Work with Visual Display Units.

Warnings and Precautions!

Rack Precautions

- Ensure that the leveling jacks on the bottom of the rack are fully extended to the floor with the full weight of the rack resting on them.

- In single rack installation, stabilizers should be attached to the rack. In multiple rack installations, the racks should be coupled together.

- Always make sure the rack is stable before extending a component from the rack.

- You should extend only one component at a time - extending two or more simultaneously may cause the rack to become unstable.

Server Precautions

- Review the electrical and general safety precautions in Chapter 4.

- Determine the placement of each component in the rack *before* you install the rails.

- Install the heaviest server components on the bottom of the rack first, and then work up.

- Use a regulating uninterruptible power supply (UPS) to protect the server from power surges, voltage spikes and to keep your system operating in case of a power failure.

- Allow the hot plug SATA drives and power supply modules to cool before touching them.

- Always keep the rack's front door and all panels and components on the servers closed when not servicing to maintain proper cooling.

Rack Mounting Considerations

Ambient Operating Temperature

If installed in a closed or multi-unit rack assembly, the ambient operating temperature of the rack environment may be greater than the ambient temperature of the room. Therefore, consideration should be given to installing the equipment in an environment compatible with the manufacturer's maximum rated ambient temperature (Tmra).

Reduced Airflow

Equipment should be mounted into a rack so that the amount of airflow required for safe operation is not compromised.

Mechanical Loading

Equipment should be mounted into a rack so that a hazardous condition does not arise due to uneven mechanical loading.

Circuit Overloading

Consideration should be given to the connection of the equipment to the power supply circuitry and the effect that any possible overloading of circuits might have on overcurrent protection and power supply wiring. Appropriate consideration of equipment nameplate ratings should be used when addressing this concern.

Reliable Ground

A reliable ground must be maintained at all times. To ensure this, the rack itself should be grounded. Particular attention should be given to power supply connections other than the direct connections to the branch circuit (i.e. the use of power strips, etc.).- Leave approximately 30 inches of clearance in the back of the system to allow for sufficient airflow and ease in servicing.

2-4 Installing the System into a Rack

This section provides information on installing the system into a rack unit. Rack installation requires the use of the optional rackmount kit [CSE-PT26(B)]. If the system has already been mounted into a rack or if you are using it as a tower, you can skip ahead to Sections 2-5 and 2-6.

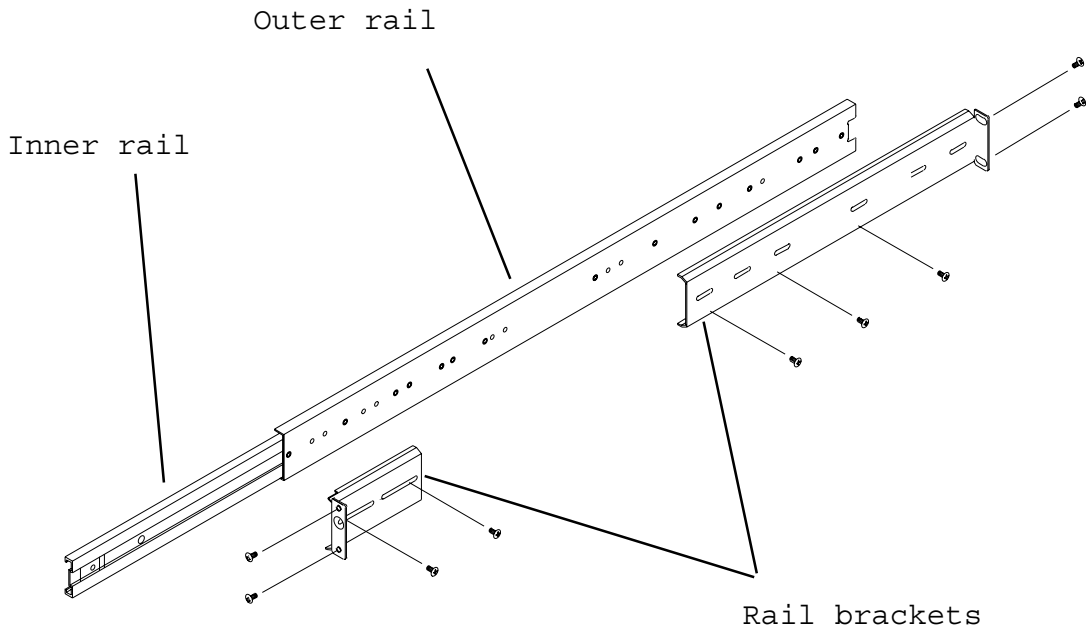
There are a variety of rack units on the market, which may mean the assembly procedure will differ slightly. The following is a guideline for installing the workstation into a rack with the rack rails provided in the rackmount kit. You should also refer to the installation instructions that came with the rack unit you are using.

Identifying the Sections of the Rack Rails

The optional rackmount kit (CSE-PT26 or CSE-PT26B - black) includes two rack rail assemblies. Each of these assemblies consist of three sections: an inner fixed chassis rail that secures to the chassis, an outer rack rail that secures directly to the rack itself and two rail brackets, which also attack to the rack (see Figure 2-1.) The inner and outer rails must be detached from each other to install.

To remove the inner chassis rail, pull it out as far as possible - you should hear a "click" sound as a locking tab emerges from inside the rail assembly and locks the inner rail. Depress the locking tab to pull the inner rail completely out. Do this for both assemblies (one for each side).

Figure 2-1. Identifying the Sections of the Rack Rails



Installing the Chassis Rails

You will need to remove the top cover and the feet to add rack rails to the chassis. First, remove the top and right covers (top and left covers when standing as a tower chassis) by first removing the screws that secure them to the chassis. Depress the button on the top (side if tower) of the chassis to release the cover and then pull the cover off. Then unscrew the four feet and remove them from the chassis (see Figure 2-2).

You can now attach rack rails to the top and bottom (now the sides) of the chassis. First add the rack handles. Then position the inner chassis rail sections you just removed along the side of the chassis making sure the screw holes line up. Note that these two rails are left/right specific. Screw the rail securely to the side of the chassis (see Figure 2-3). Repeat this procedure for the other rail on the other side of the chassis. You will also need to attach the rail brackets when installing into a telco rack.

Locking Tabs: As mentioned, the chassis rails have a locking tab, which serves two functions. The first is to lock the system into place when installed and pushed fully into the rack, which is its normal position. Secondly, these tabs also lock the system in place when fully extended from the rack. This prevents the system from coming completely out of the rack when you pull it out for servicing.

Figure 2-2. Preparing to Install the Chassis Rails

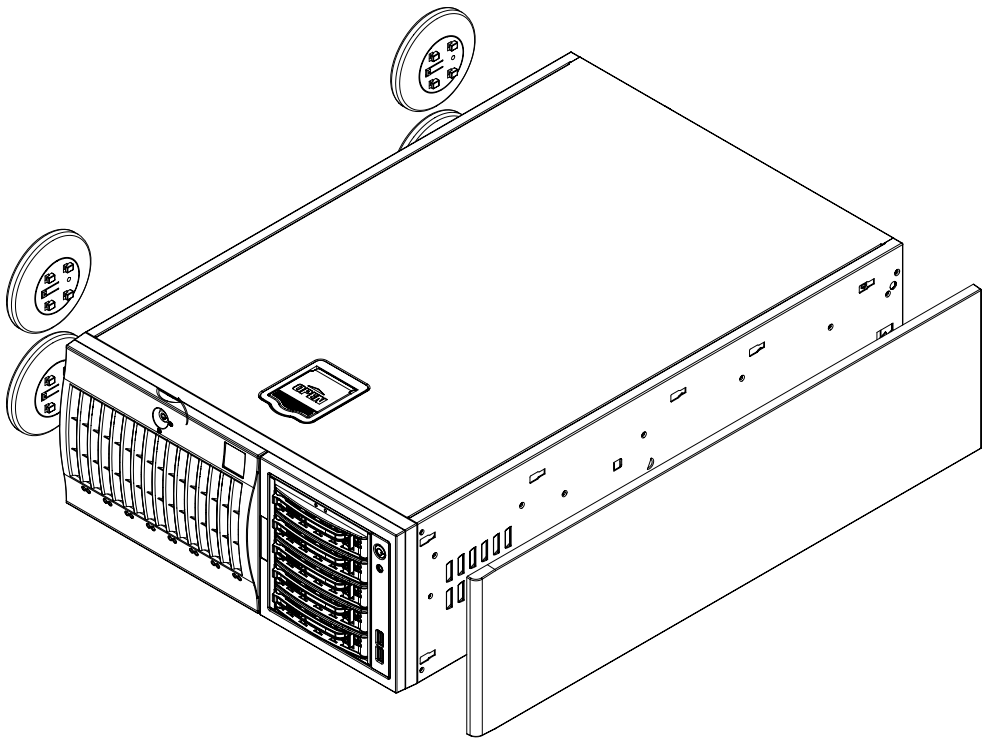
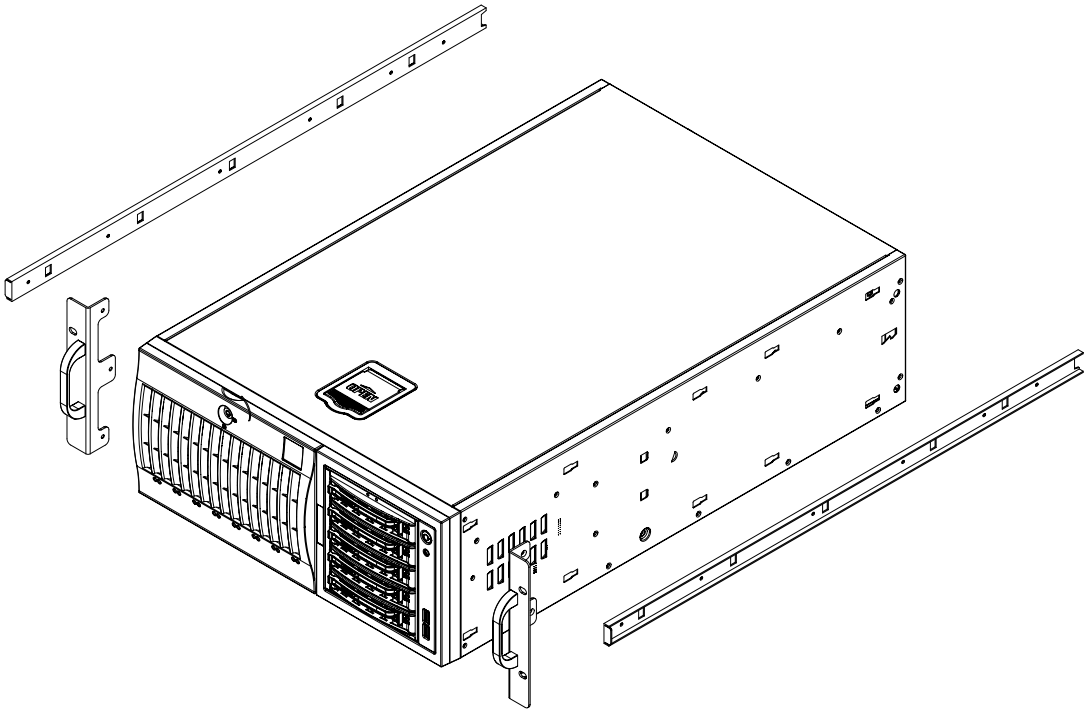


Figure 2-3. Installing the Rails to the Chassis



Installing the Rack Rails

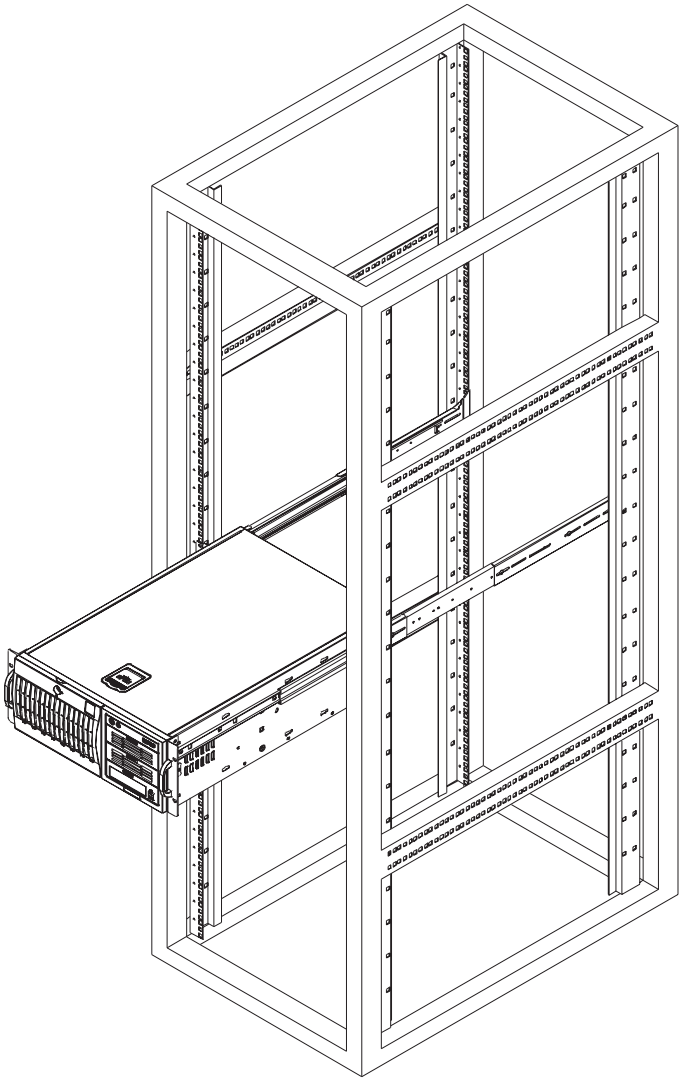
Determine where you want to place the 4021A-T2 in the rack. (See Rack and Server Precautions in Section 2-3.) Position the fixed rack rail/sliding rail guide assemblies at the desired location in the rack, keeping the sliding rail guide facing the inside of the rack. Screw the assembly securely to the rack using the brackets provided. Attach the other assembly to the other side of the rack, making sure both are at the exact same height and with the rail guides facing inward.

Installing the System into the Rack

You should now have rails attached to both the chassis and the rack unit. The next step is to install the system into the rack. You should have two brackets in the rack mount kit. Install these first keeping in mind that they are left/right specific (marked with "L" and "R"). Then, line up the rear of the chassis rails with the front of the rack rails. Slide the chassis rails into the rack rails, keeping the pressure even on both sides (you may have to depress the locking tabs when inserting).

When the system has been pushed completely into the rack, you should hear the locking tabs "click". Finish by inserting and tightening the thumbscrews that hold the front of the workstation to the rack (see Figure 2-4).

Figure 2-4. Installing the System into a Rack



2-5 Checking the Serverboard Setup

After setting up the the system, you will need to open the unit to make sure the-serverboard is properly installed and all the connections have been made.

Accessing the Inside of the System (Figure 2-5)

If the system is rack mounted, first release the retention screws that secure the unit to the rack. Grasp the two handles on either side and pull the unit straight out until it locks (you will hear a "click").

1. There are two screws that secure the cover to the chassis - remove these first.

2. Depress the button on the top (side if tower) of the chassis to release the cover.

3. You can then slide the cover off the chassis to gain full access to the inside of the workstation.

Checking the Components

1. You may have one or two processors already installed into the serverboard. Each processor should have its own heatsink attached. See Chapter 5 for instructions on processor installation.

2. Your system may have come with system memory already installed. Make sure all DIMMs are fully seated in their slots. For details on adding system memory, refer to Chapter 5.

3. If desired, you can install add-on cards to the system. See Chapter 5 for details on installing PCI add-on cards.

4. Make sure all power and data cables are properly connected and not blocking the chassis airflow. See Chapter 5 for details on cable connections.

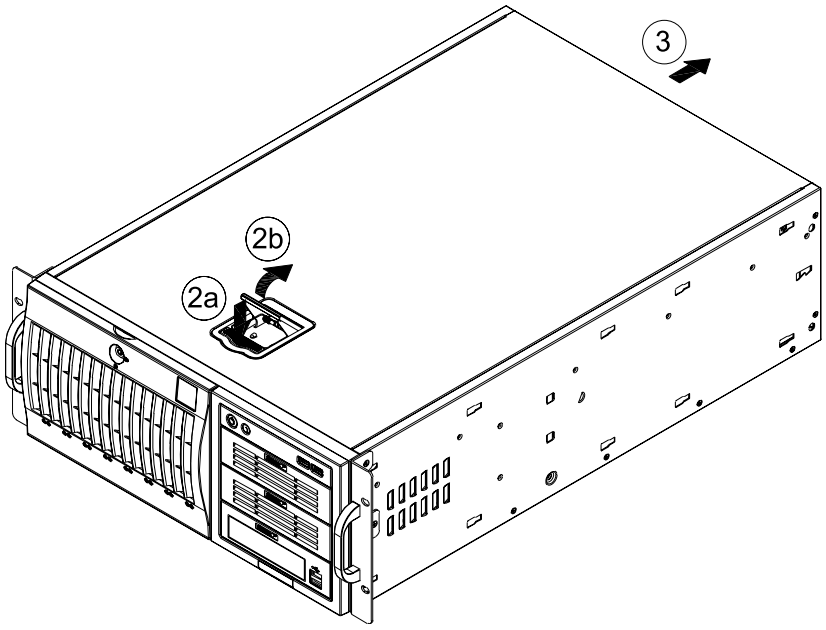
2-6 Checking the Drive Bay Setup

Next, you should check to make sure the peripheral drives and the SATA drives and SATA backplane have been properly installed and all connections have been made.

Checking the Drives

1. All drives can be accessed from the front of the workstation. For servicing the CD-ROM, IDE hard drives and floppy drives, you will need to remove the

Figure 2-5. Accessing the Inside of the System (Rack Configuration shown)



top/left chassis cover. The SATA disk drives can be installed and removed from the front of the chassis without removing any chassis covers.

2. To install components into the 5.25" drive bays, you must first remove the top/left chassis cover as described in the previous section. Refer to Chapter 6 for details.

3. Refer to Chapter 6 if you need to reinstall a CD-ROM and/or floppy disk drive to the system.

4. Depending upon your system's configuration, there may be one or more drives already installed. If you need to install SATA drives, please refer to Chapter 6.

Checking the Airflow

1. Airflow is provided by four hot-swap 8-cm chassis fans working in conjunction with an air shroud. Two 8-cm exhaust fans are also mounted at the rear of the chassis. The system component layout was carefully designed to promote sufficient airflow through the chassis.

2. Note that all power and data cables have been routed in such a way that they do not block the airflow generated by the fans. Keep this in mind when you reroute them after working on the system.

Providing Power

1. The last thing you must do is to provide input power to the system. Plug the power cord from the power supply unit into a high-quality power strip that offers protection from electrical noise and power surges. It is recommended that you use an uninterruptible power supply (UPS).

2. Depress the power on button on the front of the chassis.

Chapter 3

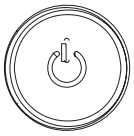
System Interface

3-1 Overview

The control panel on the 4021A-T2 has several LEDs and two buttons. There is also an LED on each Serial ATA drive carrier. These LEDs keep you constantly informed of the overall status of the system and the activity and health of specific components.

3-2 Control Panel Buttons

There are two push-buttons located on the front of the chassis. These are (in order from left to right) a power on/off button and a reset button.



Power

This is the main power button, which is used to apply or turn off the main system power. Turning off system power with this button removes the main power but keeps standby power supplied to the system.

Reset

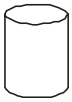
Use the reset button to reboot the system.

3-3 Control Panel LEDs

The control panel located on the front of the SC743T-645 chassis has six LEDs that provide you with critical information related to different parts of the system. This section explains what each LED indicates when illuminated and any corrective action you may need to take.

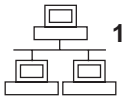
Power

Indicates power is being supplied to the system's power supply. This LED should normally be on when the system is operating.



HDD

Indicates IDE channel activity. On the SC743T-645, this LED indicates SATA drive activity when flashing.

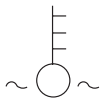


NIC1

Indicates network activity on LAN1 when flashing.

NIC2

Indicates network activity on LAN2 when flashing.



Overheat/Fan Fail

When this LED flashes, it indicates a fan failure. When on continuously it indicates an overheat condition, which may be caused by cables obstructing the airflow in

the system or the ambient room temperature being too warm. Check the routing of the cables and make sure all fans are present and operating normally. You should also check to make sure that the chassis covers are installed. Finally, verify that the heatsinks are installed properly (see Chapter 5). This LED will remain flashing or on as long as the indicated condition exists.

Power Fail (not used)

This LED indicates a power supply module has failed in a redundant power supply system. The 4021A-T2 only has a single power supply, so this LED is not used.

3-4 Serial ATA Drive Carrier LED

Each Serial ATA drive carrier has a green LED. When illuminated, this green LED (on the front of the Serial ATA drive carrier) indicates drive activity. A connection to the Serial ATA backplane enables this LED to blink on and off when that particular drive is being accessed.

Note: The second LED on the SATA carriers is not used.

Notes

Chapter 4

System Safety

4-1 Electrical Safety Precautions

Basic electrical safety precautions should be followed to protect yourself from harm and the 4021A-T2 from damage:

- Be aware of the locations of the power on/off switch on the chassis as well as the room's emergency power-off switch, disconnection switch or electrical outlet. If an electrical accident occurs, you can then quickly remove power from the system.

- Do not work alone when working with high voltage components.

- Power should always be disconnected from the system when removing or installing main system components, such as the serverboard, memory modules and peripheral drives. When disconnecting power, you should first power down the system with the operating system first and then unplug the power cords of all the power supply units in the system.

- When working around exposed electrical circuits, another person who is familiar with the power-off controls should be nearby to switch off the power if necessary.

- Use only one hand when working with powered-on electrical equipment. This is to avoid making a complete circuit, which will cause electrical shock. Use extreme caution when using metal tools, which can easily damage any electrical components or circuit boards they come into contact with.

- Do not use mats designed to decrease static electrical discharge as protection from electrical shock. Instead, use rubber mats that have been specifically designed as electrical insulators.

- The power supply power cords must include a grounding plug and must be plugged into grounded electrical outlets.

- Serverboard Battery: **CAUTION** - There is a danger of explosion if the onboard battery is installed upside down, which will reverse its polarites (see Figure 4-1). This battery must be replaced only with the same or an equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.

- CD-ROM Laser: **CAUTION** - this server may have come equipped with a CD-ROM drive. To prevent direct exposure to the laser beam and hazardous radiation exposure, do not open the enclosure or use the unit in any unconventional way.

- Mainboard replaceable soldered-in fuses: Self-resetting PTC (Positive Temperature Coefficient) fuses on the mainboard must be replaced by trained service technicians only. The new fuse must be the same or equivalent as the one replaced. Contact technical support for details and support.

4-2 General Safety Precautions

Follow these rules to ensure general safety:

- Keep the area around the 4021A-T2 clean and free of clutter.

- The 4021A-T2 weighs approximately 57 lbs (26.5 kg) when fully loaded. When lifting the system, two people at either end should lift slowly with their feet spread out to distribute the weight. Always keep your back straight and lift with your legs.

- Place the chassis top cover and any system components that have been removed away from the system or on a table so that they won't accidentally be stepped on.

- While working on the system, do not wear loose clothing such as neckties and unbuttoned shirt sleeves, which can come into contact with electrical circuits or be pulled into a cooling fan.

- Remove any jewelry or metal objects from your body, which are excellent metal conductors that can create short circuits and harm you if they come into contact with printed circuit boards or areas where power is present.

- After accessing the inside of the system, close the system back up and secure it to the rack unit with the retention screws after ensuring that all connections have been made.

4-3 ESD Precautions

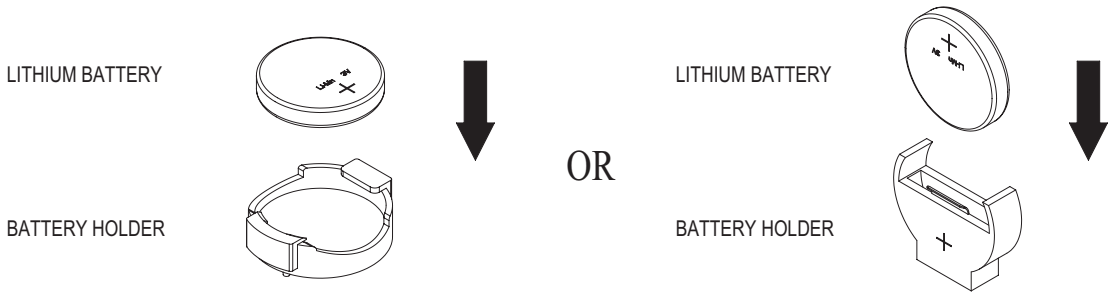
Electrostatic discharge (ESD) is generated by two objects with different electrical charges coming into contact with each other. An electrical discharge is created to neutralize this difference, which can damage electronic components and printed circuit boards. The following measures are generally sufficient to neutralize this difference before contact is made to protect your equipment from ESD:

- Use a grounded wrist strap designed to prevent static discharge.
- Keep all components and printed circuit boards (PCBs) in their antistatic bags until ready for use.
- Touch a grounded metal object before removing the board from the antistatic bag.
- Do not let components or PCBs come into contact with your clothing, which may retain a charge even if you are wearing a wrist strap.
- Handle a board by its edges only; do not touch its components, peripheral chips, memory modules or contacts.
- When handling chips or modules, avoid touching their pins.
- Put the serverboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the serverboard.

4-4 Operating Precautions

Care must be taken to assure that the chassis cover is in place when the 4021A-T2 is operating to ensure proper cooling. Out of warranty damage to the 4021A-T2 system can occur if this practice is not strictly followed.

Figure 4-1. Installing the Onboard Battery



Chapter 5

Advanced Serverboard Setup

This chapter covers the steps required to install processors and heatsinks to the H8DAE-2 serverboard, connect the data and power cables and install add-on cards. All serverboard jumpers and connections are described and a layout and quick reference chart are included in this chapter. Remember to close the chassis completely when you have finished working on the serverboard to protect and cool the system sufficiently.

5-1 Handling the Serverboard

Static electrical discharge can damage electronic components. To prevent damage to printed circuit boards, it is important to handle them very carefully (see Chapter 4). Also note that the size and weight of the serverboard can cause it to bend if handled improperly, which may result in damage. To prevent the serverboard from bending, keep one hand under the center of the board to support it when handling. The following measures are generally sufficient to protect your equipment from static discharge.

Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing any board from its antistatic bag.
- Handle a board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the serverboard, add-on cards and peripherals back into their antistatic bags when not in use.

Unpacking

The serverboard is shipped in antistatic packaging to avoid static damage. When unpacking the board, make sure the person handling it is static protected.

5-2 Mounting the Serverboard into a Chassis

All serverboards and motherboards have standard mounting holes to fit different types of chassis. Make sure that the locations of all the mounting holes for both the serverboard and the chassis match. Although a chassis may have both plastic and metal mounting fasteners, metal ones are highly recommended because they ground the serverboard to the chassis. Make sure that the metal standoffs click in or are screwed in tightly.

Check the compatibility of the serverboard ports and the I/O shield

The H8DAE-2 serverboard requires a chassis that can support Extended ATX boards of 12" x 13.05" in size, such as the SC743T-645. Make sure that the I/O ports on the serverboard align with their respective holes in the I/O shield at the rear of the chassis.

Mounting the serverboard onto the mainboard tray in the chassis

Carefully mount the serverboard onto the mainboard tray by aligning the serverboard mounting holes with the raised metal standoffs in the tray. Insert screws into all the mounting holes in the serverboard that line up with the standoffs. Then use a screwdriver to secure the serverboard to the mainboard tray - tighten until just snug (if too tight you might strip the threads). Metal screws provide an electrical contact to the serverboard ground to provide a continuous ground for the system.

5-3 Processor and Heatsink Installation

Exercise extreme caution when handling and installing the processor. Always connect the power cord last and always remove it before adding, removing or changing any hardware components.

Installing the Processor (install to the CPU#1 socket first)

1. Begin by removing the cover plate that protects the CPU. Lift the lever on CPU socket #1 until it points straight up. With the lever raised, lift open the silver CPU retention plate.

2. Use your thumb and your index finger to hold the CPU. Locate and align pin 1 of the CPU socket with pin 1 of the CPU. Both are marked with a triangle.

3. Align pin 1 of the CPU with pin 1 of the socket. Once aligned, carefully place the CPU into the socket. *Do not drop the CPU on the socket, move the CPU horizontally or vertically or rub the CPU against the socket or against any pins of the socket, which may damage the CPU and/or the socket*.

4. With the CPU inserted into the socket, inspect the four corners of the CPU to make sure that it is properly installed and flush with the socket. Then, gently lower the silver CPU retention plate into place.

5. Carefully press the CPU socket lever down until it locks into its retention tab. For a dual-processor system, repeat these steps to install another CPU into the CPU#2 socket.

Note: if using a single processor, only the CPU1 DIMM slots are addressable for a maximum of 16 GB memory.

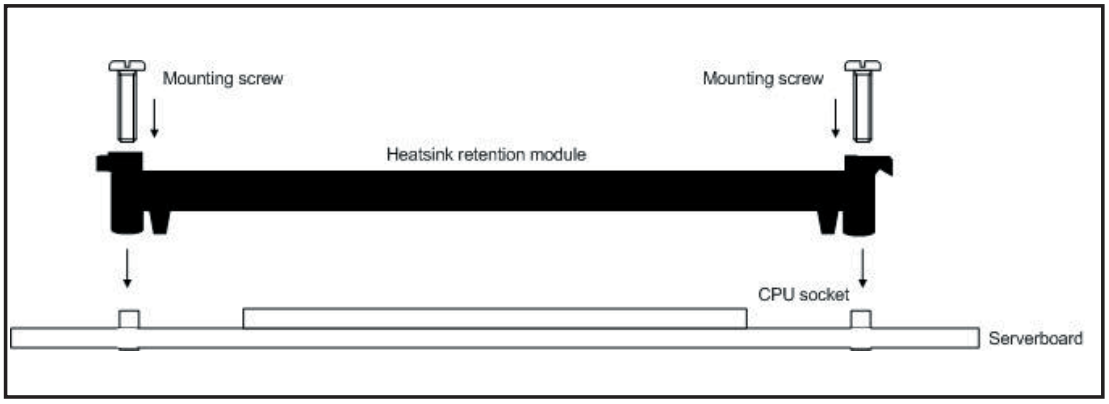
Installing the Heatsink Retention Modules

Two heatsink retention modules (BKT-0012L) and four screws are included in the retail box. Once installed, these are used to help attach the heatsinks to the CPUs.

To install, align the module with the standoffs of the preinstalled CPU backplate and with the four feet on the module contacting the serverboard. Secure the retention module to the backplate with two of the screws provided. See Figure 2-1. Repeat for the second CPU socket.

Note: BKT-0012L is included for use with non-Supermicro heatsinks only. When installing Supermicro heatsinks, only BKT-0011L (the CPU backplate) is needed. The BKT-0012L retention module was designed to provide compatibility with clip-and-cam type heatsinks from third parties.

Figure 5-1. CPU Heatsink Retention Module Installation



Installing the Heatsink

The use of active type heatsinks (except for 1U systems) are recommended. Connect the heatsink fans to the appropriate fan headers on the serverboard. To install the heatsinks, please follow the installation instructions included with your heatsink package (not included).

5-4 Connecting Cables

Now that the processors are installed, the next step is to connect the cables to the serverboard. These include the data (ribbon) cables for the peripherals and control panel and the power cables.

Connecting Data Cables

The ribbon cables used to transfer data from the peripheral devices have been carefully routed in preconfigured systems to prevent them from blocking the flow of cooling air that moves through the system from front to back. If you need to disconnect any of these cables, you should take care to reroute them as they were originally after reconnecting them (make sure the red wires connect to the pin 1 locations). If you are configuring the system, keep the airflow in mind when routing the cables. The following data cables (with their serverboard connector locations noted) should be connected. See the serverboard layout diagram in this chapter for connector locations.

- Floppy Drive cable (JFDD1)
- Serial ATA cables (SATA0 ~ SATA5)
- Control Panel cable (JF1, see next page)
- USB cable for front access (USB4/5)

Connecting Power Cables

The H8DAE-2 has a 24-pin primary ATX power supply connector designated "JPW1" for connection to the ATX power supply. Connect the appropriate connector from the power supply to the "ATX Power" connector to supply power to the serverboard. See the Connector Definitions section in this chapter for power connector pin definitions.

In addition, your power supply must be connected to the 8-pin Processor Power connection at JPW2. For systems with high load configurations, a third power connector at PW3 should also be connected to your power supply.

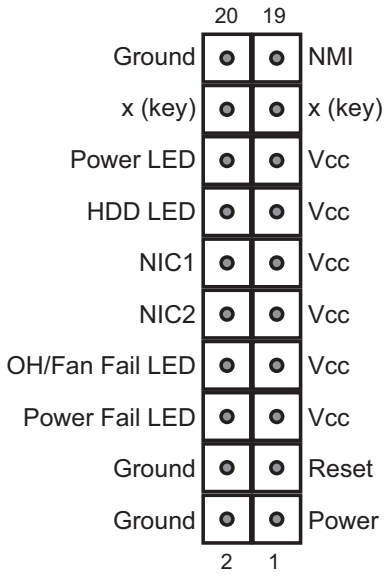
Connecting the Control Panel

JF1 contains header pins for various front control panel connectors. See Figure 5-2 for the pin locations of the various front control panel buttons and LED indicators and refer to section 5-9 for details. Note that even and odd numbered pins are on opposite sides of each header.

All JF1 wires have been bundled into single keyed ribbon cable to simplify their connection. The red wire in the ribbon cable plugs into pin 1 of JF1. Connect the other end of the cable to the Control Panel printed circuit board, located just behind the system status LEDs in the chassis.

See the Connector Definitions section in this chapter for details and pin descriptions of JF1.

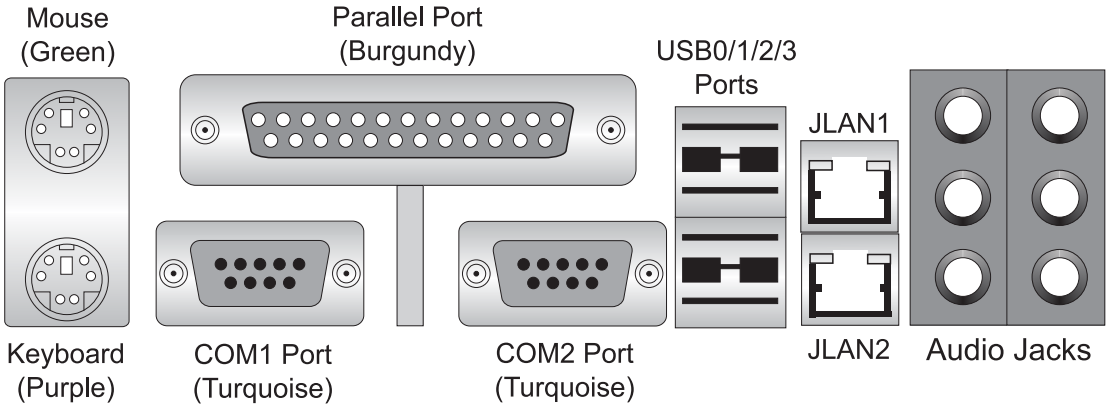
Figure 5-2. Front Control Panel Header Pins (JF1)



5-5 I/O Ports

The I/O ports are color coded in conformance with the PC 99 specification. See Figure 5-3 below for the colors and locations of the various I/O ports.

Figure 5-3. Rear Panel I/O Ports



5-6 Installing Memory

CAUTION

Exercise extreme care when installing or removing memory modules to prevent any possible damage.

1. Insert each memory module vertically into its slot, paying attention to the notch along the bottom of the module to prevent inserting the module incorrectly (see Figure 5-4). See support information below.

2. Gently press down on the memory module until it snaps into place.

Note: each processor has its own built-in memory controller, so the CPU2 DIMMs cannot be addressed if only a single CPU is installed in the CPU1 socket. 128 MB, 256 MB, 512 MB, 1 GB, 2 GB, 4 GB and 8 GB memory modules are supported. It is highly recommended that you remove the power cord from the system before installing or changing any memory modules.

Support

The H8DAE-2 supports single or dual-channel, DDR2-667/533/400 registered ECC SDRAM.

Both interleaved and non-interleaved memory are supported, so you may populate any number of DIMM slots (see note on previous page and charts on following page). The CPU2 DIMM slots can only be accessed when two CPUs are installed (however, the CPU2 DIMM slots are not required to be populated when two CPUs are installed).

Populating two adjacent slots at a time with memory modules of the same size and type will result in interleaved (128-bit) memory, which is faster than non-interleaved (64-bit) memory. See charts on following page.

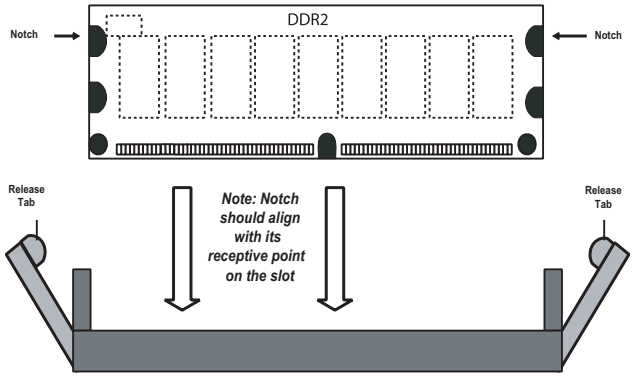
Optimizing memory performance

If two processors are installed, it is better to stagger pairs of DIMMs across both sets of CPU DIMM slots, e.g. first populate CPU1 slots 1A and 1B, then CPU2 slots 1A, and 1B, then the next two CPU1 slots, etc. This balances the load over both CPUs to optimize performance.

Maximum memory: up to 64 GB in a dual-CPU configuration. If only one CPU is installed, maximum supported memory is halved (32 GB).

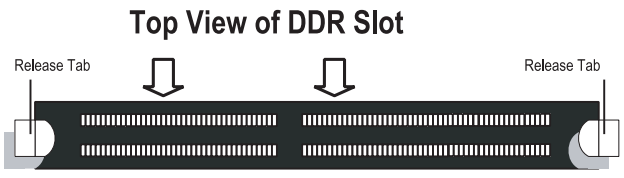
Figure 5-4. Side and Top Views of DDR Installation

To Install:
Insert module vertically and press down until it snaps into place. The release tabs should close - if they do not you should close them yourself.



Note the notch in the slot and on the bottom of the DIMM. These prevent the DIMM from being installed incorrectly.

To Remove:
Use your thumbs to gently push each release tab outward to release the DIMM from the slot.



Populating Memory Banks for 128-bit Operation							
CPU1 DIMM1A	CPU1 DIMM1B	CPU1 DIMM2A	CPU1 DIMM2B	CPU2 DIMM1A	CPU2 DIMM1B	CPU2 DIMM2A	CPU2 DIMM2B
X	X						
X	X			X	X		
X	X					X	X
X	X			X	X	X	X
X	X	X	X				
X	X	X	X	X	X		
X	X	X	X			X	X
X	X	X	X	X	X	X	X
		X	X				
		X	X	X	X		
		X	X			X	X
		X	X	X	X	X	X

Notes: X indicates a populated DIMM slot. If adding at least four DIMMs (with two CPUs installed), the configurations with DIMMs spread over both CPUs (and not like the configuration in row 5) will result in optimized performance. Note that the first two DIMMs must be installed in the CPU1 memory slots.

Populating Memory Banks for 64-bit Operation							
CPU1 DIMM1A	CPU1 DIMM1B	CPU1 DIMM2A	CPU1 DIMM2B	CPU2 DIMM1A	CPU2 DIMM1B	CPU2 DIMM2A	CPU2 DIMM2B
X							
		X					
X		X					
X				X			
X						X	
		X		X			
		X				X	

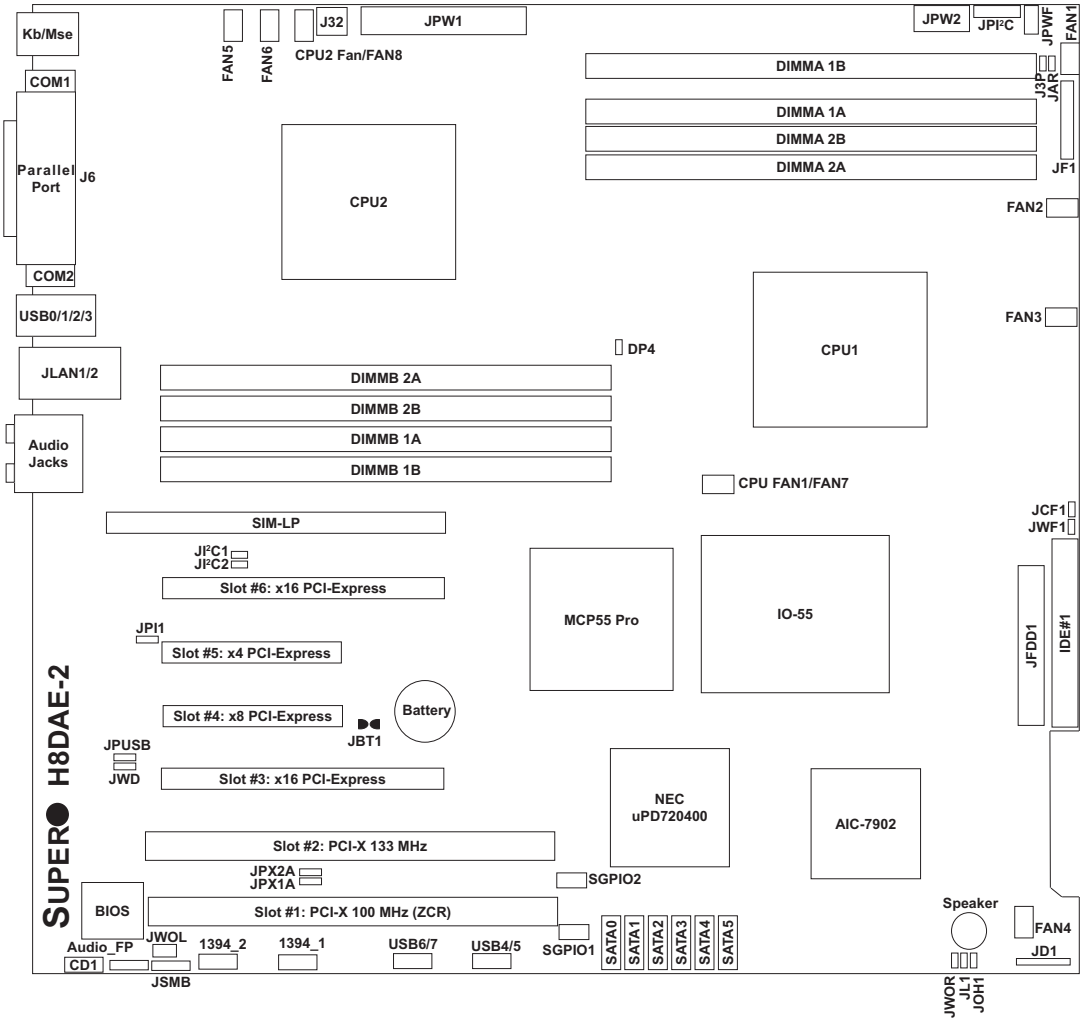
5-7 Adding PCI Expansion Cards

The SC743T-645 chassis accommodates up to seven standard size PCI cards that install directly to the serverboard (riser cards are not needed).

To install a PCI expansion card, first open the release tab on the shield that corresponds to the PCI slot you wish to populate. Insert the card into the correct slot, pushing down with your thumbs evenly on both sides. Finish by pushing the release tab back to its original (locked) position. Follow this procedure when adding a card to additional slots.

5-8 Serverboard Details

Figure 5-5. H8DAE-2 Serverboard Layout
(not drawn to scale)



Note: Jumpers not indicated are for test purposes only.

H8DAE-2 Quick Reference

Jumper	Description	Default Setting
J3P	3rd Power Fail Detect En/Disable	Closed (Enabled)
JBT1	CMOS Clear	(See Section 5-10)
JCF1	Compact Flash Master/Slave	Closed (Master)
JD1	Onboard Speaker	Pins 6-7 (Enabled)
JI²C1/JI²C2	I²C to PCI Enable/Disable	Closed (Enabled)
JPI1	IEEE 1394 Enable/Disable	Pins 1-2 (Enabled)
JPUSB	USB Power Select	Pins 1-2 (Standard Pwr)
JPX1A/JPX2A	PCI-X Slot #1/2 Freq. Select	Open (Auto)
JWD	Watch Dog	Pins 1-2 (Reset)

Connector	Description
1394_1/1394_2	IEEE 1394 (Firewire) Headers
Audio_FP	Audio Output
CD1	Audio In for CD
COM1/COM2	COM1/COM2 Serial Ports
FAN 1-8	Chassis/CPU Fan Headers
IDE#1	IDE HDD Connector
J32	4-pin Auxiliary Power Connector
JAR	Power Fail Alarm Reset Header
J137	Audio Ports
JD1	Onboard Speaker/Keylock/Power LED
JF1	Front Panel Connector
JFDD1	Floppy Disk Drive Connector
JL1	Chassis Intrusion Header
JLAN1/2	Gigabit Ethernet (RJ45) Ports
JOH1	Overheat Warning Header
JPI²C	Power Supply I²C Header
JPW1	24-pin Main ATX Power Connector
JPW2	8-pin Processor Power Connector
JPWF	3rd Power Supply Alarm Header
JSMB	System Management Bus Header
JWF1	Compact Flash Card Power Connector
JWOL/JWOR	Wake-On-LAN Header/Wake-On-Ring Header
SATA0 ~ SATA5	SATA Ports
SGPIO-1/SGPIO-2	Serial General Purpose Input/Output Headers
SIM1U	IPMI 2.0 Card Slot
USB0/1/2/3, USB4/5/6/7	Universal Serial Bus (USB) Ports, USB Headers

5-9 Connecting Cables

ATX Power Connector

The primary ATX power supply connector (JPW1) meets the SSI (Superset ATX) 24-pin specification. Refer to the table on the right for the pin definitions of the ATX 24-pin power connector. This connection supplies power to the chipset, fans and memory.

Note: You must also connect the 8-pin (JPW2) and 4-pin (J32) power connectors to your power supply (see below).

ATX Power 24-pin Connector Pin Definitions (JPW1)

Pin#	Definition	Pin #	Definition
13	+3.3V	1	+3.3V
14	-12V	2	+3.3V
15	COM	3	COM
16	PS_ON	4	+5V
17	COM	5	COM
18	COM	6	+5V
19	COM	7	COM
20	Res (NC)	8	PWR_OK
21	+5V	9	5VSB
22	+5V	10	+12V
23	+5V	11	+12V
24	COM	12	+3.3V

Processor Power Connector

In addition to the primary ATX power connector (above), the 12v, 8-pin processor power connector at JPW2 must also be connected to your power supply. This connection supplies power to the CPUs. See the table on the right for pin definitions.

Processor Power Connector Pin Definitions (JPW2)

Pins	Definition
1 through 4	Ground
5 through 8	+12V

Required Connection

Auxiliary Power Connector

The 4-pin auxiliary power connector at J32 must also be connected to your power supply. This connection supplies extra power that may be needed for high loads. See the table on the right for pin definitions.

Auxiliary Power Connector Pin Definitions (J32)

Pins	Definition
1 & 2	Ground
3 & 4	+12V

Required Connection

Power LED

The Power LED connection is located on pins 15 and 16 of JF1. Refer to the table on the right for pin definitions.

Power LED Pin Definitions (JF1)

Pin#	Definition
15	Vcc
16	Control

HDD LED

The HDD (IDE Hard Disk Drive) LED connection is located on pins 13 and 14 of JF1. Attach the IDE hard drive LED cable to display disk activity. Refer to the table on the right for pin definitions.

HDD LED Pin Definitions (JF1)	
Pin#	Definition
13	Vcc
14	HD Active

NIC1 LED

The NIC1 (Network Interface Controller) LED connection is located on pins 11 and 12 of JF1. Attach the NIC1 LED cable to display network activity. Refer to the table on the right for pin definitions.

NIC1 LED Pin Definitions (JF1)	
Pin#	Definition
11	Vcc
12	NIC1 Active

NIC2 LED

The NIC2 (Network Interface Controller) LED connection is located on pins 9 and 10 of JF1. Attach the NIC2 LED cable to display network activity. Refer to the table on the right for pin definitions.

NIC2 LED Pin Definitions (JF1)	
Pin#	Definition
9	Vcc
10	NIC2 Active

Overheat/Fan Fail LED

Connect an LED to the OH connection on pins 7 and 8 of JF1 to provide advanced warning of chassis overheating. Refer to the table on the right for pin definitions and status indicators.

OH/Fan Fail LED Pin Definitions (JF1)	
Pin#	Definition
7	Vcc
8	Control

OH/Fan Fail LED Status	
State	Indication
Solid	Overheat
Blinking	Fan fail

Power Fail LED

The Power Fail LED connection is located on pins 5 and 6 of JF1. Refer to the table on the right for pin definitions. This feature is only available for systems with redundant power supplies.

Power Fail LED Pin Definitions (JF1)

Pin#	Definition
5	Vcc
6	Control

Reset Button

The Reset Button connection is located on pins 3 and 4 of JF1. Attach it to the hardware reset switch on the computer case. Refer to the table on the right for pin definitions.

Reset Button Pin Definitions (JF1)

Pin#	Definition
3	Reset
4	Ground

Power Button

The Power Button connection is located on pins 1 and 2 of JF1. Momentarily contacting both pins will power on/off the system. This button can also be configured to function as a suspend button (see the Power Button Mode setting in BIOS). To turn off the power when set to suspend mode, depress the button for at least 4 seconds. Refer to the table on the right for pin definitions.

Power Button Pin Definitions (JF1)

Pin#	Definition
1	PW_ON
2	Ground

Universal Serial Bus Ports (USB0/1/2/3)

Four Universal Serial Bus ports (USB2.0) are located beside the JLAN1/2 ports. See the table on the right for pin definitions.

Universal Serial Bus Ports Pin Definitions (USB0/1/2/3)

USB0		USB1	
Pin #	Definition	Pin #	Definition
1	+5V	1	+5V
2	PO-	2	PO-
3	PO+	3	PO+
4	Ground	4	Ground

USB Headers

Four additional USB2.0 headers (USB4/5 and USB6/7) are included on the serverboard. These may be connected to provide front side access. A USB cable (not included) is needed for the connection. See the table on the right for pin definitions.

Universal Serial Bus Headers Pin Definitions (USB4/5/6/7)

USB2		USB3/4	
Pin #	Definition	Pin #	Definition
1	+5V	1	+5V
2	PO-	2	PO-
3	PO+	3	PO+
4	Ground	4	Ground
5	Key	5	No connection

Serial Ports

The COM1 and COM2 serial ports are located under the parallel port. Refer to Figure 2-3 for locations and the table on the right for pin definitions.

Serial Port Pin Definitions (COM1/COM2)

Pin #	Definition	Pin #	Definition
1	DCD	6	DSR
2	RXD	7	RTS
3	TXD	8	CTS
4	DTR	9	RI
5	Ground	10	NC

Note: NC indicates no connection.

Fan Headers

The H8DAE-2 has eight fan headers, which are designated FAN1 through FAN8. Fans are Pulse Width Modulated (PWM) and their speed is controlled via Thermal Management with a BIOS setting. See the table on the right for pin definitions.

Note: when using active heatsinks (those with fans), connect the heatsink fan for CPU1 to the FAN7 header and the heatsink fan for CPU2 to the FAN8 header.

Fan Header Pin Definitions (FAN1-8)

Pin#	Definition
1	Ground (Black)
2	+12V (Red)
3	Tachometer
4	PWM Control

JLAN1/2 (Ethernet Ports)

Two Gigabit Ethernet ports (designated JLAN1 and JLAN2) are located beside the COM2 port. These Ethernet ports accept RJ45 type cables.

Notes: JLAN1 is the top port and JLAN2 is the bottom port. Wake-On-LAN from S3, S4, and S5 are supported by JLAN1, JLAN2 supports WOL from S1 only.

Power LED/Speaker

On JD1, pins 1, 2, and 3 are for the power LED and pins 4 through 7 are for the speaker. See the tables on the right for pin definitions.

Note: The speaker connector pins are for use with an external speaker. If you wish to use the onboard speaker, you should close pins 6 and 7 with a jumper.

PWR LED Connector Pin Definitions (JD1)

Pin#	Definition
1	+Vcc
2	Control
3	Control

Speaker Connector Pin Definitions (JD1)

Pin#	Definition
4	Red wire, +5V
5	No connection
6	Buzzer signal
7	Speaker data

ATX PS/2 Keyboard and PS/2 Mouse Ports

The ATX PS/2 keyboard and the PS/2 mouse ports are located on the IO backplane. The mouse is the top (green) port. See the table on the right for pin definitions.

PS/2 Keyboard and Mouse Port Pin Definitions

Pin#	Definition
1	Data
2	NC
3	Ground
4	VCC
5	Clock
6	NC

Overheat LED

Connect an LED to the JOH1 header to provide warning of chassis overheating. See the table on the right for pin definitions.

Overheat LED Pin Definitions (JOH1)

Pin#	Definition
1	3.3V
2	OH Active

CD1 Header

The 4-pin CD1 header allows you to use the onboard sound for audio CD playback. Connect the audio cable from your CC drive to this header. See the table on the right for pin definitions.

CD1 Pin Definitions

Pin#	Definition
1	Right Signal
2	Ground
3	Ground
4	Left Signal

Wake-On-LAN

The Wake-On-LAN header is designated JWOL. See the table on the right for pin definitions. You must have a LAN card with a Wake-On-LAN connector and cable to use the Wake-On-LAN feature.

(Note: Wake-On-LAN from S3, S4, S5 are supported by LAN1. LAN2 supports Wake-On-LAN from S1 only.)

Wake-On-LAN Pin Definitions (JWOL)

Pin#	Definition
1	+5V Standby
2	Ground
3	Wake-up

Wake-On-Ring

The Wake-On-Ring header is designated JWOR. This function allows your computer to receive and "wake-up" by an incoming call to the modem when in suspend state. See the table on the right for pin definitions. You must have a Wake-On-Ring card and cable to use this feature.

Wake-On-Ring Pin Definitions (JWOR)

Pin#	Definition
1	Ground (Black)
2	Wake-up

Power Supply I²C Header

The JPI²C header is for I²C, which may be used to monitor the status of the power supply, fans and system temperature. See the table on the right for pin definitions.

I²C Header Pin Definitions (JPI²C)

Pin#	Definition
1	Clock
2	Data
3	PWR Fail
4	Gnd
5	+3.3V

SMBus Header

The header at JSMB is for the System Management Bus. Connect the appropriate cable here to utilize SMB on the system. See the table on the right for pin definitions.

SMBus Header Pin Definitions (JSMB)

Pin#	Definition
1	Data
2	Ground
3	Clock
4	No Connection

3rd Power Supply Alarm Header

Connect a cable from your power supply to JPWF to provide you with warning of a power supply failure. The warning signal is passed through the PWR_LED pin to indicate a power failure. See the table on the right for pin definitions.

3rd Power Supply Alarm Header Pin Definitions (JPWF)

Pin#	Definition
1	P/S 1 Fail Signal
2	P/S 2 Fail Signal
3	P/S 3 Fail Signal
4	Reset (from MB)

Note: This feature is only available when using redundant power supplies.

Power Fail Alarm Reset Header

Connect JAR to the alarm reset button on your chassis (if available) or to a microswitch to allow you to turn off the alarm that sounds when a power supply module fails. See the table on the right for pin definitions.

Alarm Reset Header Pin Definitions (JAR)

Pin#	Definition
1	Ground
2	Reset Signal

Compact Flash Power Header

A Compact Flash Card Power header is located at JWF1. For the Compact Flash Card to work properly, you will first need to connect the device's power cable to JWF1 and correctly set the Compact Flash Jumper (JCF1).

Compact Flash Power Header Pin Definitions (JWF1)

Pin#	Definition
1	+5V
2	Ground
3	Signal

Chassis Intrusion

A Chassis Intrusion header is located at JL1. Attach the appropriate cable to inform you of a chassis intrusion.

Chassis Intrusion Pin Definitions (JL1)

Pin#	Definition
1	Battery voltage
2	Intrusion signal

Firewire Headers

The headers designated 1394_1 and 1394_2 are firewire headers. Attach the appropriate cable to utilize a firewire device on your system.

Firewire HeadersPin Definitions (1394_1/1394_2)

Pin #	Definition	Pin #	Definition
1	TPA+	6	TPA-
2	Ground	7	Ground
3	TPB+	8	TPB-
4	VP	9	VP
5	Key	10	S-Ground

SGPIO

SGPIO1 and SGPIO2 (Serial General Purpose Input/Output) provide a bus between the SATA controller and the SATA drive backplane to provide SATA enclosure management functions. Connect the appropriate cables from the backplane to the SGPIO1 and SGPIO2 header(s) to utilize SATA management functions on your system.

SGPIO Header Pin Definitions (SGPIO1, SGPIO2)

Pin#	Definition	Pin #	Definition
1	NC	2	NC
3	Ground	4	Data
5	Load	6	Ground
7	NC	8	NC

Note: NC indicates no connection.

Audio Output Header

The Audio_FP header gives you the option of directing the audio output to Line In/Line Out/ Mic jacks that may be added to the front of the chassis (requires additional hardware, not included). See the tables on the right for pin definitions. Note that the pin definitions differ for the 1.1 and 1.2 revision levels of the serverboard PCB (found near the model name on the serverboard).

Audio Output Header: rev. 1.1 Pin Definitions (Audio_FP)

Pin#	Definition
1	MIC left channel
2	Ground
3	MIC right channel
4	Front panel audio detect
5	Front audio jack detect
6	MIC jack detect
7	Line out right channel
8	Key
9	Line out left channel
10	Line out jack detect

Notes: Pins 6 and 10 are not active when a header is used for front side audio access.

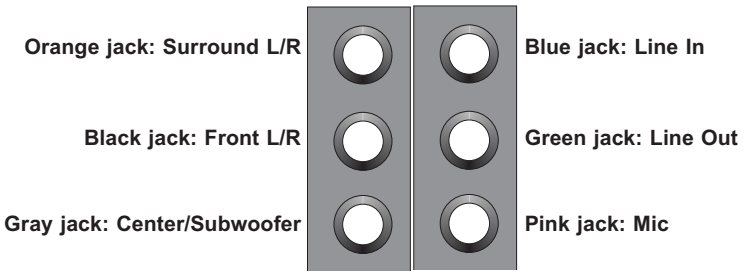
Audio Output Header: rev. 1.2 Pin Definitions (Audio_FP)	
Pin#	Definition
1	MIC left channel
2	Ground
3	MIC right channel
4	Front panel audio detect
5	Line out right channel
6	MIC jack detect
7	Front audio jack detect
8	Key
9	Line out left channel
10	Line out jack detect

HD Audio

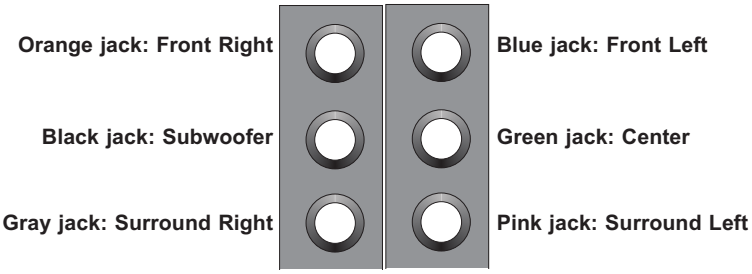
HD (High Definition) audio is provided with an onboard Realtek ALC883 audio chip. The H8DAE-2 features 6-channel (5.1) sound for front L&R, rear L&R, center and subwoofer speakers with the use of a mic or line-in device. Without the use of a mic or line-in device, 8-channel sound (7.1) may be used. Sound is output through the Line In, Line Out and MIC jacks (see below). There is also a CD1 header on the board that can be used for audio.

Refer to the diagrams below for the port definitions when employing 5.1 or 7.1 sound on your system.

Audio Jacks: 5.1 Sound



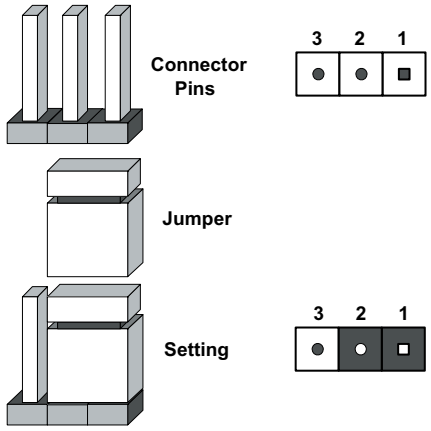
Audio Jacks: 7.1 Sound



5-10 Jumper Settings

Explanation of Jumpers

To modify the operation of the serverboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. See the diagram at right for an example of jumping pins 1 and 2. Refer to the serverboard layout page for jumper locations.



Note: On two-pin jumpers, "Closed" means the jumper is on and "Open" means the jumper is off the pins.

CMOS Clear

JBT1 is used to clear CMOS and will also clear any passwords. Instead of pins, this jumper consists of contact pads to prevent accidentally clearing the contents of CMOS.

To clear CMOS

1. First power down the system and unplug the power cord(s).
2. With the power disconnected, short the CMOS pads with a metal object such as a small screwdriver for at least four seconds.
3. Remove the screwdriver (or shorting device).
4. Reconnect the power cord(s) and power on the system.

Notes:

Do not use the PW_ON connector to clear CMOS.The onboard battery does not need to be removed when clearing CMOS, however you must short JBT1 for at least four seconds.


JBT1 contact pads

3rd Power Supply Fail Signal Enable/Disable

This feature is only for systems that are equipped with three redundant power supply units and thus is not used on the 4021A-T2.

3rd Power Supply Fail Signal Jumper Settings (J3P)	
Jumper Setting	Definition
Open	Disabled
Closed	Enabled

I²C to PCI Enable/Disable

The JI²C1/2 pair of jumpers allows you to connect the System Management Bus to the PCI expansion slots. The default setting is closed (on) for both jumpers to enable the connection. Both connectors must be set the same (JI²C1 is for data and JI²C2 is for the clock). See the table on right for jumper settings.

I²C to PCI Enable/Disable Jumper Settings (JI²C1/2)	
Jumper Setting	Definition
Closed	Enabled
Open	Disabled

Watch Dog

JWD controls Watch Dog, a system monitor that takes action when a software application freezes the system. Jumping pins 1-2 will cause WD to reset the system if an application is hung up. Jumping pins 2-3 will generate a non-maskable interrupt signal for the application that is hung up. See the table on the right for jumper settings. Watch Dog can also be enabled via BIOS.

Watch Dog Jumper Settings (JWD)	
Jumper Setting	Definition
Pins 1-2	Reset
Pins 2-3	NMI
Open	Disabled

Note: When enabled, the user needs to write their own application software in order to disable the Watch Dog timer.

Compact Flash Master/Slave

The JCF1 jumper allows you to assign either master or slave status a compact flash card installed in IDE1. See the table on the right for jumper settings.

Compact Flash Master/Slave Jumper Settings (JCF1)	
Jumper Setting	Definition
Closed	Master
Open	Slave

Onboard Speaker Enable/Disable

The JD1 header allows you to use either an external speaker or the internal (onboard) speaker. To use the internal onboard speaker, close pins 6 and 7 with a jumper. To use an external speaker, remove the jumper and connect the speaker wires to pins 4 (+5V) and 7 (control signal). See the table on the right for settings and the table associated with the Power LED/ Keylock/Speaker connection (previous section) for jumper settings.

Onboard Speaker Enable/Disable Pin Definitions (JD1)	
Pins	Definition
6 and 7	Jump for onboard speaker
4 and 7	Attach external speaker wires

Note: Pins 4-7 are used only for the on-board speaker.

PCI-X Slot Speed

Jumpers JPX1A and JPX2A on the H8DAE-2 can be used to change the speed of PCI-X slot #1 and PCI-X slot #2, respectively. See the tables on the right for jumper settings.

PCI-X Slot Speed Jumper Settings (JPX1A/JPX2A)	
Jumper Setting	Definition
Open	Auto
Pins 1-2	PCI-X 66 MHz
Pins 2-3	PCI 66 MHz

Note: JPX1A controls the speed for PCI-X slot #1 and JPX1B controls the speed for PCI-X slot #2. The default setting for both is Auto.

USB Power Select

Jumper JPUSB is used to select the power state for the USB ports. The Standard setting means power is applied to the ports only when the system is powered on. The Dual Power setting will allow the USB ports to have power whenever the system's AC power cord is connected, regardless of whether the system is powered on or not. See the table on right for jumper settings.

USB Power Select Jumper Settings (JPUSB)	
Jumper Setting	Definition
Pins 1-2	Standard Power
Pins 2-3	Dual Power

IEEE 1394 Enable/Disable

JPI1 is used to enable or disable the onboard IEEE (Firewire) headers. See the table on right for jumper settings.

IEEE Enable/Disable Jumper Settings (JPI1)	
Jumper Setting	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled

5-11 Onboard Indicators

JLAN1/JLAN2 LEDs

The Ethernet ports (located beside the VGA port) have two LEDs. On each Gb LAN port, one LED indicates activity while the other LED may be amber or off to indicate the speed of the connection. See the table on the right for the functions associated with the connection speed LED.

JLAN LED (Connection Speed Indicator)	
LED Color	Definition
Off	10/100 MHz
Amber	1 GHz

+3.3V Standby LED

DP4 is an Onboard Power LED. When this LED is lit, it means power is present on the serverboard. In suspend mode this LED will blink on and off. Be sure to turn off the system and unplug the power cord(s) before removing or installing components.

+3.3V Standby LED (DP4)	
State	System Status
On	Standby power present on serverboard
Off	No power connected
Flashing	System in standby state

5-12 Floppy, IDE, Parallel Port and SATA Drives

Use the following information to connect the floppy and hard disk drive cables.

- The floppy disk drive cable has seven twisted wires.
- A red mark on a wire typically designates the location of pin 1.
- A single floppy disk drive ribbon cable has 34 wires and two connectors to provide for two floppy disk drives. The connector with twisted wires always connects to drive A, and the connector that does not have twisted wires always connects to drive B.
- The 80-wire ATA133 IDE hard disk drive cable that came with your system has two connectors to support two drives. This special cable should be used to take advantage of the speed this new technology offers. The blue connector connects to the onboard IDE connector interface and the other connector(s) to your hard drive(s). Consult the documentation that came with your disk drive for details on actual jumper locations and settings for the hard disk drive.

Floppy Connector

The floppy connector is located beside the IDE#1 connector. See the table on the right for pin definitions.

Floppy Drive Connector Pin Definitions (JFDD1)

Pin#	Definition	Pin #	Definition
1	GND	2	FDHDIN
3	GND	4	Reserved
5	Key	6	FDEDIN
7	GND	8	Index-
9	GND	10	Motor Enable
11	GND	12	Drive Select B-
13	GND	14	Drive Select A-
15	GND	16	Motor Enable
17	GND	18	DIR-
19	GND	20	STEP-
21	GND	22	Write Data-
23	GND	24	Write Gate-
25	GND	26	Track 00-
27	GND	28	Write Protect-
29	GND	30	Read Data-
31	GND	32	Side 1 Select-
33	GND	34	Diskette

IDE Connector

There are no jumpers to configure the onboard IDE#1 connector. See the table on the right for pin definitions.

IDE Drive Connector Pin Definitions (IDE#1)

Pin#	Definition	Pin #	Definition
1	Reset IDE	2	Ground
3	Host Data 7	4	Host Data 8
5	Host Data 6	6	Host Data 9
7	Host Data 5	8	Host Data 10
9	Host Data 4	10	Host Data 11
11	Host Data 3	12	Host Data 12
13	Host Data 2	14	Host Data 13
15	Host Data 1	16	Host Data 14
17	Host Data 0	18	Host Data 15
19	Ground	20	Key
21	DRQ3	22	Ground
23	I/O Write	24	Ground
25	I/O Read	26	Ground
27	IOCHRDY	28	BALE
29	DACK3	30	Ground
31	IRQ14	32	IOCS16
33	Addr1	34	Ground
35	Addr0	36	Addr2
37	Chip Select 0	38	Chip Select 1
39	Activity	40	Ground

Parallel Port

The parallel (printer) port is designated "Printer". See the table on the right for pin definitions.

Parallel Port Connector Pin Definitions (Printer)

Pin#	Definition	Pin #	Definition
1	Strobe-	2	Auto Feed-
3	Data Bit 0	4	Error-
5	Data Bit 1	6	Init-
7	Data Bit 2	8	SLCT IN-
9	Data Bit 3	10	GND
11	Data Bit 4	12	GND
13	Data Bit 5	14	GND
15	Data Bit 6	16	GND
17	Data Bit 7	18	GND
19	ACK	20	GND
21	BUSY	22	Write Data
23	PE	24	Write Gate
25	SLCT	26	NC

SATA Ports

There are no jumpers to configure the SATA ports, which are designated SATA0 through SATA5. See the table on the right for pin definitions.

SATA Port Pin Definitions (SATA0~SATA5)

Pin #	Definition
1	Ground
2	TXP
3	TXN
4	Ground
5	RXN
6	RXP
7	Ground

5-13 Enabling SATA RAID

Serial ATA (SATA)

Serial ATA (SATA) is a physical storage interface that employs a single cable with a minimum of four wires to create a point-to-point connection between devices. This connection is a serial link. The serial cables used in SATA are thinner than the traditional cables used in Parallel ATA (PATA) and can extend up to one meter in length, compared to only 40 cm for PATA cables. Overall, SATA provides better functionality than PATA.

Installing the OS/SATA Driver

Before installing the OS (operating system) and SATA RAID driver, you must decide if you wish to have the operating system installed as part of a bootable RAID array or installed to a separate non-RAID hard drive. If on a separate drive, you may install the driver either during or after the OS installation. If you wish to have the OS on a SATA RAID array, you must follow the procedure below and install the driver during the OS installation.

Note: the SATA RAID driver is supported by Windows 2000 and XP only.

Building a Driver Diskette

You must first build a driver diskette from the CD-ROM that was included with the system. (You will have to create this disk on a computer that is already running and with the OS installed.) Insert the CD into your CD-ROM drive and start the system. A driver display will appear. Click on the icon labeled "Build Driver Diskettes and Manuals" and follow the instructions to create a floppy disk with the driver on it. Once it's been created, remove the floppy and insert the installation CD for the Windows Operating System you wish to install into the CD-ROM drive of the new system you are about to configure.

Enabling SATA RAID in the BIOS

Before installing the Windows Operating System, you must change some settings in BIOS. Boot up the system and hit the <Del> key to enter the BIOS Setup Utlility. After the Setup Utility loads,

1. Use the arrow keys to move to the Exit menu. Scroll down with the arrow keys to the "Load Optimal Defaults setting and press <Enter>. Select "OK" to confirm, then <Enter> to load the default settings.

2. Use the arrow keys to move to the "Advanced" menu, then scroll down to "IDE Configuration" and press the <Enter> key. Once in the IDE Configuration submenu, scroll down to "Configuration nVidia RAID ROM" and press <Enter> to access the submenu. Highlight the setting "RAID Option ROM" and press enter, change the setting to Enabled and hit <Enter> again. Two new settings should now be displayed: "Master SATA as RAID" and "Slave SATA as RAID". Enable both of these settings, whether you think you'll use them both or not.

3. Hit the <Esc> key twice and scroll to the Exit menu. Select "Save Changes and Exit" and hit <enter>, then hit <Enter> again to verify.

4. After exiting the BIOS Setup Utility, the system will reboot. When prompted during the startup, press the <F10> key when prompted to run the nVidia RAID Utility program.

Using the nVidia RAID Utility

The nVidia RAID Utility program is where you can define the drives you want to include in the RAID array and the mode and type of RAID. Two main windows are shown in the utility (see Figure 5-6). The "Free Disks" window on the left will list all available drives. Use the arrow keys to select and move drives to the window on the right, which lists all drives that are to become part of the RAID array.

Once you have finished selecting the drives and type of RAID you wish to use for your RAID array, press the <F7> key. You will be prompted to verify your choice; if you want to continue with your choices, select "Yes". Note that selecting "Yes" will clear all previous data from the drives you selected to be a part of the array. You are then given the choice of making the RAID array bootable by pressing the the <B> key. After you have finshed, press the <Ctrl> and <X> keys simultaneously. Figure 5-7 shows a list of arrays that have been set up with the utility.

Installing the OS and Drivers

With the Windows OS installation CD in the CD-ROM drive, restart the system. When you see the prompt, hit the <F6> key to enter Windows setup. Eventually a blue screen will appear with a message that begins "Windows could not determine the type of one or more storage devices . . ." When you see the screen, hit the <S> key to "Specify Additional Device", then insert the driver diskette you just created into the floppy drive. Highlight "Manufuacturer Supplied Hardware Support Disk" and hit the <Enter> key. Highlight the first "nVidia RAID" driver shown and press the <Enter> key to install it. Soon a similar blue screen will appear again. Again hit the <S> key, then highlight the second item, "nForce Storage Controller" and press the <Enter> key, then <Enter> again to continue with the Windows setup.

Figure 5-6. SATA RAID Utility: Main Screen

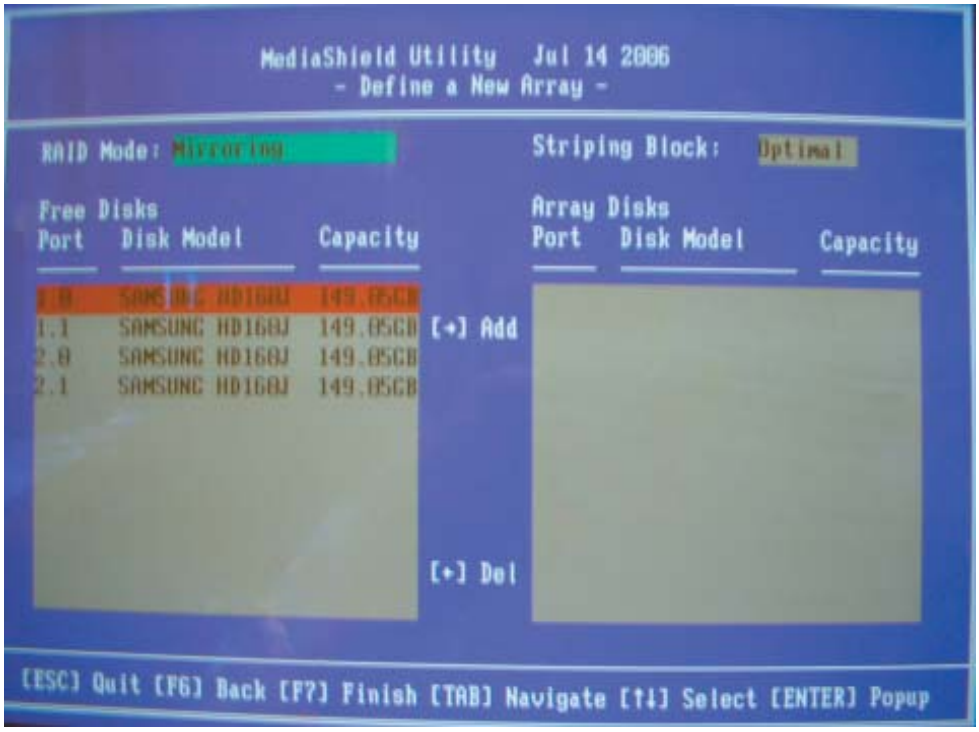
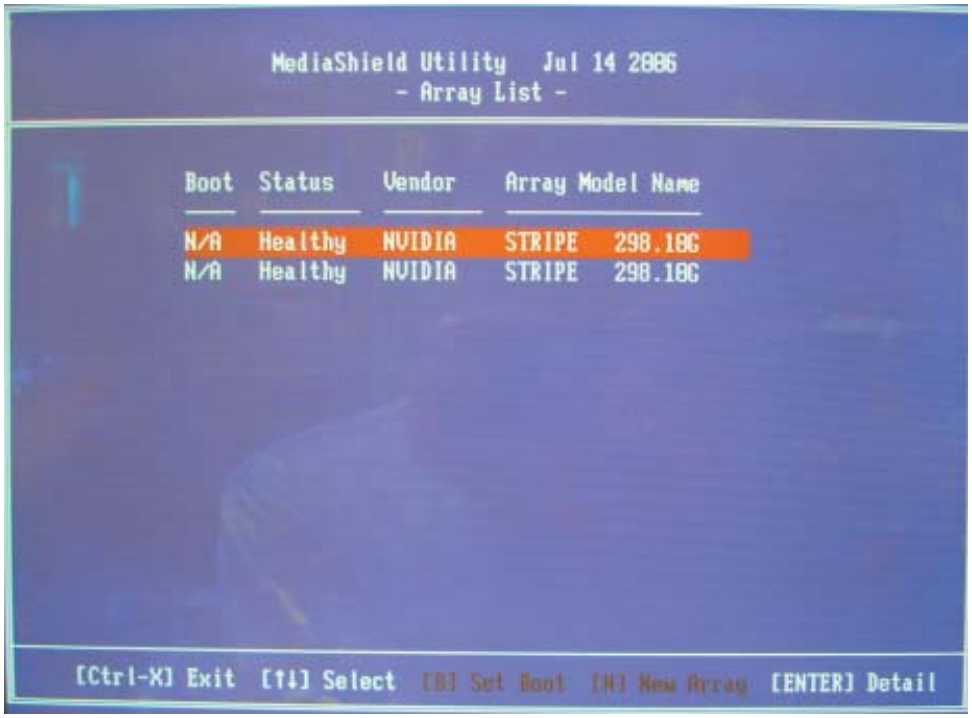


Figure 5-7 SATA RAID Utility: Array List



Chapter 6

Advanced Chassis Setup

This chapter covers the steps required to install components and perform simple maintenance on the SC743T-645 chassis. Following the component installation steps in the order given will eliminate most common problems. If some steps are unnecessary, skip ahead to the step that follows. Refer to Chapter 2 for instructions on installing the system as a 4U rackmount.

Tools Required: The only tool you will need is a Philips screwdriver.

6-1 Static-Sensitive Devices

Static electrical discharge can damage electronic components. To prevent damage to any printed circuit boards (PCBs), it is important to handle them very carefully. The following measures are generally sufficient to protect your equipment from static discharge.

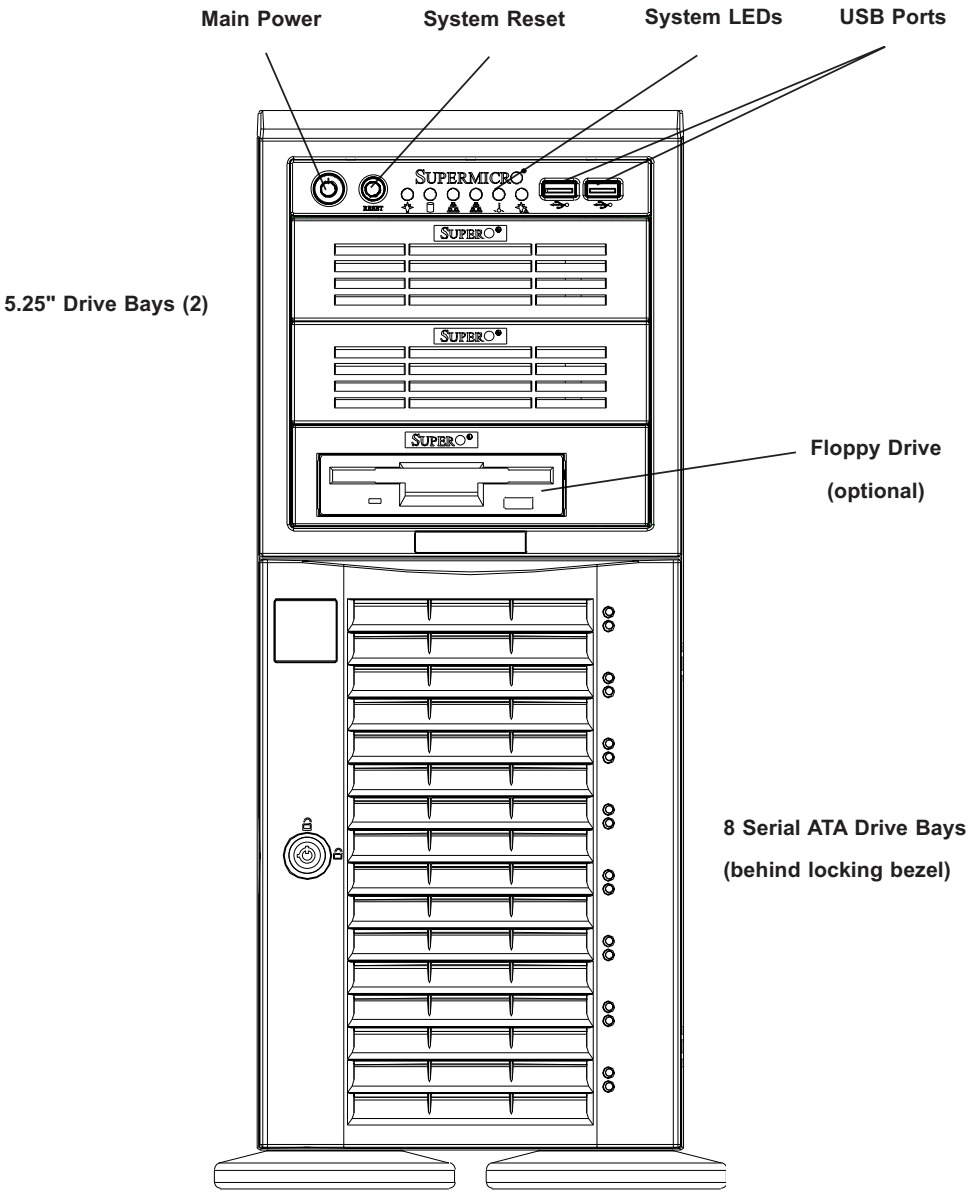
Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing any board from its antistatic bag.
- Handle a board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the serverboard, add-on cards and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the serverboard.

Unpacking

The serverboard is shipped in antistatic packaging. When unpacking the board, make sure the person handling it is static protected.

Figure 6-1. Chassis Front View



6-2 Front Control Panel

The front control panel must be connected to the JF1 connector on the serverboard to provide you with system status and alarm indications. A ribbon cable has bundled these wires together to simplify this connection. Connect the cable from JF1 on the serverboard (making sure the red wire plugs into pin 1) to the appropriate comnnector on the front control panel PCB (printed circuit board). Pull all excess cabling over to the control panel side of the chassis. The LEDs on the control panel inform you of system status - see Figure 6-2 for details. See Chapter 5 for details on JF1.

Figure 6-2. Front Control Panel LEDs

Power  Indicates power is being supplied to the system.

HDD  Indicates IDE and SATA hard drive activity.

NIC1  Indicates network activity on the LAN 1 port.

NIC2  Indicates network activity on the LAN 2 port.

Overheat/Fan Fail  When this LED flashes, it indicates a fan failure. When on continuously it indicates an overheat condition (see Chapter 3 for details).

Power Fail  Indicates a power supply failure (n/a to the 4021A-T2).

6-3 System Fans

Four 8-cm chassis cooling fans (located in the center of the chassis) provide cooling airflow while two 8-cm exhaust fans expel hot air from the chassis. The chassis is also fitted with an air shroud to concentrate the flow of cooling air over the areas where the most heat is generated. The fans should all be connected to headers on the serverboard (see Chapter 5). The power supply module also has a cooling fan.

Fan Failure

Under normal operation, all four chassis fans, the fans on the CPU heatsinks and the power supply fan will all run continuously. The four chassis fans are hot-swappable and can be replaced without powering down the system.

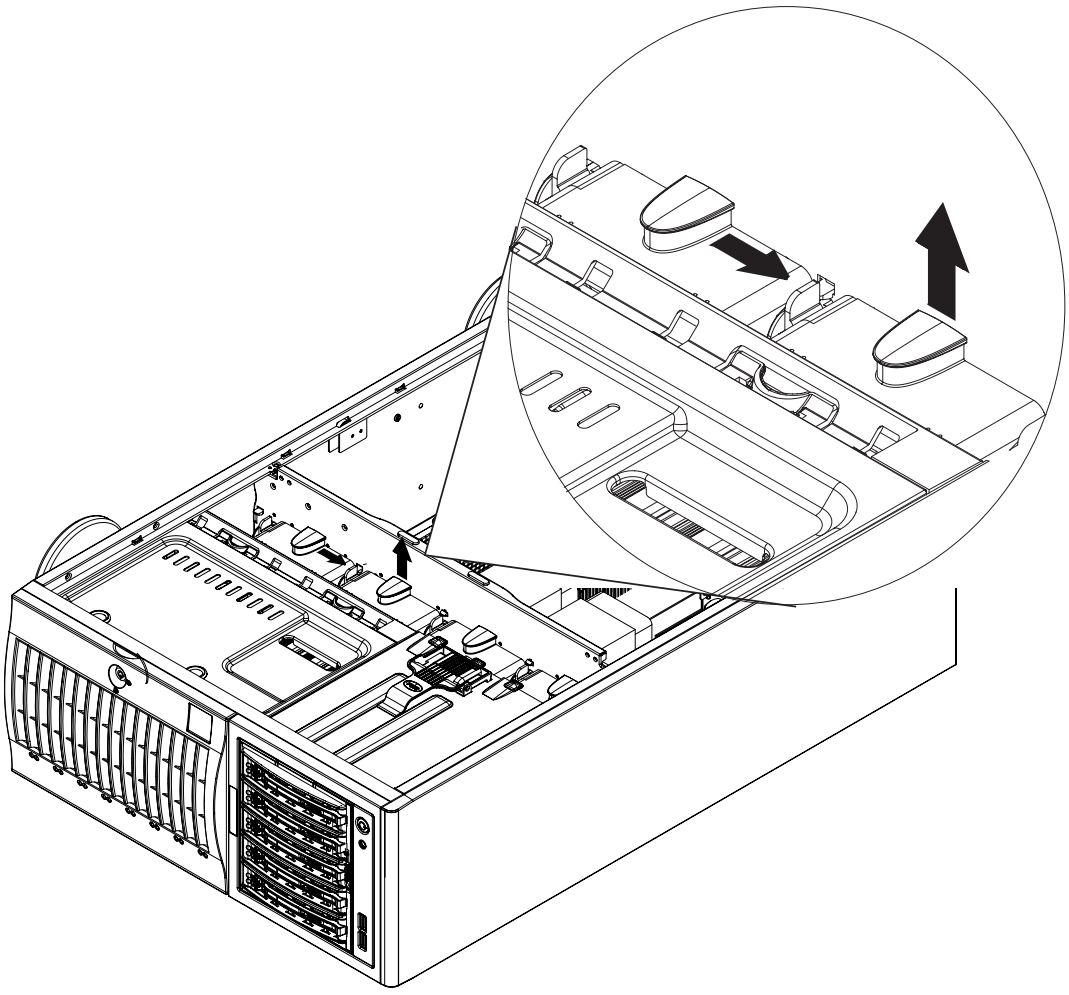
Replacing Chassis Cooling Fans

1. Begin by removing the top/left chassis cover (see Chapter 2 for details on removing the cover) to visually inspect the system to determine which fan has failed.

2. Depress the locking tab on the side of the chassis fan and pull the unit straight out by the handle (see arrows in Figure 6-3). The fan wiring for these fans has been designed to detach automatically.

3. Replace the failed fan with an identical one (p/n FAN-0074). Install it in (and then reassemble) the fan housing, then plug the housing back into its slot; it should click into place when fully inserted. Check that the fan is working then replace the top/left side chassis panel.

Figure 6-3. Removing a Chassis Fan



6-4 Drive Bay Installation

Serial ATA Drives

A total of six SATA drives may be housed in the SC743T-645 chassis. The drive IDs are preconfigured as 0 through 5 in order from bottom to top (or from left to right if rackmounted). A bezel covers the SATA drive area but does not need to be removed to access the drives; simply swing it open. If you wish to remove the bezel piece, push on the three tabs on the inside of the left lip of the front chassis cover. Then slightly swing out the same (left) side of the cover - about ½ inch only. Remove by pushing on the open side of the cover to remove it from the chassis (do not try to swing or pull it straight out after opening the left side).

Regardless of how many SATA hard drives are installed, all drive carriers must remain in the drive bays to promote proper airflow.

Installing/Removing SATA Drives

The six SATA drive carriers are all easily accessible at the front of the chassis. These drives are hot-swappable, meaning they can be removed and installed without powering down the system. Your operating system must have RAID support to enable the hot-swap capability of the SATA drives.

1. To remove a carrier, first swing open the front bezel then push the release button located beside the drive LEDs.

2. Swing the handle fully out and then use it to pull the drive straight out.

Mounting a SATA Drive in a Carrier

The SATA drives are mounted in drive carriers to simplify their installation and removal from the chassis. These carriers also work to promote proper airflow for the system. For this reason, even carriers without SATA drives must remain in the workstation.

1. Insert the drive into the carrier with the printed circuit board side facing down so that the mounting holes align with those in the carrier.

2. Secure the drive to the carrier with four screws (see Figure 6-5).

Figure 6-4. Removing a SATA Drive Carrier

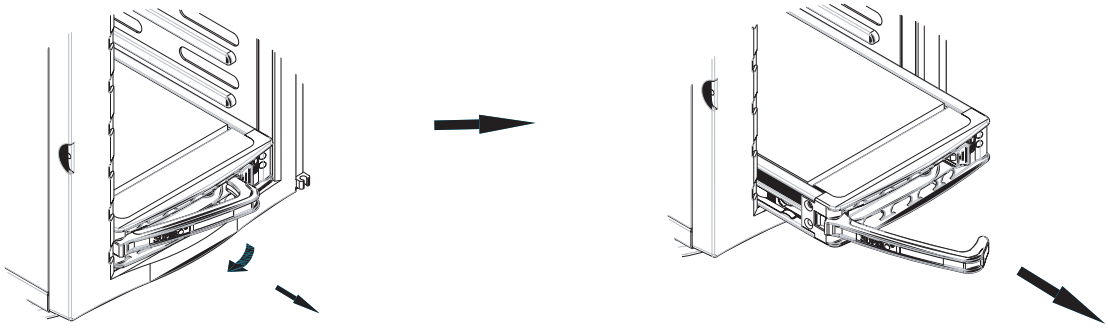
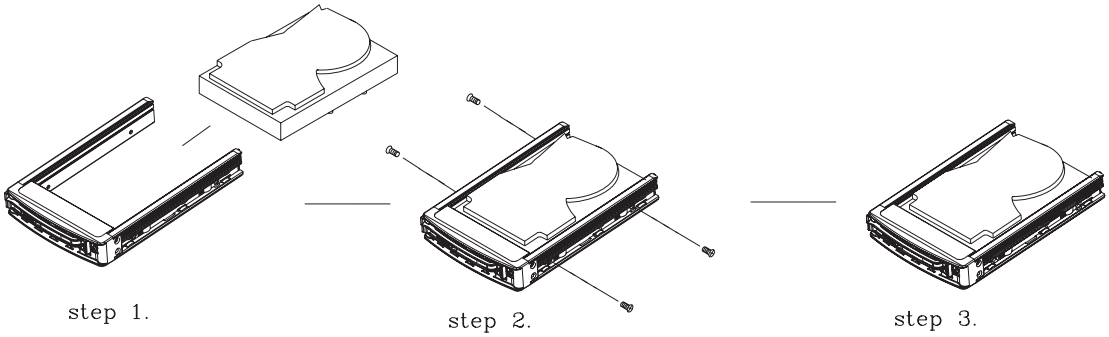


Figure 6-5. Mounting a SATA Drive in a Carrier



Important! Use extreme caution when working around the SATA backplane. Do not touch the backplane with any metal objects and make sure no cables touch the backplane or obstruct the airflow holes.

SATA Backplane

The SATA drives plug into a SATA backplane. There are two power connectors on the backplane - both should be connected. See Figure 6-6 for the locations of backplane connectors - the reverse side of the backplane has four channel connectors that the SATA drives plug into when inserted with a SATA drive carrier. You cannot cascade the SATA backplane.

Installing Components in the 5.25" Drive Bays

The 4021A-T2 has two 5.25" drive bays. Components such as an extra floppy drive, IDE hard drives or CD-ROM drives can be installed into these 5.25" drive bays.

Mounting Components in the Drive Bays

1. Power down the system and then remove the top/left chassis cover to access the drive components.

2. With the cover off, remove the two or four screws that secure the drive carrier to the chassis (one side only).

3. Push the entire empty drive carrier out from the back.

Adding a CD-ROM Drive

1. Remove the guide plates (one on each side) from the empty drive carrier and screw them into both sides of the CD-ROM drive using the holes provided.

2. Slide the CD-ROM into the bay and secure it to the chassis with the drive carrier screws you first removed.

3. Attach the power and data cables to the drive.

4. Replace the top/left chassis cover before restoring power to the system.

Adding an IDE or Floppy Drive

1. Install one of these types of drives into one of the removed empty drive carriers with the printed circuit board side toward the carrier and with the drive's mounting holes aligned with those in the carrier.

2. Secure the drive to the carrier with four screws then slide the assembly into the bay and secure it to the chassis with the drive carrier screws you first removed.

3. Attach the power and data cables to the drive.

4. Replace the top/left chassis cover before restoring power to the system.

Note: A red wire typically designates the location of pin 1. You should keep the drive carriers inserted in any unused drive bays to reduce EMI and noise and to facilitate the airflow inside the chassis.

6-5 Power Supply

The 4021A-T2 has a single 645 watt power supply. This power supply has an auto-switching capability, which enables it to automatically sense and operate at a 100V or 240V (+- 10%) input voltage.

Power Supply Failure

If the power supply unit fails, the system will shut down and you will need to replace the power supply unit. Replace with the same model - SP645-PS (p/n PWS-0060). As there is only one power supply unit in the 4021A-T2, the workstation must be powered down before removing and/or replacing the power supply for whatever reason.

Replacing the Power Supply

1. Power down the workstation and unplug the power cord from the power supply module.

2. Remove the screws that secure the power supply to the chassis then pull it completely out.

3. Replace the failed unit with another unit having the exact same part number (PWS-0060). Gently but firmly push the new unit all the way into the open bay.

4. Secure it to the chassis using the screws you previously removed.

5. Finish by replacing the chassis left/top cover and then plugging the power cord back into the new module you just added.

Notes

Chapter 7

BIOS

7-1 Introduction

This chapter describes the AMIBIOS™ Setup utility for the H8DAE-2. The AMI ROM BIOS is stored in a flash chip and can be easily upgraded using a floppy disk-based program.

Note: Due to periodic changes to the BIOS, some settings may have been added or deleted and might not yet be recorded in this manual. Please refer to the Manual Download area of our web site for any changes to BIOS that may not be reflected in this manual.

Starting the Setup Utility

To enter the BIOS Setup Utility, hit the <Delete> key while the system is booting-up. (In most cases, the <Delete> key is used to invoke the BIOS setup screen. There are a few cases when other keys are used, such as <F1>, <F2>, etc.) Each main BIOS menu option is described in this manual.

The Main BIOS screen has two main frames. The left frame displays all the options that can be configured. “Grayed-out” options cannot be configured. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it. (Note that BIOS has default text messages built in. We retain the option to include, omit, or change any of these text messages.) Settings printed in **Bold** are the default values.

A "▶" indicates a submenu. Highlighting such an item and pressing the <Enter> key will open the list of settings within that submenu.

The BIOS setup utility uses a key-based navigation system called hot keys. Most of these hot keys (<F1>, <F10>, <Enter>, <ESC>, <Arrow> keys, etc.) can be used at any time during the setup navigation process.

7-2 Main Menu

When you first enter AMI BIOS Setup Utility, you will see the Main Menu screen. You can always return to the Main Menu by selecting the ***Main*** tab on the top of the screen with the arrow keys.

The Main Menu screen provides you with a system overview, which includes the version, built date and ID of the AMIBIOS, the type, speed and number of the processors in the system and the amount of memory installed in the system.

System Time/System Date

You can edit this field to change the system time and date. Highlight *System Time* or *System Date* using the <Arrow> keys. Enter new values through the keyboard. Press the <Tab> key or the <Arrow> keys to move between fields. The date must be entered in DAY/MM/DD/YYYY format. The time is entered in HH:MM:SS format. Please note that time is in a 24-hour format. For example, 5:30 A.M. appears as 05:30:00 and 5:30 P.M. as 17:30:00.

7-3 Advanced Settings Menu

▶ BIOS Features

Quick Boot

If Enabled, this option will skip certain tests during POST to reduce the time needed for the system to boot up. The options are **Enabled** and Disabled.

Quiet Boot

If Disabled, normal POST messages will be displayed on boot-up. If **Enabled**, this display the OEM logo instead of POST messages.

OS Installation

Change this setting if using a Linux operating system. The available options are **Other** and Linux.

ACPI Mode

Use this setting to determine whether ACPI mode will be used. The options are **Yes** and No.

Suspend Mode

This setting is used to select the ACPI state used for system suspend. The options are S1 (POS), S3 (STR) and **Auto**.

▶ Advanced ACPI Configuration

MCP55 ACPI HPET Table

Use this setting to Enable or **Disable** the MCP55 ACPI HPET Table.

IO55 ACPI HPET Table

Use this setting to Enable or **Disable** the IOP55 ACPI HPET Table.

ACPI Version Features

Use this setting the determine which ACPI version to use. Options are **ACPI v1.0**, ACPI v2.0 and ACPI v3.0.

ACPI APIC Support

Determines whether to include the ACPI APIC table pointer in the RSDT pointer list. The available options are **Enabled** and Disabled.

ACPI OEMB Table

Determines whether to include the ACPI APIC table pointer in the RSDT pointer list. The available options are **Enabled** and Disabled.

Headless Mode

Use this setting to Enable or **Disable** headless operation mode through ACPI.

Power Button Mode

Allows the user to change the function of the power button. Options are **On/Off** and Suspend.

Watch Dog Timer Select

This setting is used to Enable or Disable the Watch Dog Timer function. It must be used in conjunction with the Watch Dog jumper (see Chapter 2 for details). To enable, choose from 1, 2, 3, 4, 8, 15 or 30 min.

Restore on AC Power Loss

This setting allows you to choose how the system will react when power returns after an unexpected loss of power. The options are Power Off, Power On and **Last State.**

MPS Revision

This setting allows the user to select the MPS revision level. The options are 1.1 and **1.4.**

▶ Floppy/IDE/SATA Configuration

Onboard Floppy Controller

Use this setting to Enable or Disable the onboard floppy controller.

Floppy A

Move the cursor to these fields via up and down <arrow> keys to select the floppy type. The options are Disabled, 360 KB 5 1/4", 1.2 MB 5 1/4", 720 KB 3½", 1.44 MB 3½", and 2.88 MB 3½".

Onboard IDE Controller

There is a single floppy controller on the motherboard, which may be **Enabled** or Disabled with this setting.

Serial ATA Devices

This setting is used to determine if SATA drives will be used and how many. Options are Disabled, Device 0, Device 0/1 and **Device 0/1/2**.

▶ nVidia RAID Setup

nVidia RAID Function

This setting is used to Enable or **Disable** the nVidia ROM. If Enabled, the setting below will appear.

SATA0/1/2 Primary/Secondary Channel

This setting is used to Enable or **Disable** the SATA0 Primary, SATA0 Secondary, SATA1 Primary, SATA1 Secondary, SATA2 Primary and SATA2 Secondary channels (six settings total).

Primary IDE Master/Slave

Highlight one of the items above and press <Enter> to access the submenu for that item.

Type

Select the type of device connected to the system. The options are Not Installed, **Auto**, CDROM and ARMD.

LBA/Large Mode

LBA (Logical Block Addressing) is a method of addressing data on a disk drive. The options are Disabled and **Auto**.

Block (Multi-Sector Transfer)

Block mode boosts IDE drive performance by increasing the amount of data transferred. Only 512 bytes of data can be transferred per interrupt if block mode is not used. Block mode allows transfers of up to 64 KB per interrupt. Select "Disabled" to allow the data to be transferred from and to the device one sector at a time. Select "Auto" to allows the data transfer from and to the device occur multiple sectors at a time if the device supports it. The options are **Auto** and Disabled.

PIO Mode

PIO (Programmable I/O) mode programs timing cycles between the IDE drive and the programmable IDE controller. As the PIO mode increases, the cycle time decreases. The options are **Auto**, 0, 1, 2, 3, and 4. Select Auto to allow AMI BIOS to auto detect the PIO mode. Use this value if the IDE disk drive support cannot be determined. Select 0 to allow AMI BIOS to use PIO mode 0. It has a data transfer rate of 3.3 MBs. Select 1 to allow AMI BIOS to use PIO mode 1. It has a data transfer rate of 5.2 MBs. Select 2 to allow AMI BIOS to use PIO mode 2. It has a data transfer rate of 8.3 MBs. Select 3 to allow AMI BIOS to use PIO mode 3. It has a data transfer rate of 11.1 MBs. Select 4 to allow AMI BIOS to use PIO mode 4. It has a data transfer rate of 16.6 MBs. This setting generally works with all hard disk drives manufactured after 1999. For other disk drives, such as IDE CD-ROM drives, check the specifications of the drive.

DMA Mode

Selects the DMA Mode. Options are SWDMA0, SWDMA1, SWDMA2, MWDMA0. MDWDMA1, MWDMA2, UDMA0. UDMA1, UDMA2, UDMA3, UDMA4 and UDMA5. (SWDMA=Single Word DMA, MWDMA=Multi Word DMA, UDMA=UltraDMA.)

S.M.A.R.T.

Self-Monitoring Analysis and Reporting Technology (SMART) can help predict impending drive failures. Select "Auto" to allow BIOS to auto detect hard disk drive support. Select "Disabled" to prevent AMI BIOS from using the S.M.A.R.T. Select "Enabled" to allow AMI BIOS to use the S.M.A.R.T. to support hard drive disk. The options are Disabled, Enabled, and **Auto**.

32-Bit Data Transfer

Select "Enabled" to activate the function of 32-Bit data transfer. Select "Disabled" to deactivate the function. The options are Enabled and **Disabled**.

Serial ATA0/1/2 Primary/Secondary Channel

Highlight one of the items above and press <Enter> to access the submenu for that item. If a drive is present, information on that drive will be displayed here.

LBA/Large Mode

LBA (Logical Block Addressing) is a method of addressing data on a disk drive. The options are Disabled and **Auto**.

Block (Multi-Sector Transfer)

Block mode boosts IDE drive performance by increasing the amount of data transferred. Only 512 bytes of data can be transferred per interrupt if block mode is not used. Block mode allows transfers of up to 64 KB per interrupt. Select "Disabled" to allow the data to be transferred from and to the device one sector at a time. Select "Auto" to allows the data transfer from and to the device occur multiple sectors at a time if the device supports it. The options are **Auto** and Disabled.

PIO Mode

PIO (Programmable I/O) mode programs timing cycles between the IDE drive and the programmable IDE controller. As the PIO mode increases, the cycle time decreases. The options are **Auto**, 0, 1, 2, 3, and 4. Select Auto to allow AMI BIOS to auto detect the PIO mode. Use this value if the IDE disk drive support cannot be determined. Select 0 to allow AMI BIOS to use PIO mode 0. It has a data transfer rate of 3.3 MBs. Select 1 to allow AMI BIOS to use PIO mode 1. It has a data transfer rate of 5.2 MBs. Select 2 to allow AMI BIOS to use PIO mode 2. It has a data transfer rate of 8.3 MBs. Select 3 to allow AMI BIOS to use PIO mode 3. It has a data transfer rate of 11.1 MBs. Select 4 to allow AMI BIOS to use PIO mode 4. It has a data transfer rate of 16.6 MBs. This setting generally works with all hard disk drives manufactured after 1999. For other disk drives, such as IDE CD-ROM drives, check the specifications of the drive.

DMA Mode

Selects the DMA Mode. Options are SWDMA0, SWDMA1, SWDMA2, MWD-MA0. MDWDMA1, MWDMA2, UDMA0. UDMA1, UDMA2, UDMA3, UDMA4 and UDMA5. (SWDMA=Single Word DMA, MWDMA=Multi Word DMA, UDMA=UltraDMA.)

S.M.A.R.T.

Self-Monitoring Analysis and Reporting Technology (SMART) can help predict impending drive failures. Select "Auto" to allow BIOS to auto detect hard disk drive support. Select "Disabled" to prevent AMI BIOS from using the S.M.A.R.T.

Select "Enabled" to allow AMI BIOS to use the S.M.A.R.T. to support hard drive disk. The options are Disabled, Enabled, and **Auto**.

32-Bit Data Transfer

Select "Enabled" to activate the function of 32-Bit data transfer. Select "Disabled" to deactivate the function. The options are Enabled and **Disabled**.

Hard Disk Write Protect

Select Enabled to enable the function of Hard Disk Write Protect to prevent data from being written to HDD. The options are Enabled or **Disabled**.

IDE Detect Time Out (Sec)

This feature allows the user to set the time-out value for detecting ATA, ATAPI devices installed in the system. Options are 0 (sec), 5, 10, 15, 20, 25, 30 and **35**.

ATA(PI) 80Pin Cable Detection

This setting allows AMI BIOS to auto-detect the 80-Pin ATA(PI) cable. The options are Host, Device and **Host & Device**.

SATA0 IDE Interface

This setting is used to **Enable** or Disable the serial controller for SATA0.

SATA1 IDE Interface

This setting is used to **Enable** or Disable the serial controller for SATA1.

First Boot Device From

Use this setting to select the first boot device as being **P-ATA** or S-ATA.

▶ Configuration nVidia RAID ROM

RAID Option ROM

This setting is used to Enable or **Disable** the nVidia ROM. If Enabled, the setting below will appear.

Master SATA as RAID

This setting is used to Enable or **Disable** the 3rd Master as RAID.

▶ PCI/PnP Configuration

Load Onboard LAN Option ROM

Use this setting to Enable or **Disable** the onboard option ROM.

Clear NVRAM

Select Yes to clear NVRAM during boot-up. The options are Yes and **No**.

Plug & Play OS

Select Yes to allow the OS to configure Plug & Play devices. (This is not required for system boot if your system has an OS that supports Plug & Play.) Select **No** to allow AMIBIOS to configure all devices in the system.

PCI Latency Timer

This option sets the latency of all PCI devices on the PCI bus. Select a value to set the PCI latency in PCI clock cycles. Options are 32, **64**, 96, 128, 160, 192, 224 and 248.

Allocate IRQ to PCI VGA

Set this value to allow or restrict the system from giving the VGA adapter card an interrupt address. The options are **Yes** and No.

Palette Snooping

Select "Enabled" to inform the PCI devices that an ISA graphics device is installed in the system in order for the graphics card to function properly. The options are Enabled and **Disabled**.

PCI IDE BusMaster

Set this value to allow or prevent the use of PCI IDE busmastering. Select "Enabled" to allow AMI BIOS to use PCI busmaster for reading and writing to IDE drives. The options are **Disabled** and Enabled.

Offboard PCI/ISA IDE Card

This option allows the user to assign a PCI slot number to an Off-board PCI/ISA IDE card in order for it to function properly. The options are **Auto**, PCI Slot1, PCI Slot2, PCI Slot3, PCI Slot4, PCI Slot5, and PCI Slot6.

▶ Advanced Chipset Control

▶ NorthBridge Configuration

▶ Memory Configuration

Memclock Mode

This setting determines how the memory clock is set. **Auto** has the memory clock by code and Limit allows the user to set a standard value.

MCT Timing Mode

Sets the timing mode for memory. Options are **Auto** and Manual.

Bank Interleaving

Select Auto to automatically enable interleaving-memory scheme when this function is supported by the processor. The options are **Auto** and Disabled.

Enable Clock to All Dimms

Use this setting to enable unused clocks to all DIMMSs, even if some DIMM slots are unpopulated. Options are Enabled and **Disabled**.

Mem Clk Tristate C3/ALTVID

Use this setting to Enable or **Disable** memory clock tristate during C3 and ALT VID.

CS Sparing Enable

Use this setting to Enable or **Disable** CS Sparing.

Memory Hole Remapping

When "Enabled", this feature enables hardware memory remapping around the memory hole. Options are **Enabled** and Disabled.

▶ ECC Configuration

DRAM ECC Enable

DRAM ECC allows hardware to report and correct memory errors automatically. Options are **Enabled** and Disabled.

4-Bit ECC Mode

Allows the user to enabled 4-bit ECC mode (also known as ECC Chipkill). Options are Enabled and **Disabled**.

DRAM Scrub Redirect

Allows system to correct DRAM ECC errors immediately, even with background scrubbing on. Options are Enabled and **Disabled**.

DRAM BG Scrub

Corrects memory errors so later reads are correct. Options are **Disabled** and various times in nanoseconds and microseconds.

L2 Cache BG Scrub

Allows L2 cache RAM to be corrected when idle. Options are **Disabled** and various times in nanoseconds and microseconds.

Data Cache BG Scrub

Allows L1 cache RAM to be corrected when idle. Options are **Disabled** and various times in nanoseconds and microseconds.

Power Down Control

Allows DIMMs to enter power down mode by deasserting the clock enable signal when DIMMs are not in use. Options are **Auto** and Disabled.

Alternate VID

Specify the alternate VID while in low power states. Options are **Auto** and various voltages from .8V to 1.15V in increments of .025V.

▶ SouthBridge/MCP55 Configuration

USB 1.1 Controller

Enable or disable the USB 1.1 controller.

USB 2.0 Controller

Enable or disable the USB 2.0 controller.

MAC0 LAN0

Settings are Auto and Disabled for MAC0 LAN0.

MAC0 LAN0 Bridge

Settings are **Enabled** and Disabled for MAC0 LAN0 bridge.

MAC1 LAN1

Settings are **Auto** and Disabled for MAC1 LAN1.

MAC1 LAN1 Bridge

Settings are **Enabled** and Disabled for MAC1 LAN1 bridge.

Legacy USB Support

Select "Enabled" to enable the support for USB Legacy. Disable Legacy support if there are no USB devices installed in the system. "Auto" disabled Legacy support if no USB devices are connected. The options are Disabled, **Enabled** and Auto.

▶ Processor & Clock Options

This submenu lists CPU information and the following settings:

MTRR Mapping

This determines the method used for programming CPU MTRRs when 4 GB or more memory is present. The options are **Continuous**, which makes the PCI hole non-cacheable, and Discrete, which places the PCI hole below the 4 GB boundary.

Thermal Throttling

Used to Enable or **Disable** thermal to generate a power management event.

Power Now

This setting is used to Enable or **Disable** the AMD Power Now feature.

▶ I/O Device Configuration

Serial Port1 Address

This option specifies the base I/O port address and Interrupt Request address of serial port 1. Select "Disabled" to prevent the serial port from accessing any system resources. When this option is set to *Disabled*, the serial port physically becomes unavailable. Select "3F8/IRQ4" to allow the serial port to use 3F8 as its I/O port address and IRQ 4 for the interrupt address. The options are Disabled, **3F8/IRQ4**, 3E8/IRQ4 and 2E8/IRQ3.

Serial Port2 Address

This option specifies the base I/O port address and Interrupt Request address of serial port 2. Select "Disabled" to prevent the serial port from accessing any system resources. When this option is set to "Disabled", the serial port physically becomes unavailable. Select "2F8/IRQ3" to allow the serial port to use 2F8 as its I/O port address and IRQ 3 for the interrupt address. The options are Disabled, **2F8/IRQ3**, 3E8/IRQ4 and 2E8/IRQ3.

Serial Port 2 Mode

Tells BIOS which mode to select for serial port 2. The options are **Normal**, IrDA and ASKIR.

Parallel Port Address

Select the base I/O address for the parallel port. The options are **378**, 278 and 3BC.

Parallel Port Mode

Specify the parallel port mode. The options are **Normal**, Bi-directional, EPP and ECP.

Parallel Port IRQ

Select the IRQ (interrupt request) for the parallel port. The options are IRQ5 and **IRQ7**.

▶ DMI Event Logging

View Event Log

Highlight this item and press <Enter> to view the contents of the event log.

Mark All Events as Read

Highlight this item and press <Enter> to mark all events as read.

Clear Event Log

Select Yes and press <Enter> to clear all event logs. The options are Yes and No to verify.

▶ Console Redirection

Remote Access

Allows you to Enable or **Disable** remote access. If enabled, the settings below will appear.

Serial Port Number

Selects the serial port to use for console redirection. Options are **COM1** and COM2.

Serial Port Mode

Selects the serial port settings to use. Options are **(115200 8, n, 1)**, (57600 8, n, 1), (38400 8, n, 1), (19200 8, n, 1) and (09600 8, n, 1).

Flow Control

Selects the flow control to be used for console redirection. Options are **None,** Hardware and Software.

Redirection After BIOS POST

Options are Disable (no redirection after BIOS POST), Boot Loader (redirection during POST and during boot loader) and **Always** (redirection always active). Note that some OS's may not work with this set to Always.

Terminal Type

Selects the type of the target terminal. Options are **ANSI**, VT100 and VT-UTF8.

VT-UTF8 Combo Key Support

Allows you to **Enable** or Disable VT-UTF8 combination key support for ANSI/VT100 terminals.

Sredir Memory Display Delay

Use this setting to set the delay in seconds to display memory information. Options are **No Delay**, 1 sec, 2 secs and 4 secs.

▶ Hardware Monitor

CPU Overheat Alarm

Use the "+" and "-" keys to set the CPU temperature threshold to between 65° and 90° C. When this threshold is exceeded, the overheat LED on the chassis will light up and an alarm will sound. The LED and alarm will turn off once the CPU temperature has dropped to 5 degrees below the threshold set. The default setting is **72° C**.

▶ System Fan Monitor

Fan Speed Control

This feature allows the user to determine how the system will control the speed of the onboard fans. Select "Workstation" if your system is used as a Workstation. Select "Server" if your system is used as a Server. Select "Disable" to disable the fan speed control function to allow the onboard fans to continuously run at full speed (12V). The options are **1) Disable (Full Speed),** 2) 3-pin (Server), 3) 3-pin (Workstation), 4) 4-pin (Server), 5) 4-pin (Workstation, 6) Quiet and 7) Super Quiet.

Other items in the submenu are systems monitor displays for the following information:

CPU1 Temperature, CPU2 Temperature (for 2U systems), System Temperature, VCoreA, VCoreB (for 2U systems), HT Voltage, CPU1 Mem VTT, CPU2 Mem VTT, CPU1 Mem, CPU2 Mem, VDD, 1.5V, MCP55 VCcore, 3.3V, 12V, -12V, 5V VSB and VBAT.

▶ IPMI Configuration

▶ View BMC System Event Log

Pressing the Enter key will open the event log. Use the "+" and "-" keys to navigate through the system event log.

Clear BMC System Event Log

Selecting this and pressing the Enter key will clear the BMC system event log.

▶ Set LAN Configuration

Use the "+" and "-" keys to choose the desired channel number.

▶ IP Address

Use the "+" and "-" keys to select the parameter. The IP address and current IP address in the BMC are shown.

▶ MAC Address

Use the "+" and "-" keys to select the parameter. The MAC address and current MAC address in the BMC are shown.

▶ Subnet Address

Use the "+" and "-" keys to select the parameter. The subnet address and current subnet address in the BMC are shown.

▶ Set PEF Configuration

PEF Support

Use this setting to **Enable** or Disable PEF support. If enabled, the following PEF settings will appear.

PEF Action Global Control

Options are **Alert**, Power Down, Reset Sysytem, Power Cycle, OEM Action and Diagnostic Int..

Alert Startup Delay

Use this setting to Enable or **Disable** the alert startup delay.

Startup Delay

Use this setting to Enable or **Disable** the startup delay.

Event Message for PEF Action

Use this setting to Enable or **Disable** event messages for a PEF action.

BMC Watch Dog Timer Action

This setting is used to set the Watch Dog function. The options are **Disabled**, Reset System, Power Down and Power Cycle.

7-4 Boot Menu

This feature allows the user to configure the following items:

▶ Boot Device Priority

This feature allows the user to prioritize the boot sequence from the available devices. The devices to set are:

· 1st Boot Device

· 2nd Boot Device

· 3rd Boot Device

· 4th Boot Device

▶ Hard Disk Drives

This feature allows the user to specify the boot sequence from available hard disk drives.

1st Drive

Specifies the boot sequence for the 1st Hard Drive.

▶ Removable Drives

This feature allows the user to specify the Boot sequence from available removable drives.

1st Drive

Specifies the boot sequence for the 1st Removable Drive. The options are **1st Floppy Drive** and Disabled.

▶ CD/DVD Drives

This feature allows the user to specify the Boot sequence from available CD/DVD drives.

7-5 Security Menu

AMI BIOS provides a Supervisor and a User password. If you use both passwords, the Supervisor password must be set first.

Change Supervisor Password

Select this option and press <Enter> to access the sub menu, and then type in the password.

Change User Password

Select this option and press <Enter> to access the sub menu, and then type in the password.

Boot Sector Virus Protection

This option is near the bottom of the Security Setup screen. Select "Disabled" to deactivate the Boot Sector Virus Protection. Select "Enabled" to enable boot sector protection. When "Enabled", AMI BIOS displays a warning when any program (or virus) issues a Disk Format command or attempts to write to the boot sector of the hard disk drive. The options are Enabled and **Disabled**.

7-6 Exit Menu

Select the Exit tab from AMI BIOS Setup Utility screen to enter the Exit BIOS Setup screen.

Save Changes and Exit

When you have completed the system configuration changes, select this option to leave BIOS Setup and reboot the computer, so the new system configuration parameters can take effect. Select Save Changes and Exit from the Exit menu and press <Enter>.

Discard Changes and Exit

Select this option to quit BIOS Setup without making any permanent changes to the system configuration and reboot the computer. Select Discard Changes and Exit from the Exit menu and press <Enter>.

Discard Changes

Select this option and press <Enter> to discard all the changes and return to AMI BIOS Utility Program.

Load Optimal Defaults

To set this feature, select Load Optimal Defaults from the Exit menu and press <Enter>. Then Select "OK" to allow BIOS to automatically load the Optimal Defaults as the BIOS Settings. The Optimal settings are designed for maximum system performance, but may not work best for all computer applications.

Load Fail-Safe Defaults

To set this feature, select Load Fail-Safe Defaults from the Exit menu and press <Enter>. The Fail-Safe settings are designed for maximum system stability, but not maximum performance.

Appendix A

BIOS Error Beep Codes

During the POST (Power-On Self-Test) routines, which are performed each time the system is powered on, errors may occur.

Non-fatal errors are those which, in most cases, allow the system to continue the boot-up process. The error messages normally appear on the screen.

Fatal errors are those which will not allow the system to continue the boot-up procedure. If a fatal error occurs, you should consult with your system manufacturer for possible repairs.

These fatal errors are usually communicated through a series of audible beeps. The numbers on the fatal error list, on the following page, correspond to the number of beeps for the corresponding error. All errors listed, with the exception of Beep Code 8, are fatal errors.

POST codes may be read on the debug LEDs located beside the LAN port on the serverboard backplane. See the description of the Debug LEDs (LED1 and LED2) in Chapter 5.

A-1 AMIBIOS Error Beep Codes

Beep Code	Error Message	Description
1 beep	Refresh	Circuits have been reset. (Ready to power up.)
5 short, 1 long	Memory error	No memory detected in system
8 beeps	Video error	Video adapter disabled or missing

Notes

Appendix B

BIOS POST Checkpoint Codes

When AMIBIOS performs the Power On Self Test, it writes checkpoint codes to I/O port 0080h. If the computer cannot complete the boot process, diagnostic equipment can be attached to the computer to read I/O port 0080h.

B-1 Uncompressed Initialization Codes

The uncompressed initialization checkpoint codes are listed in order of execution:

Checkpoint	Code Description
D0h	The NMI is disabled. Power on delay is starting. Next, the initialization code checksum will be verified.
D1h	Initializing the DMA controller, performing the keyboard controller BAT test, starting memory refresh and entering 4 GB flat mode next.
D3h	Starting memory sizing next.
D4h	Returning to real mode. Executing any OEM patches and setting the Stack next.
D5h	Passing control to the uncompressed code in shadow RAM at E000:0000h. The initialization code is copied to segment 0 and control will be transferred to segment 0.

B-2 Bootblock Recovery Codes

The bootblock recovery checkpoint codes are listed in order of execution:

Checkpoint	Code Description
E0h	The onboard floppy controller if available is initialized. Next, beginning the base 512 KB memory test.
E1h	Initializing the interrupt vector table next.
E2h	Initializing the DMA and Interrupt controllers next.
E6h	Enabling the floppy drive controller and Timer IRQs. Enabling internal cache memory.
Edh	Initializing the floppy drive.
Eeh	Looking for a floppy diskette in drive A:. Reading the first sector of the diskette.
Efh	A read error occurred while reading the floppy drive in drive A:.
F0h	Next, searching for the AMIBOOT.ROM file in the root directory.
F1h	The AMIBOOT.ROM file is not in the root directory.
F2h	Next, reading and analyzing the floppy diskette FAT to find the clusters occupied by the AMIBOOT.ROM file.
F3h	Next, reading the AMIBOOT.ROM file, cluster by cluster.
F4h	The AMIBOOT.ROM file is not the correct size.
F5h	Next, disabling internal cache memory.
FBh	Next, detecting the type of flash ROM.
FCh	Next, erasing the flash ROM.
FDh	Next, programming the flash ROM.
FFh	Flash ROM programming was successful. Next, restarting the system BIOS.

B-3 Uncompressed Initialization Codes

The following runtime checkpoint codes are listed in order of execution.

These codes are uncompressed in F0000h shadow RAM.

Checkpoint	Code Description
03h	The NMI is disabled. Next, checking for a soft reset or a power on condition.
05h	The BIOS stack has been built. Next, disabling cache memory.
06h	Uncompressing the POST code next.
07h	Next, initializing the CPU and the CPU data area.
08h	The CMOS checksum calculation is done next.
0Ah	The CMOS checksum calculation is done. Initializing the CMOS status register for date and time next.
0Bh	The CMOS status register is initialized. Next, performing any required initialization before the keyboard BAT command is issued.
0Ch	The keyboard controller input buffer is free. Next, issuing the BAT command to the keyboard controller.
0Eh	The keyboard controller BAT command result has been verified. Next, performing any necessary initialization after the keyboard controller BAT command test.
0Fh	The initialization after the keyboard controller BAT command test is done. The keyboard command byte is written next.
10h	The keyboard controller command byte is written. Next, issuing the Pin 23 and 24 blocking and unblocking command.
11h	Next, checking if <End or <Ins> keys were pressed during power on. Initializing CMOS RAM if the Initialize CMOS RAM in every boot AMIBIOS POST option was set in AMIBCP or the <End> key was pressed.
12h	Next, disabling DMA controllers 1 and 2 and interrupt controllers 1 and 2.
13h	The video display has been disabled. Port B has been initialized. Next, initializing the chipset.
14h	The 8254 timer test will begin next.
19h	Next, programming the flash ROM.
1Ah	The memory refresh line is toggling. Checking the 15 second on/off time next.
2Bh	Passing control to the video ROM to perform any required configuration before the video ROM test.
2Ch	All necessary processing before passing control to the video ROM is done. Looking for the video ROM next and passing control to it.
2Dh	The video ROM has returned control to BIOS POST. Performing any required processing after the video ROM had control
23h	Reading the 8042 input port and disabling the MEGAKEY Green PC feature next. Making the BIOS code segment writable and performing any necessary configuration before initializing the interrupt vectors.
24h	The configuration required before interrupt vector initialization has completed. Interrupt vector initialization is about to begin.

Checkpoint	Code Description
25h	Interrupt vector initialization is done. Clearing the password if the POST DIAG switch is on.
27h	Any initialization before setting video mode will be done next.
28h	Initialization before setting the video mode is complete. Configuring the monochrome mode and color mode settings next.
2Ah	Bus initialization system, static, output devices will be done next, if present. See the last page for additional information.
2Eh	Completed post-video ROM test processing. If the EGA/VGA controller is not found, performing the display memory read/write test next.
2Fh	The EGA/VGA controller was not found. The display memory read/write test is about to begin.
30h	The display memory read/write test passed. Look for retrace checking next.
31h	The display memory read/write test or retrace checking failed. Performing the alternate display memory read/write test next.
32h	The alternate display memory read/write test passed. Looking for alternate display retrace checking next.
34h	Video display checking is over. Setting the display mode next.
37h	The display mode is set. Displaying the power on message next.
38h	Initializing the bus input, IPL, general devices next, if present. See the last page of this chapter for additional information.
39h	Displaying bus initialization error messages. See the last page of this chapter for additional information.
3Ah	The new cursor position has been read and saved. Displaying the Hit <DEL> message next.
3Bh	The Hit <DEL> message is displayed. The protected mode memory test is about to start.
40h	Preparing the descriptor tables next.
42h	The descriptor tables are prepared. Entering protected mode for the memory test next.
43h	Entered protected mode. Enabling interrupts for diagnostics mode next.
44h	Interrupts enabled if the diagnostics switch is on. Initializing data to check memory wraparound at 0:0 next.
45h	Data initialized. Checking for memory wraparound at 0:0 and finding the total system memory size next.
46h	The memory wraparound test is done. Memory size calculation has been done. Writing patterns to test memory next.
47h	The memory pattern has been written to extended memory. Writing patterns to the base 640 KB memory next.
48h	Patterns written in base memory. Determining the amount of memory below 1 MB next.
49h	The amount of memory below 1 MB has been found and verified.
4Bh	The amount of memory above 1 MB has been found and verified. Checking for a soft reset and clearing the memory below 1 MB for the soft reset next. If this is a power on situation, going to checkpoint 4Eh next.

Checkpoint	Code Description
4Ch	The memory below 1 MB has been cleared via a soft reset. Clearing the memory above 1 MB next.
4Dh	The memory above 1 MB has been cleared via a soft reset. Saving the memory size next. Going to checkpoint 52h next.
4Eh	The memory test started, but not as the result of a soft reset. Displaying the first 64 KB memory size next.
4Fh	The memory size display has started. The display is updated during the memory test. Performing the sequential and random memory test next.
50h	The memory below 1 MB has been tested and initialized. Adjusting the displayed memory size for relocation and shadowing next.
51h	The memory size display was adjusted for relocation and shadowing.
52h	The memory above 1 MB has been tested and initialized. Saving the memory size information next.
53h	The memory size information and the CPU registers are saved. Entering real mode next.
54h	Shutdown was successful. The CPU is in real mode. Disabling the Gate A20 line, parity, and the NMI next.
57h	The A20 address line, parity, and the NMI are disabled. Adjusting the memory size depending on relocation and shadowing next.
58h	The memory size was adjusted for relocation and shadowing. Clearing the Hit <DEL> message next.
59h	The Hit <DEL> message is cleared. The <WAIT...> message is displayed. Starting the DMA and interrupt controller test next.
60h	The DMA page register test passed. Performing the DMA Controller 1 base register test next.
62h	The DMA controller 1 base register test passed. Performing the DMA controller 2 base register test next.
65h	The DMA controller 2 base register test passed. Programming DMA controllers 1 and 2 next.
66h	Completed programming DMA controllers 1 and 2. Initializing the 8259 interrupt controller next.
67h	Completed 8259 interrupt controller initialization.
7Fh	Extended NMI source enabling is in progress.
80h	The keyboard test has started. Clearing the output buffer and checking for stuck keys. Issuing the keyboard reset command next.
81h	A keyboard reset error or stuck key was found. Issuing the keyboard controller interface test command next.
82h	The keyboard controller interface test completed. Writing the command byte and initializing the circular buffer next.
83h	The command byte was written and global data initialization has completed. Checking for a locked key next.
84h	Locked key checking is over. Checking for a memory size mismatch with CMOS RAM data next.
85h	The memory size check is done. Displaying a soft error and checking for a password or bypassing WINBIOS Setup next.

Checkpoint	Code Description
86h	The password was checked. Performing any required programming before WINBIOS Setup next.
87h	The programming before WINBIOS Setup has completed. Uncompressing the WINBIOS Setup code and executing the AMIBIOS Setup or WINBIOS Setup utility next.
88h	Returned from WINBIOS Setup and cleared the screen. Performing any necessary programming after WINBIOS Setup next.
89h	The programming after WINBIOS Setup has completed. Displaying the power on screen message next.
8Ch	Programming the WINBIOS Setup options next.
8Dh	The WINBIOS Setup options are programmed. Resetting the hard disk controller next.
8Fh	The hard disk controller has been reset. Configuring the floppy drive controller next.
91h	The floppy drive controller has been configured. Configuring the hard disk drive controller next.
95h	Initializing the bus option ROMs from C800 next. See the last page of this chapter for additional information.
96h	Initializing before passing control to the adaptor ROM at C800.
97h	Initialization before the C800 adaptor ROM gains control has completed. The adaptor ROM check is next.
98h	The adaptor ROM had control and has now returned control to BIOS POST. Performing any required processing after the option ROM returned control.
99h	Any initialization required after the option ROM test has completed. Configuring the timer data area and printer base address next.
9Ah	Set the timer and printer base addresses. Setting the RS-232 base address next.
9Bh	Returned after setting the RS-232 base address. Performing any required initialization before the Coprocessor test next.
9Ch	Required initialization before the Coprocessor test is over. Initializing the Coprocessor next.
9Dh	Coprocessor initialized. Performing any required initialization after the Coprocessor test next.
9Eh	Initialization after the Coprocessor test is complete. Checking the extended keyboard, keyboard ID, and Num Lock key next. Issuing the keyboard ID command next.
A2h	Displaying any soft errors next.
A3h	The soft error display has completed. Setting the keyboard typematic rate next.
A4h	The keyboard typematic rate is set. Programming the memory wait states next.
A5h	Memory wait state programming is over. Clearing the screen and enabling parity and the NMI next.
A7h	NMI and parity enabled. Performing any initialization required before passing control to the adaptor ROM at E000 next.
A8h	Initialization before passing control to the adaptor ROM at E000h completed. Passing control to the adaptor ROM at E000h next.

Checkpoint	Code Description
A9h	Returned from adaptor ROM at E000h control. Performing any initialization required after the E000 option ROM had control next.
Aah	Initialization after E000 option ROM control has completed. Displaying the system configuration next.
Abh	Uncompressing the DMI data and executing DMI POST initialization next.
B0h	The system configuration is displayed.
B1h	Copying any code to specific areas.
00h	Code copying to specific areas is done. Passing control to INT 19h boot loader next.

Notes

Appendix C

System Specifications

Processors

Single or dual 64-bit AMD Socket F, Opteron 2000 type processors

Note: Please refer to our web site for a complete listing of supported processors.

Chipset

nVidia MCP55 Pro/IO-55 and NEC uPD720400

BIOS

8 Mb AMI® Flash ROM

Memory Capacity

Eight 240-pin DIMM slots that can support up to 64 GB of registered ECC DDR2-667/533/400 SDRAM

Note: Both interleaved and non-interleaved memory supported. Memory capacity is halved when using a single processor. See the memory section in Chapter 5 for details.

Serial ATA Controller

nVidia on-chip controller for 6-port Serial ATA (RAID 0, 1, 0+1 and 5 supported)

Serial ATA Drive Bays

Six (6) hot-swap drive bays to house six standard 3.5" SATA drives

Peripheral Drive Bays

One (1) 3.5" floppy drive

Two (2) 5.25" drive bays

Expansion Slots

Two PCI-Express x16 slots, one PCI-Express x8 slot, one PCI-Express x4 slot, one 133 MHz PCI-X slot and one 100 MHz PCI-X slot

Motherboard
Model: H8DAE-2 (Extended ATX form factor)
Dimensions: 12 x 13 in (305 x 330 mm)

Chassis
SC743T-645 Form Factor: tower/4U rackmount
Dimensions (as tower): (WxHxD) 7 x 17.1 x 25.5 in. (178 x 434 x 648 mm)

Weight
Gross (Bare Bone): 64 lbs. (29.1 kg.)

System Cooling
Four (4) 8-cm system cooling fans

System Input Requirements
AC Input Voltage: 100-240V AC auto-range
Rated Input Current: 100 (11A) - 240V (5A)
Rated Input Frequency: 50 to 60 Hz

PFC Power Supply
Rated Output Power: 645W (Model# SP645-PS, Part# PWS-0060)
Rated Output Voltages: +3.3V (30A), +5V (30A), +12V (46A), +5Vsb (4A), -12V (0.6A)

Operating Environment
Operating Temperature: 10º to 35º C (50º to 95º F)
Non-operating Temperature: -40º to 70º C (-40º to 158º F)
Operating Relative Humidity: 8% to 90% (non-condensing)
Non-operating Relative Humidity: 5 to 95% (non-condensing)

Regulatory Compliance
Electromagnetic Emissions:
FCC Class B, EN 55022 Class B, EN 61000-3-2/-3-3, CISPR 22 Class B

Electromagnetic Immunity:
EN 55024/CISPR 24, (EN 61000-4-2, EN 61000-4-3, EN 61000-4-4, EN 61000-4-5, EN 61000-4-6, EN 61000-4-8, EN 61000-4-11)

Safety:
EN 60950/IEC 60950-Compliant, UL Listed (USA), CUL Listed (Canada), TUV Certified (Germany), CE Marking (Europe)

California Best Management Practices Regulations for Perchlorate Materials:
This Perchlorate warning applies only to products containing CR (Manganese Dioxide) Lithium coin cells. “Perchlorate Material-special handling may apply. See www.dtsc.ca.gov/hazardouswaste/perchlorate”

Notes