

IPMI Firmware / BIOS Release Notes Form

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

Product Name	X10DSC-TP4S
Release Version	3.2 SPS: 3.1.3.72
Release Date	11/21/2019
Build Date	11/21/2019
Previous Version	3.1c
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Changed BIOS revision to 3.2. 2. Enabled RFC4122 UUID support. 3. Improved tCCD_L value. 4. Updated SINIT ACM 3.1.4 PW for Intel-SA00240 (CVE-2019-0151 CVSS 3.1: 7.5 High). 5. Updated Broadwell-EP B0/M0/R0 CPU microcode MEF406F1_0B000038 for Intel-SA00219 (CVE-2019-0117) and Intel-SA00220 (CVE-2019-0123).
New features	N/A
Fixes	N/A

Release Notes from Previous Release(s)

3.1c (6/14/2019)

1. *Changed BIOS revision to 3.1c.*
2. *Updated Intel Server Platform Services 3.1.3.72 for Grantley Refresh Platforms.*
3. *Updated Haswell-EP/Broadwell-EP CPU microcode from INTEL-SA-00233.*
4. *Updated VBIOS to 1.09.00.*

3.1 (6/11/2018)

1. *Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.*
2. *Changed BIOS revision to 3.1.*
3. *Set Descriptor Region (Bit 0) of BIOS Region Write Access to "No".*