

## IPMI Firmware / BIOS Release Notes Form

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| <b>Product Name</b>     | <b>H12DST-B</b>  |
| <b>Release Version</b>  | <b>1.0b</b>  |
| <b>Release Date</b>     | <b>11/18/2019</b>  |
| <b>Previous Version</b> | <b>1.0</b>   |
| <b>Update Category</b>  | <b>Recommended</b>   |
| <b>Dependencies</b>     | <b>N/A</b>   |
| <b>Important Notes</b>  | <b>BIOS Image: H12DST9.B18<br/>BIOS Update Package: H12DST9.B18.ZIP</b>  |
| <b>Enhancements</b>     | <ol style="list-style-type: none"><li>1. Updated BIOS version to 1.0b.</li><li>2. Added "DRAM Scrub Time" to Memory Configuration.</li><li>3. Updated AGESA RomePI to 1.0.0.4 based on 5.14_RomeCrb_0ACMK012.</li><li>4. Set AMD CBS "PCIe ARI Support" item to be used instead of "ARI Forwarding".</li><li>5. Updated item string "Input the description" and "HTTP Boot One Time" to adhere to Rome BIOS Setup Template v0.7_20190705.</li><li>6. Displayed 3rd IPMI version in BIOS setup.</li><li>7. Updated SSID of AMD Host Bridge according to each project's board ID.</li><li>8. Set IOMMU default to Auto (Enabled).</li><li>9. Updated type 9 for SIOM slot to 1 base.</li><li>10. Displayed "Preferred IO" item.</li><li>11. Updated ASPEED VBIOS to version 1.09.00.</li><li>12. Added item "4-link xGMI max speed" to NB Configuration.</li><li>13. Set the de-emphasis as -2.18 for SATA port 0-5 for BPN-ADP-6SATA3H4-1UB and BPN-SAS3-217BHQ-N4 SKU.</li></ol> |
| <b>New features</b>     | <b>N/A</b>   |

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| Fixes | <ol style="list-style-type: none"> <li>1. Fixed problem of BIOS logging ECC error as "Multi Bit ECC Memory Error" when system generates ECC error log.</li> <li>2. Fixed inability to create SLIC table after flashing OA2 with automation process.</li> <li>3. Fixed inability of SUM to preserve "Common" BIOS configuration setting after flashing BIOS.</li> <li>4. Fixed problem of NumLock/CapsLock/ScrollLock state clearing when user sends any key via Console redirection.</li> <li>5. Fixed failures of memory frequency and data fabric memory frequency to match and performance to match expected result.</li> <li>6. Added some AMD CBS items to CPU Configuration, NB Configuration, and ACPI Settings pages.</li> <li>7. Added item "DF Cstates" to NB configuration page.</li> <li>8. Hid items "APCB Version", "APOB Version", and "APPB Version".</li> <li>9. Fixed inability of IPMI command to change boot option order for UEFI USB key.</li> <li>10. Updated riser card string for auto testing.</li> <li>11. Fixed inability of BMC WebGUI to display hardware information.</li> <li>12. Corrected M.2 to "M.2-HC CPU1 PCI-E 3.0 X4" and SIOM to SIOM: CPU1 PCI-E 4.0 X16+X1 of Type 9 slot information.</li> <li>13. Corrected the DIMM location of IPMI "Memory Device Disabled".</li> <li>14. Fixed failure to restore boot priority to the default setting after flashing BIOS.</li> <li>15. Fixed "SMBIOS preservation" item.</li> <li>16. Fixed problem of the type 40 data maps to type 41 as unknown if the instance is larger than 1.</li> <li>17. Fixed problem of the HD Audio test of WHQL HLK with Windows Server 2019 detecting System Audio device existence to verify audio WHQL.</li> <li>18. Prevented display of any AMD memory error messages during the POST phase.</li> <li>19. Fixed malfunction of recovery.</li> <li>20. Added support for OOB SATA HDD information and asset information of 2 SATA controllers.</li> <li>21. Fixed missing screen output when Boot Mode is changed to EFI.</li> <li>22. Fixed the issue of "SMCI POST Screen Message" appearing on BIOS setup menu.</li> <li>23. Fixed the issue of "SMCI POST Screen Message" appearing on POST screen when executing EFI Shell application.</li> <li>24. Set PCIe bifurcation to x4x4x4x4 for AOC-SLG3-2M2.</li> </ol> |
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***Release Notes from Previous Release(s)***

**1.0 (8/16/2019)**

Initial Release