

## IPMI Firmware / BIOS Release Notes Form

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<b>Product Name</b>	<b>X10DDW-I</b>
<b>Release Version</b>	<b>3.2 SPS: 3.1.3.72</b>
<b>Release Date</b>	<b>12/16/2019</b>
<b>Build Date</b>	<b>12/16/2019</b>
<b>Previous Version</b>	<b>3.1a</b>
<b>Update Category</b>	<b>Recommended</b>
<b>Dependencies</b>	<b>None</b>
<b>Important Notes</b>	<b>None</b>
<b>Enhancements</b>	<b>1. Changed BIOS revision to 3.2. 2. Updated CPU µcode Haswell-EP C0/C1/M1 rev. 43 and Broadwell-EP B0/M0/R0 rev. 38. 3. Updated SINIT ACM 3.1.4 PW for Intel-SA00240 (CVE-2019-0151 CVSS 3.1: 7.5 High). 4. Added support for RFC4122 UUID format feature. 5. Updated initial value of memory tcdd.</b>
<b>New features</b>	<b>N/A</b>
<b>Fixes</b>	<b>N/A</b>

*Release Notes from Previous Release(s)*

**3.1a (5/17/2019)**

1. Updated CPU microcodes to Haswell-EP C0/C1/M1 rev. 43 and Broadwell-EP B0/M0/R0 rev. 36.
2. Updated Intel firmware to E5\_03.01.03.072.0\_WBG\_REL SPS.
3. Updated Intel VROC (RSTe) SATA/sSATA driver to version 6.1.0.1017.

**3.1 (3/22/2019)**

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
2. Updated ASTVBIOS to V1.09.00 to address CVE-2019-6260/Pantsdown.
3. Added energy performance configuration to Advanced Power Management.

**3.0a (3/1/2018)**

1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.
2. Updated RC 4.3.0.
3. Added Intel SPI vulnerability patch.
4. Implemented tCCD\_L.
5. Enabled setup options for slot1 of RSC-R2UW2E8E16 to be excluded from "Above 4G Decoding".
6. Filled SMBIOS type 11 default OEM string.
7. Fixed missing console redirection output when "Resolution 100x31" is disabled.