IPMI Firmware / BIOS Release Notes Form

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Product Name	X10DRH-CLN4/ILN4
Release Version	3.2 SPS: 3.1.3.72
Release Date	11/21/2019
Build Date	11/21/2019
Previous Version	3.1
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Changed BIOS revision to 3.2.
	2. Updated SINIT ACM 3.1.4 PW for Intel-SA00240 (CVE-2019-0151 CVSS
	3.1: 7.5 High).
	3. Updated Broadwell-EP B0/M0/R0 CPU microcode
	MEF406F1_0B000038 for Intel-SA00219 (CVE-2019-0117) and Intel-
	SA00220 (CVE-2019-0123).
	4. Updated Intel Server Platform Services 3.1.3.72 for Grantley Refresh
	Platforms.
	5. Updated Haswell-EP/Broadwell-EP CPU microcode from INTEL-SA-
	00233.
	6. Updated VBIOS from 1.03.01 to 1.09.00.
	7. Enabled RFC4122 UUID support.
New features	N/A
Fixes	N/A

Release Notes from Previous Release(s)

3.1 (6/11/2018)

1. Implemented enhancement to address 'Spectre' variant 2 (CVE-2018-3639 & CVE-2018-3640) security patch issue.

2. Updated RC to 4.3.0.

3. Changed BIOS revision to 3.1.

4. Updated SSATA and SATA RXEQ according to the lab test result.

- 5. Added "tCCD_L Relaxation" item under Memory Configuration menu.
- 6. Fixed issue with WHQL USB 3.0 mapping.
- 7. Fixed missing sensor readings for some NVDIA AOCs due to BIOS not sending data to IPMI.
- 8. Set Descriptor Region (Bit 0) of BIOS Region Write Access to "No".

3.0a (2/28/2018)

- 1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.
- 2. Updated RC to 4.3.0.
- 3. Changed BIOS revision to 3.0a.
- 4. Updated SSATA and SATA RXEQ according to the lab test result.
- 5. Added "tCCD_L Relaxation" item under Memory Configuration menu.
- 6. Fixed issue with WHQL USB 3.0 mapping.

7. Fixed missing sensor readings for some NVDIA AOCs due to BIOS not sending data to IPMI.