

IPMI Firmware / BIOS Release Notes Form

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Product Name	X10DRG-O(T)-CPU
Release Version	3.2 SPS: 3.1.3.72
Release Date	12/13/2019
Build Date	12/13/2019
Previous Version	3.1c
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Updated BIOS version to 3.2 for formal release. 2. Updated SINIT ACM 3.1.4 PW for Intel-SA00240 (CVE-2019-0151 CVSS 3.1: 7.5 High). 3. Updated Broadwell-EP B0/M0/R0 CPU microcode MEF406F1_0B000038 for Intel-SA00219 (CVE-2019-0117) and Intel-SA00220 (CVE-2019-0123). 4. Updated initial value of memory tcdd.
New features	N/A
Fixes	N/A

Release Notes from Previous Release(s)

3.1c (8/13/2019)

1. Updated BIOS version to 3.1c for formal release.
2. Updated Haswell microcode to 43 and Broadwell microcode to 36.
3. Updated VBIOS to v1.09.00.
4. Updated SPS version 05:3.1.3.72.
5. Updated module to support new UUID feature.
6. Fixed problem of Memtest86 startup hanging during multiprocessor test.

3.1 (7/13/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
2. Updated BIOS version to 3.1 for formal release.
3. Set Descriptor Region (Bit 0) of BIOS Region Write Access to "No".
4. Fixed issue of SMBIOS System Slot (Type 9) Bus Numbers not matching.
5. Fixed issue of PCIe being out of resources when LAN card and GPU cards are under one PLX bridge.

3.0b (4/23/2018)

1. Updated BIOS revision to 3.0b for formal release.
2. Fixed failure of add-on card (AOC-S3108L-H8iR) to display OPROM message on X9DRG-O-PCIE PCIe extended board.

3.0a (2/27/2018)

1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.
2. Updated BIOS version to 3.0a for formal release.
3. Updated for Intel security patch.
4. Updated Onboard VGA (Aspeed) VBIOS to v1.02.07.
5. Fixed problem of X10DRG-O+-CPU hanging at post code 33 after 544 cycles for system on/off.