IPMI Firmware / BIOS Release Notes Form

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

Product Name	H12DST-B
Release Version	1.0b
Release Date	12/12/2019
Previous Version	1.0
Update Category	Recommended
Dependencies	N/A
Important Notes	BIOS Image: H12DST9.C12 BIOS Update Package: H12DST9.C12.ZIP
Enhancements	 Updated BIOS version to 1.0b. Added "DRAM Scrub Time" to Memory Configuration. Updated AGESA RomePI to 1.0.0.4 based on 5.14_RomeCrb_0ACMK012. Set AMD CBS "PCIe ARI Support" item to be used instead of "ARI Forwarding". Updated item string "Input the description" and "HTTP Boot One Time" to adhere to Rome BIOS Setup Template v0.7_20190705. Displayed 3rd IPMI version in BIOS setup. Updated SSID of AMD Host Bridge according to each project's board ID. Set IOMMU default to Auto (Enabled). Updated type 9 for SIOM slot to 1 base. Displayed "Preferred IO" item. Updated ASPEED VBIOS to version 1.09.00. Added item "4-link xGMI max speed" to NB Configuration. Set the de-emphasis as -2.18 for SATA port 0-5 for BPN-ADP-6SATA3H4-1UB and BPN-SAS3-217BHQ-N4 SKU.
New features	N/A

- 1. Fixed problem of BIOS logging ECC error as "Multi Bit ECC Memory Error" when system generates ECC error log.
- 2. Fixed inability to create SLIC table after flashing OA2 with automation process.
- 3. Fixed inability of SUM to preserve "Common" BIOS configuration setting after flashing BIOS.
- 4. Fixed problem of NumLock/CapsLock/ScrollLock state clearing when user sends any key via Console redirection.
- 5. Fixed failures of memory frequency and data fabric memory frequency to match and performance to match expected result.
- 6. Added some AMD CBS items to CPU Configuration, NB Configuration, and ACPI Settings pages.
- 7. Added item "DF Cstates" to NB configuration page.
- 8. Hid items "APCB Version", "APOB Version", and "APPB Version".
- 9. Fixed inability of IPMI command to change boot option order for UEFI USB key.
- 10. Updated riser card string for auto testing.
- 11. Fixed inability of BMC WebGUI to display hardware information.
- 12. Corrected M.2 to "M.2-HC CPU1 PCI-E 3.0 X4" and SIOM to SIOM: CPU1 PCI-E 4.0 X16+X1 of Type 9 slot information.
- 13. Corrected the DIMM location of IPMI "Memory Device Disabled".
- 14. Fixed failure to restore boot priority to the default setting after flashing BIOS.
- 15. Fixed "SMBIOS preservation" item.
- 16. Fixed problem of the type 40 data maps to type 41 as unknown if the instance is larger than 1.
- 17. Fixed problem of the HD Audio test of WHQL HLK with Windows Server 2019 detecting System Audio device existence to verify audio WHQL.
- 18. Prevented display of any AMD memory error messages during the POST phase.
- 19. Fixed malfunction of recovery.
- 20. Added support for OOB SATA HDD information and asset information of 2 SATA controllers.
- 21. Fixed missing screen output when Boot Mode is changed to EFI.
- 22. Fixed the issue of "SMCI POST Screen Message" appearing on BIOS setup menu.
- 23. Fixed the issue of "SMCI POST Screen Message" appearing on POST screen when executing EFI Shell application.
- 24. Set PCIe bifurcation to x4x4x4x4 for AOC-SLG3-2M2.

Release Notes from Previous Release(s)

1.0 (8/16/2019)

Initial Release

Fixes