## **IPMI Firmware / BIOS Release Notes Form**

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Product Name	H12DST-B
Release Version	1.1
Release Date	1/10/2020
<b>Previous Version</b>	1.0b
<b>Update Category</b>	Recommended
Dependencies	N/A
Important Notes	BIOS Image: H12DSTB0.110 BIOS Update Package: H12DSTB0.110.ZIP
Enhancements	<ol> <li>Changed BIOS revision to 1.1.</li> <li>Updated AGESA RomePI to 1.0.0.5 based on 5.14_RomeCrb_0ACMK013.</li> <li>Displayed "PCI AER Support" setup item on ACPI page.</li> <li>Added patch to support AOC-M25G-i2S PXE boot.</li> </ol>
New features	N/A
Fixes	<ol> <li>Fixed issue of system hanging at post code A7h.</li> <li>Fixed inability of SUM to change the function of NUMA Node Per Socket.</li> </ol>

## 1.0b (12/12/2019)

- 1. Updated BIOS version to 1.0b.
- 2. Added "DRAM Scrub Time" to Memory Configuration.
- 3. Updated AGESA RomePI to 1.0.0.4 based on 5.14\_RomeCrb\_0ACMK012.
- 4. Set AMD CBS "PCIe ARI Support" item to be used instead of "ARI Forwarding".
- 5. Updated item string "Input the description" and "HTTP Boot One Time" to adhere to Rome BIOS Setup Template v0.7 20190705.
- 6. Displayed 3rd IPMI version in BIOS setup.
- 7. Updated SSID of AMD Host Bridge according to each project's board ID.
- 8. Set IOMMU default to Auto (Enabled).
- 9. Updated type 9 for SIOM slot to 1 base.
- 10. Displayed "Preferred IO" item.
- 11. Updated ASPEED VBIOS to version 1.09.00.
- 12. Added item "4-link xGMI max speed" to NB Configuration.
- 13. Set the de-emphasis as -2.18 for SATA port 0-5 for BPN-ADP-6SATA3H4-1UB and BPN-SAS3-217BHQ-N4 SKU.
- 14. Fixed problem of BIOS logging ECC error as "Multi Bit ECC Memory Error" when system generates ECC error log.
- 15. Fixed inability to create SLIC table after flashing OA2 with automation process.
- 16. Fixed inability of SUM to preserve "Common" BIOS configuration setting after flashing BIOS.
- 17. Fixed problem of NumLock/CapsLock/ScrollLock state clearing when user sends any key via Console redirection.
- 18. Fixed failures of memory frequency and data fabric memory frequency to match and performance to match expected result.
- 19. Added some AMD CBS items to CPU Configuration, NB Configuration, and ACPI Settings pages.
- 20. Added item "DF Cstates" to NB configuration page.
- 21. Hid items "APCB Version", "APOB Version", and "APPB Version".
- 22. Fixed inability of IPMI command to change boot option order for UEFI USB key.
- 23. Updated riser card string for auto testing.
- 24. Fixed inability of BMC WebGUI to display hardware information.
- 25. Corrected M.2 to "M.2-HC CPU1 PCI-E 3.0 X4" and SIOM to SIOM: CPU1 PCI-E 4.0 X16+X1 of Type 9 slot information.
- 26. Corrected the DIMM location of IPMI "Memory Device Disabled".
- 27. Fixed failure to restore boot priority to the default setting after flashing BIOS.
- 28. Fixed "SMBIOS preservation" item.
- 29. Fixed problem of the type 40 data maps to type 41 as unknown if the instance is larger than 1.
- 30. Fixed problem of the HD Audio test of WHQL HLK with Windows Server 2019 detecting System Audio device existence to verify audio WHQL.
- 31. Prevented display of any AMD memory error messages during the POST phase.
- 32. Fixed malfunction of recovery.
- 33. Added support for OOB SATA HDD information and asset information of 2 SATA controllers.
- 34. Fixed missing screen output when Boot Mode is changed to EFI.
- 35. Fixed the issue of "SMCI POST Screen Message" appearing on BIOS setup menu.
- 36. Fixed the issue of "SMCI POST Screen Message" appearing on POST screen when executing EFI Shell application.
- 37. Set PCIe bifurcation to x4x4x4x4 for AOC-SLG3-2M2.

## 1.0 (8/16/2019)

Initial Release