IPMI Firmware / BIOS Release Notes Form

Product Name	**H12SST-PS**
Release Version	**1.1**
Release Date	**2/21/2020**
Build Date	**2/21/2020**
Previous Version	**1.0c**
Update Category	**Recommended**
Dependencies	**N/A**
Important Notes	**BIOS Image: H12SST0.221** **BIOS Update Package: H12SST0.221.ZIP**
Enhancements	1. **Changed BIOS revision to 1.1.** 2. **Updated AGESA RomePI to 1.0.0.5 based on 5.14_RomeCrb_0ACMK013.** 3. **Displayed "PCI AER Support" setup item on ACPI page.** 4. **Added SMC HDD Security feature.**
New features	**N/A**
Fixes	1. **Fixed issue of system hanging at post code A7h.** 2. **Fixed inability of SUM to change the function of NUMA Node Per Socket.** 3. **Fixed problem of system sometimes rebooting during legacy Windows 2019 OS installation when using Rome CPU 7502.** 4. **Removed requirement to use Admin password for erasing TCG device.**

1.0c (11/25/2019)

1. Updated BIOS version to 1.0c.
2. Added "DRAM Scrub Time" to Memory Configuration.
3. Updated AGESA RomePI to 1.0.0.4 based on 5.14_RomeCrb_0ACMK012.
4. Set AMD CBS "PCIe ARI Support" item to be used instead of "ARI Forwarding".
5. Updated item string "Input the description" and "HTTP Boot One Time" to adhere to Rome BIOS Setup Template v0.7_20190705.
6. Displayed 3rd IPMI version in BIOS setup.
7. Updated SSID of AMD Host Bridge according to each project's board ID.
8. Set IOMMU default to Auto (Enabled)
9. Displayed "Preferred IO" item.
10. Prevented display of any AMD memory error messages during the POST phase.
11. Fixed malfunction of recovery.
12. Added support for OOB SATA HDD information and asset information of 2 SATA controllers.
13. Fixed missing screen output when Boot Mode is changed to EFI.
14. Fixed the issue of "SMCI POST Screen Message" appearing on BIOS setup menu.
15. Fixed the issue of "SMCI POST Screen Message" appearing on POST screen when executing EFI Shell application.
16. Fixed problem of SR-IOV enable Onboard LAN Option ROM item hiding.

1.0 (8/16/2019)

Initial Release