

IPMI Firmware / BIOS Release Notes Form

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Product Name	X11DSC+
Release Version	3.3
Release Date	02/21/2020
Previous Version	3.2
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Patched problem of system hanging at 94 with new NVidia RTX 6000/8000.2. Enabled saving memory CE location into PPR variable at runtime even if memory correctable error reporting is disabled.3. Added sighting CLX28 workaround and downgraded patrol scrub UC to CE.4. Added SMC HDD Security feature.5. Added support for Redfish feature.6. Updated BIOS ACM 1.7.40 and SINIT ACM 1.7.48 PW.7. Updated Skylake-SP/Cascade Lake-SP CPU microcode for Intel-SA-00329 (CVE2020-0548 2.8 Low and CVE2020-0549 6.5 Medium), Intel-SA-00288 (CVE2019-11157 5.3 Medium), and Intel-SA-00317 (CVE2019-14607 7.9 High).8. Added support for Redfish functions.

	<p>9. Updated AMI label 5.14_PurleyCrb_0ACLA050 beta for IPU2020.1 PV.</p> <p>10. Updated SPS_E5_04.01.04.381 from IPU 2020.1 PV.</p> <p>11. Added setup item "HDD password prompt Control" to control "Hard-Drive Password Check" for enabling/disabling HDD password prompt window during POST.</p>
New features	None
Fixes	<p>1. Removed requirement to use Admin password for erasing TCG device.</p> <p>2. Fixed problem of two CentOS boot items occurring in boot order if CentOS is installed in RAID 1 system.</p>

Release Notes from Previous Release(s)

3.2 (10/18/2019)

1. Updated AMI label 5.14_PurleyCrb_0ACLA049_BETA for BKC WW36 IPU 2019.2 and AMI security update SA50072.
2. Updated SINIT/BIOS ACM from BKC WW36 IPU 2019.2 to address CVE-2019-0151 and CVE-2019-0152.
3. Updated SPS_E5_04.01.04.339.0 from BKC WW36 IPU 2019.2 to address PSIRT-TA-201905-011.
4. Updated Skylake-SP and Cascade Lake-SP CPU microcode.
5. Displayed Setup item "ARI Support".
6. Added setup item to control 16Gb based Single Die Package DIMM tRFC optimization.
7. Disabled ADDDC/SDDC and set PPR as hPPR.
8. Added Enhanced PPR function and set disabled as default.
9. Removed PXE option for "OnBoard LanX Option ROM" when "Boot mode select" is UEFI.
10. Corrected display of the IPMI AUX revision.

3.1 (5/22/2019)

1. Added support for Purley Refresh platform.
2. Enhanced BIOS setup menu to auto-switch the Option ROM's value and hide the unexpected value when boot mode changes.
3. Updated to SPS 4.0.04.381 or above for INTEL-SA-00213 Security Advisory.
4. Updated Skylake-SP/Cascade Lake-SP CPU microcode.
5. Updated Intel BKCWW16 2019 PV PLR1.
6. Updated SPS_E5_04.01.04.296.0 from BKC WW16 2019.
7. Updated EIP467272 for AMI SA50069, SA50070.
8. Set SDDC Plus One or SDDC to disabled by default.
9. Updated SATA/sSATA RAID OPROM/EFI driver to VROC PreOS v6.1.0.1017.
10. Set ADDDC to enabled by default.

11. Fixed problem of UUID showing IPMI MAC incorrectly after disabling onboard LAN chip.
12. Temporarily fixed error for SKX (CLX) 4114 CPU memory training with Micron (18ASF2G72PDZ-2G6E1) RDIMM.
13. Fixed failure to boot into VMware OS when set to Maximum Performance even if Monitor/MWAIT is enabled.
14. Fixed problem of system halting or rebooting at POST code 0xB2 or 0x92 when total GPT partition number is more than 256 and RSD is enabled.
15. Fixed problem of "[1;31;40m" showing on POST screen when EFI driver is "Unhealthy".
16. Fixed inability to change IPv6 address or IPv6 Router1 IP address.
17. Set Leaky Bucket that can decrease one memory correctable error count within 2.15 minutes with threshold 512.
18. Fixed malfunction of workaround for GPU P2P low bandwidth.

3.0b (3/4/2019)

1. Added support for Purley Refresh platform.
2. Updated CPU microcodes from SRV_P_270.
3. Updated SINIT ACM 1.7.2 PW from BKC WW06 2019.
4. Updated SPS_E5_04.01.04.251.0 from Intel VIP Kit #130885.
5. Updated SATA RAID OPRM/EFI driver to RSTe PreOS v6.0.0.1024.
6. Set PCIe correctable and uncorrectable (non-fatal) error report to disabled by default.
7. Added support for RFC4122 UUID format feature to set RFC4122 encoding from build time produced by IPMICFG 1.29 tool or newer.
8. Disabled unused SPS ME "CUPS IIO" and "Volumetric Airflow" sensor.
9. Added support for Linux built-in utility efibootmgr.
10. Updated IPv6 router-related setup item string.
11. Reduced redundant reboot for offboard VGA switching.
12. Set NVDIMM ADR timeout to 600us.
13. Updated to SPS 4.1.02.174 or above for INTEL-SA-00185 Security Advisory and RC 549.D13 or above for INTEL-SA-00192 Security Advisory.
14. Fixed problem of setup item "Quiet Boot" staying enabled after flashing BIOS with "Quiet Boot" disabled.
15. Modified UEFI network description to IPv4/IPv6 to follow Industry Standard.
16. Patched problem of system hanging at 0x94 when plugging in Nvidia Tesla T4 card.
17. Fixed inability of "Network Stack"-related items to get/change via SUM OOB method.

2.1 (7/6/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
2. Updated BIOS ACM 1.3.7 and SINIT ACM 1.3.4.
3. Updated SPS 4.00.04.340 PL version.
4. Updated 5.12_PurleyCrb_0ACFD088 for Purley Skylake platform PLR7, BKC 2018 WW20.
5. Updated SATA RAID OPRM/EFI driver to RSTe PreOS v5.4.0.1039.
6. Added support for UEFI mode PXE boot of F12 hot key Net boot.
7. Added BIOS/ME downgrade check for SPS 4.0.4.340.
8. Added support for RFC3021.
9. Corrected help message for TPH BIOS setup items.
10. Displayed PPR setup item.
11. Added support for SATA FLR with enabled as default.
12. Added a patch to prevent reboot hang when installing AVAL APX-3224 card.

13. Added one event log to record that the event log is full.
14. Fixed problem of SUM GetSataInfo showing incorrect "Configuration Type" when setting "Configure sSATA as" to "AHCI" or "RAID" on sSATA controller.
15. Fixed problem of Ctrl + Alt + Del causing system to hang after flashing BIOS.
16. Fixed inability of AFUWIN to keep VMD setting.
17. Fixed failure of WDT function.
18. Fixed inability of SMBIOS Type 40 to report SIOM card AOC-MTG-i2T/i4T information.
19. Fixed problem of system repeatedly rebooting when SIOM card AOC-MTG-i4T is plugged in.

2.0b (2/26/2018)

1. Updated 5.12_PurleyCrb_OACFD085Beta for Purley Skylake platform PLR 4, BKC WW02.
2. Updated SPS 4.00.04.294 PLR 3.1 PV version.
3. Updated DMI CTLE value.
4. Updated re-driver for AOC-SLG3-2E4R.
5. Added setup item "SmcBusMasterEn" for enabling Parent bridge of the devices in the gSmcPciForceEnableBusMasterList.
6. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability, with Enabled as default.
7. Updated CPU microcode SRV_B_216 for Skylake-SP H0/M0/U0 stepping CPUs.
8. Updated BIOS version to 2.0b.
9. Updated BIOS ACM 1.3.5 and SINIT ACM 1.3.3.
10. Changed maximum speed in SMBIOS type 4 to 4500Mhz.
11. Added one event log to record that the event log is full.
12. Added support for VMD settings to be preserved after flashing.
13. Changed RC default setting to upgrade POST Memory correctable error to uncorrectable error and map out the channel.
14. Fixed inability of BIOS to boot into OS with Intel P3608 PCIe NVMe drive installed.
15. Fixed issue with IPMI force boot.
16. Fixed issue of all commands requesting to be persistent.
17. Fixed malfunction of "SMBIOS Preservation" Disabled.
18. Fixed problem of SMC skipping first boot option in UEFI mode when retrying boot.
19. Fixed problem of the endpoint PCIe device having error bits in PCI Status or Device Status register.
20. Fixed inability to set memory policy.
21. Fixed inability to reset the system under DOS by pressing "Ctrl-Alt-Del" on USB keyboard when "Port 60/64 Emulation" is disabled.
22. Fixed failure of BIOS ECO ATT test case 306.
23. Disabled CPU2 IIO PCIe root port ACPI hot plug function.
24. Fixed inability of some CPUs to display correct microcode revision under BIOS setup menu.s