IPMI Firmware / BIOS Release Notes Form

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

D 1 1/ 1						
Release Version	3.3					
Release Date	02/21/2020					
Build Date	02/21/2020					
Previous Version	3.0					
Update Category	Critical					
Dependencies	None					
Important Notes	None					
	1. Disabled ADDDC/SDDC and set PPR as hPPR.					
	2. Updated AMI label 5.14 PurleyCrb_0ACLA049_BETA for BKC WW36					
	IPU 2019.2 and AMI security update SA50072.					
	3. Updated SINIT/BIOS ACM from BKC WW36 IPU 2019.2 to address					
	CVE-2019-0151 and CVE-2019-0152.					
	4. Updated SPS_E5_04.01.04.339.0 from BKC WW36 IPU 2019.2 to					
Enhancements	address PSIRT-TA-201905-011.					
	5. Updated Cascade Lake-SP CPU microcode and Skylake-SP microcode.					
	6. Added support for firmware version information.					
	7. Added support for fd0v.					
	8. Displayed Setup item "ARI Support".					
	9. Enabled saving memory CE location into PPR variable at runtime					
	even if memory correctable error reporting is disabled.					
	10. Added sighting CLX28 workaround and downgraded patrol scrub UC					

	to CE.				
	11. Added SMC HDD Security feature.				
	12. Updated AMI label 5.14_PurleyCrb_0ACLA050 beta for IPU2020.1				
	PV.				
	13. Updated SPS_E5_04.01.04.381 from IPU 2020.1 PV.				
	14. Updated BIOS ACM 1.7.40 and SINIT ACM 1.7.48 PW.				
	15. Added setup item "HDD password prompt Control" to control				
	"Hard-Drive Password Check" for enabling/disabling HDD password				
	prompt window during POST.				
	16. Updated Skylake-SP/Cascade Lake-SP CPU microcode for Intel-SA-				
	00329 (CVE2020-0548 2.8 Low and CVE2020-0549 6.5 Medium), Intel-				
	SA-00288 (CVE2019-11157 5.3 Medium), and Intel-SA-00317 (CVE2019-				
	14607 7.9 High).				
New features	1. Added Enhanced PPR function and set disabled as default.				
Fixes	1. Fixed failure of IPMI event log Date/Time to sync to BIOS System				
	Date/Time for ME project.				
	2. Fixed ability to see memory correctable error event during MRC				
	when use a single bit bad DIMM.				
	3. Removed PXE option for "OnBoard LanX Option ROM" when "Boot				
	mode select" is UEFI.				
	4. Corrected display of the IPMI AUX revision.				
	5. Fixed issue of system resetting under ATTO Fiber network card legacy				
	OPROM user menu.				
	6. Fixed mismatch of Secure Boot Mode value.				
	7. Removed requirement to use Admin password for erasing TCG				
	device.				
	8. Fixed problem of two CentOS boot items occurring in boot order if				
	CentOS is installed in RAID 1 system.				

Release Notes from Previous Release(s)	Release	Notes	from	Previous	Release(s)
--	---------	-------	------	----------	------------

				_
2 N	/ 2 /	ne.	/201	a
J.U	100/	UJ/	ZUI	J

1. First Release