IPMI Firmware / BIOS Release Notes Form

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

Product Name	X10SLM+-LN4F
Release Version	3.3
Release Date	3/27/2020
Build Date	3/27/2020
Previous Version	3.1
Update Category	Critical
Dependencies	None
Important Notes	None
Enhancements	1. Updated the SINIT ACM to 20190708 for Denlow Server IPU 2019.2
	for INTEL-SA-00240 to address CVE-2019-0151 (7.5, High) security
	issue.
	2. Updated SPS firmware to SPS_EN_03.00.07.060.0 for Denlow
	Server IPU 2019.2.
	3. Updated Broadwell-E3/Broadwell-H stepping CPU microcode
	M2240671_00000022 (E-0 G-0 Stepping) and Haswell/Haswell-E3
	stepping CPU microcode M32306C3_00000028 (C-0 Stepping) for
	Denlow Server IPU 2020.1.
	4. Changed BIOS revision to 3.3.
	5. Adjusted IPMI KCS command delay time to prevent failure to
	respond to get PSU info command when AC power cycle IPMI initial
	time is too long.

New features	N/A
Fixes	1. Enabled getting BIOS CFG file by SUM Inband.

Release Notes from Previous Release(s)

3.1 (2/14/2018)

- 1. Updated CPU microcode to address 'Spectre' variant 2 (CVE 2017-5715) security issue.
- 2. Added Intel SPI vulnerability patch.
- 3. Changed BIOS revision to 3.1.