

## IPMI Firmware / BIOS Release Notes Form

*Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.*

<b>Product Name</b>	<b>H11DSU-iN</b>
<b>Release Version</b>	<b>2.1b</b>
<b>Release Date</b>	<b>06/09/2020</b>
<b>Build Date</b>	<b>06/09/2020</b>
<b>Previous Version</b>	<b>2.1a</b>
<b>Update Category</b>	<b>Recommended</b>
<b>Dependencies</b>	<b>MB Rev 2.10</b>
<b>Important Notes</b>	<b>BIOS Image: H11DSU0.609_D32 BIOS Update Package: H11DSU0.609_D32.zip Please update BIOS with attached Flash Utility in package.</b>
<b>Enhancements</b>	<b>1. Changed BIOS revision to 2.1b.</b>
<b>New features</b>	<b>N/A</b>
<b>Fixes</b>	<b>1. Disabled NVDIMM function to patch HW redundant power failure designed limitation.</b>

**2.1a (05/14/2020)**

1. Changed BIOS revision to 2.1a.
2. Updated AGESA RomePI to 1.0.0.7.
3. Updated 8310 SSP-B2 microcode 8301038.
4. Fixed problem of system hanging on POSTCODE 0xB2 when JPG1 is set to disabled and the VGA card is plugged in.
5. Added SMCI HDD Security feature.
6. Fixed failure of FRU0 - Manufacturer Name(PM) to sync to SMBIOS Type 3 - Manufacturer(CM).
7. Removed "\$SMCUNHIDE\$" string from "PCI AER support" setup item help string.
8. Added AMD IOMMU patch code for fixing problem of NVMe devices dropping and hardware error in RHEL 7.x.
9. Fixed TCG admin password reverse bug.

**2.1 (02/21/2020)**

1. Changed BIOS revision to 2.1.
2. Updated AGESA RomePI to 1.0.0.5 based on 5.14\_RomeCrb\_0AC MK013.
3. Displayed "PCI AER Support" setup item on ACPI page.
4. Added SMC HDD Security feature.
5. Removed requirement to use Admin password for erasing TCG device.
6. Fixed issue of system hanging at post code A7h.
7. Fixed inability of SUM to change the function of NUMA Node Per Socket.
8. Fixed problem of system sometimes rebooting during legacy Windows 2019 OS installation when using Rome CPU 7502.

**2.0b (11/15/2019)**

1. Changed BIOS revision to 2.0b.
2. Added "DRAM Scrub Time" to Memory Configuration.
3. Updated AGESA RomePI to 1.0.0.4 based on 5.14\_RomeCrb\_0ACMK012.
4. Set AMD CBS "PCIe ARI Support" item to be used instead of "ARI Forwarding".
5. Updated item string "Input the description" and "HTTP Boot One Time" to adhere to Rome BIOS Setup Template v0.7\_20190705.
6. Displayed 3rd IPMI version in BIOS setup.
7. Updated SSID of AMD Host Bridge according to each project's board ID.
8. Forced all PCIe to Gen3 only for H11 drop-in projects.
9. Displayed the "4-link xGMI max speed" setup item and set to 10.667Gbps by default on H11 DP drop-in projects.
10. Set IOMMU default to Auto (Enabled).
11. Displayed "Preferred IO" item.
12. Prevented display of any AMD memory error messages during the POST phase.
13. Fixed malfunction of recovery.
14. Added support for OOB SATA HDD information and asset information of 2 SATA controllers.
15. Fixed missing screen output when Boot Mode is changed to EFI.

16. Fixed problem of system hanging when installing NVidia RTX 2080/5000/6000.
17. Fixed the issue of "SMCI POST Screen Message" appearing on BIOS setup menu.
18. Fixed the issue of "SMCI POST Screen Message" appearing on POST screen when executing EFI Shell application.
19. Fixed problem of the xGMI speed reaching 16G after pressing "load default."
20. Fixed problem of the default xGMI speed not being 10.6G.

## **2.0 (8/16/2019)**

1. First Release.
2. Fixed the PXE error message "No enough memory to load an image" when load FTU7.
3. Fixed the issue of AOC sensor reading.