## **IPMI Firmware / BIOS Release Notes Form**

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Product Name	X11SRL-F
Release Version	2.1
Release Date	6/19/2020
Build Date	6/19/2020
<b>Previous Version</b>	2.0a
Update Category	Critical
Dependencies	None
Important Notes	None
Enhancements	1. Updated the BIOS version to 2.1.
	2. Enhanced support for Glacier Falls WS Turbo Boost 3.0 feature.
	3. Updated ME 11.12.77.1664 for INTEL-SA-00295 Security Advisory to
	address CVE-2020-0542 (7.8, High), CVE-2020-0532 (7.1, High), CVE-
	2020-0538 (7.5, High), CVE-2020-0534 (7.5, High), CVE-2020-0541 (6.7,
	Medium), CVE-2020-0533 (7.5, High), CVE-2020-0537 (4.9, Medium),
	CVE-2020-0531 (6.5, Medium), CVE-2020-0535 (5.3, Medium), CVE-
	2020-0536 (5.5, Medium), CVE-2020-0545 (4.4, Medium), CVE-2020-
	0540 (5.3, Medium), CVE-2020-0566 (7.3, High), CVE-2020-0539 (3.3,
	Low), CVE-2020-0586 (7.3, High), CVE-2020-0594 (9.8, Critical), CVE-
	2020-0595 (9.8, Critical), CVE-2020-0596 (7.5, High), CVE-2020-8674
	(4.3, Medium), and CVE-2020-0597 (6.5, Medium) security issues.
	4. Set "No memory DIMM detected, install memory DIMMs" string to
	display when there is no DIMM in a slot.

	5. Added back the "NVMe Configuration" items.
	6. Updated Intel RSTe RAID Option ROM/UEFI Driver to 6.3.0.1005.
	7. Updated Skylake microcode MB750654_02006906 for INTEL-SA-
	00329 Security Advisory to address CVE-2020-0548 (2.8, Low) and
	CVE-2020-0549 (6.5, Medium) security issues and Cascade Lake
	microcodes MBF50657_05002F01 and MBF50656_04002F01 for
	INTEL-SA-00329 Security Advisory to address CVE-2020-0548 (2.8,
	Low) and CVE-2020-0549 (6.5, Medium) security issues.
	8. Swapped CPU Slot 3 and Slot 4 strings.
	9. Updated RC code to 1524.D00 for GlacierFallsWS IPU 2020.1.
New features	N/A
Fixes	1. Fixed failure of system to boot when plugging in K80.
	2. Fixed problem of the SMBIOS type 17 manufacturing date being
	empty.
	3. Fixed problem of the system hanging up when EuP mode is enabled
	then CMOS is cleaned.
	4. Fixed failure of SUM TC: 210/304/408 test.
	5. Corrected DMI Type2 when the PCD product name is shorter than
	the FRU1 product name.

## Release Notes from Previous Release(s)

## 2.0a (2/26/2020)

1. Updated Glacier Falls RC code to 2.0.44.50.

2. Added support for system boot up when plugging in more than 4 VGA add-on cards.

3. Added ACS control item to Advanced/Chipset Configuration/North Bridge/IIO Configuration/Intel VT for Directed I/O (VT-d) page.

4. Added function to limit the first priority VGA use to under 4G resource.

5. Removed the "System Firmware Error (POST ERROR)" error log from BMC and "EFI 01030006" from BIOS event log.

6. Fixed problem of the system hanging up at CP: 3B when plugging in 5 VGAs to the system, including one VGA in the PCH.

## 2.0 (12/11/2019)

1. Added support for Glacier Falls CLX-W CPU.

2. Updated PCIe module in the Glacier Falls platform.

3. Updated the USB module in the Glacier Falls platform.

4. Updated the TPM module in the Glacier Falls platform.

5. Updated the console redirection module in the Glacier Falls platform.

6. Updated the serial port function in the Glacier Falls platform.

7. Updated the power policy setting in the Glacier Falls platform.

8. Updated the SATA module function in the Glacier Falls platform.

9. Added SMC Password style in the Glacier Falls Projects.

10. Added AER and MCE items.

11. Added "SMBIOS Preservation" Enabled/Disabled item for flash recovery.

12. Updated ME 11.12.00.1622.

13. Updated VROC VMD driver, RSTe UEFI driver, and Legacy ROM to 6.2.0.1034.

14. Implemented prompt message at post screen when entering BIOS recovery mode for the platform to support early video.

15. Updated Skylake microcode MB750654\_02000065 for INTEL-SA-00271 Security Advisory to address CVE-2019-11139 (5.8, Medium) security issue and Cascade Lake microcodes MBF50656\_0400002C & MBF50657\_0500002C for INTEL-SA-00270 Security Advisory to address CVE-2019-11135 (6.5, Medium) security issue.

16. Set Memory Topology to hide empty items when there is no memory in slots.

17. Modified the POST time (DXE) POST code foreground and background color for Supermicro logo during quiet boot.

18. Updated Basin Falls BIOS ACM and SINIT ACM to PW signed version.

19. Fixed inability of system to boot when setting BIOS setup menu CSM to disabled.

20. Fixed failure of recovery BIOS when plugging in TPM 1.2 or TPM 2.0 device.

21. Fixed problem of the "Storage Option ROM/UEFI Driver" being masked when using AMI SCE tool to dump to BIOS setup items.

22. Fixed problem of non-support status returning when using AFU tool to clean event log.

23. Fixed inability to change the VGA priority by SMC Option ROM control function.

24. Set message to show in the onboard VGA output when changing VGA priority to add-on card.

25. Added item "Storage Option ROM/UEFI Driver" to Advanced/SATA and RST Configuration page.

26. Fixed inability of X11SRL to use AFU to flash BIOS when secure flash is enabled.

27. Fixed inability to load VMD driver when enabling VMD ports under "Intel VMD Technology" menu.

28. Prevented CMOS from writing failed status.

29. Fixed failure of SUM test case 308.

*30. Corrected the memory Data Width.* 

*31. Fixed problem of key details showing "Security Violation" after loading Factory secure boot keys.* 

32. Updated the "Change SMBIOS Event Log Settings" string.

33. Set IPMI WDT function to enabled.

34. Fixed ability to enter Setup Menu if ADMIN password is set after pressing "Enter".

35. Fixed failure of serial port when using PC Check tool test.

*36. Enabled the PROCHOT pin as BIDIRECTIONAL by default to support processor hot feature.* 

*37. Fixed malfunction of the Watchdog.* 

*38. Fixed failure of X11SRL SOL function test.* 

*39. Fixed failure of user password to clear after clearing administrator password.* 

40. Fixed inability to use IPMI Web to eject when plugging in NVMe add-on card (AOC-SLG3-4E4).

41. Fixed failure of IPMI web setting to sync with BIOS IPMI page.

42. Fixed problem of system hanging up in CP: A2h when running PCH on/off test.

43. Fixed problem of PCH temperature reading fail event log occurring for IPMI when updating BIOS with the ME.