

BIOS Release Notes Form

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Product Name	H12DST-B
Release Version	1.2
Release Date	9/16/2020
Build Date	9/16/2020
Previous Version	1.1
Update Category	Recommended
Dependencies	N/A
Important Notes	BIOS Image: H12DSTB0.916 BIOS Update Package: H12DSTB0.916.ZIP
Enhancements	<ol style="list-style-type: none">1. Changed BIOS revision to 1.2.2. Updated AGESA RomePI to 1.0.0.8.3. Updated 8310 SSP-B0 microcode 8301038.4. Added SMCI HDD Security feature.5. Updated help string of item "Input the description".6. Set GPIO 108 output to high and internal pull to high.7. Displayed "Relaxed Ordering" setup item and set to disabled by default.
New features	N/A
Fixes	<ol style="list-style-type: none">1. Fixed failures of FRU0 - Manufacturer Name (PM) to sync to SMBIOS Type 3 - Manufacturer (CM) and FRU0 - Product Part/Model Number (PPM) to sync to SMBIOS Type 1 - ProductName (PN).2. Removed "\$SMCUNHIDE\$" string from "PCI AER support" setup item help string.3. Added AMD IOMMU patch code to fix NVMe Devices drop and Hardware error in RH 7.x.4. Fixed problem of TCG admin password reverting.5. Fixed problem of "Update IPMI LAN Configuration" staying set to Yes when "IPv6 support" setup item is disabled.6. Corrected CPU speed information in BIOS setup.7. Fixed failure of Mellanox CX6 of AOC-MIBE6-m1CM to fill in VPD data to Type 40.

Release Notes from Previous Release(s)

1.1 (1/10/2020)

- 1 Changed BIOS revision to 1.1.
- 2 Updated AGESA RomePI to 1.0.0.5 based on 5.14_RomeCrb_OACMK013.
- 3 Displayed "PCI AER Support" setup item on ACPI page.
- 4 Added patch to support AOC-M25G-i2S PXE boot.
- 5 Fixed issue of system hanging at post code A7h.
- 6 Fixed inability of SUM to change the function of NUMA Node Per Socket.

1.0b (12/12/2019)

- 1 Updated BIOS version to 1.0b.
- 2 Added "DRAM Scrub Time" to Memory Configuration.
- 3 Updated AGESA RomePI to 1.0.0.4 based on 5.14_RomeCrb_OACMK012.
- 4 Set AMD CBS "PCIe ARI Support" item to be used instead of "ARI Forwarding".
- 5 Updated item string "Input the description" and "HTTP Boot One Time" to adhere to Rome BIOS Setup Template v0.7_20190705.
- 6 Displayed 3rd IPMI version in BIOS setup.
- 7 Updated SSID of AMD Host Bridge according to each project's board ID.
- 8 Set IOMMU default to Auto (Enabled).
- 9 Updated type 9 for SIOM slot to 1 base.
- 10 Displayed "Preferred IO" item.
- 11 Updated ASPEED VBIOS to version 1.09.00.
- 12 Added item "4-link xGMI max speed" to NB Configuration.
- 13 Set the de-emphasis as -2.18 for SATA port 0-5 for BPN-ADP-6SATA3H4-1UB and BPN-SAS3-217BHQ-N4 SKU.
- 14 Fixed problem of BIOS logging ECC error as "Multi Bit ECC Memory Error" when system generates ECC error log.
- 15 Fixed inability to create SLIC table after flashing OA2 with automation process.
- 16 Fixed inability of SUM to preserve "Common" BIOS configuration setting after flashing BIOS.
- 17 Fixed problem of NumLock/CapsLock/ScrollLock state clearing when user sends any key via Console redirection.
- 18 Fixed failures of memory frequency and data fabric memory frequency to match and performance to match expected result.
- 19 Added some AMD CBS items to CPU Configuration, NB Configuration, and ACPI Settings pages.
- 20 Added item "DF Cstates" to NB configuration page.
- 21 Hid items "APCB Version", "APOB Version", and "APPB Version".
- 22 Fixed inability of IPMI command to change boot option order for UEFI USB key.
- 23 Updated riser card string for auto testing.
- 24 Fixed inability of BMC WebGUI to display hardware information.
- 25 Corrected M.2 to "M.2-HC CPU1 PCI-E 3.0 X4" and SIOM to SIOM: CPU1 PCI-E 4.0 X16+X1 of Type 9 slot information.
- 26 Corrected the DIMM location of IPMI "Memory Device Disabled".
- 27 Fixed failure to restore boot priority to the default setting after flashing BIOS.
- 28 Fixed "SMBIOS preservation" item.
- 29 Fixed problem of the type 40 data maps to type 41 as unknown if the instance is larger than 1.
- 30 Fixed problem of the HD Audio test of WHQL HLK with Windows Server 2019 detecting System Audio device existence to verify audio WHQL.
- 31 Prevented display of any AMD memory error messages during the POST phase.
- 32 Fixed malfunction of recovery.
- 33 Added support for OOB SATA HDD information and asset information of 2 SATA controllers.

- 34 Fixed missing screen output when Boot Mode is changed to EFI.
- 35 Fixed the issue of "SMCI POST Screen Message" appearing on BIOS setup menu.
- 36 Fixed the issue of "SMCI POST Screen Message" appearing on POST screen when executing EFI Shell application.
- 37 Set PCIe bifurcation to x4x4x4x4 for AOC-SLG3-2M2.

1.0 (8/16/2019)

Initial Release