# **BIOS Release Notes Form**

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Product Name	H12DSU-iN
Release Version	2.0
Release Date	2/22/2021
<b>Build Date</b>	2/22/2021
Previous Version	1.3
<b>Update Category</b>	Recommended
Dependencies	N/A
Important Notes	BIOS Image: BIOS_H12DSU-1B54_20210222_2.0_STDsp.bin BIOS Update Package: BIOS_H12DSU-1B54_20210222_2.0_STDsp_Up.zip Notice: 1) BIOS R 2.x supports 7002 and next generation processor. 2) Flash Utility in the package supports BIOS update from R 1.x to R 2.x only, cannot roll back.
Enhancements	<ol> <li>Changed BIOS revision to 2.0.</li> <li>Updated AGESA to 1.0.0.1 for next generation processors.</li> <li>Updated A011 GN B1 microcode 0A001119.</li> <li>Updated AGESA RomePI to 1.0.0.A.</li> <li>Fixed problem of BIOS initialization showing IPV6 address when IPV6 is disabled in the IPMI GUI.</li> <li>Enhanced SMBIOS Type39 System Power Supply information.</li> <li>Displayed TSME, DDR Power Down Enable, PCIe Ten Bit Support, xGMI Link Width Control, xGMI Force Link Width, xGMI Max Link Width Control, xGMI Max Link Width, and xGMI Link Max Speed for GPU performance tuning.</li> </ol>
New features	N/A
Fixes	N/A

#### Release Notes from Previous Release(s)

#### 1.3 (11/25/2020)

- 1. Changed BIOS revision to 1.3.
- 2. Updated AGESA RomePI to 1.0.0.9.
- 3. Updated 8310 SSP-B0 microcode 830104D.
- 4. Displayed some items for GPU performance tuning.

## 1.2 (8/10/2020)

- 1. Changed BIOS revision to 1.2.
- 2. Updated AGESA RomePI to 1.0.0.8.
- 3. Updated help string of item "Input the description".
- 4. Corrected CPU speed information in BIOS setup.

## 1.0a (6/10/2020)

- 1. Changed BIOS revision to 1.0a.
- 2. Added new Gen1 ID support on BPN-SAS3-119A-N12 for new backplane CPLD firmware use.

## 1.0 (5/18/2020)

**Initial Release**