

## BIOS Release Notes Form

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<b>Product Name</b>	<b>X10SLL+-F</b>
<b>Release Version</b>	<b>3.4</b>
<b>Release Date</b>	<b>3/10/2021</b>
<b>Build Date</b>	<b>3/10/2021</b>
<b>Previous Version</b>	<b>3.3</b>
<b>Update Category</b>	<b>Critical</b>
<b>Dependencies</b>	<b>None</b>
<b>Important Notes</b>	<b>None</b>
<b>Enhancements</b>	<b>N/A</b>
<b>New features</b>	<b>N/A</b>
<b>Fixes</b>	<b>1. Fixed failure of chipsec items BLE, BWP, and TCO SMI lock.</b>

**Release Notes from Previous Release(s)**

**3.3 (3/28/2020)**

1. Updated the SINIT ACM to 20190708 for Denlow Server IPU 2019.2 for INTEL-SA-00240 to address CVE-2019-0151 (7.5, High) security issue.
2. Updated SPS firmware to SPS\_EN\_03.00.07.060.0 for Denlow Server IPU 2019.2.
3. Updated Broadwell-E3/Broadwell-H stepping CPU microcode M2240671\_00000022 (E-0 G-0 Stepping) and Haswell/Haswell-E3 stepping CPU microcode M32306C3\_00000028 (C-0 Stepping) for Denlow Server IPU 2020.1.
4. Changed BIOS revision to 3.3.
5. Adjusted IPMI KCS command delay time to prevent failure to respond to get PSU info command when AC power cycle IPMI initial time is too long.
6. Enabled getting BIOS CFG file by SUM Inband.

**3.2 (5/10/2018)**

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 and CVE-2018-3640).
2. Changed BIOS revision to 3.2.