

BIOS Release Notes Form

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Product Name	X10SLL
Release Version	3.4
Release Date	3/8/2021
Build Date	3/8/2021
Previous Version	3.3
Update Category	Critical
Dependencies	None
Important Notes	None
Enhancements	N/A
New features	N/A
Fixes	1. Updated BIOS version to 3.4. 2. Fixed failure of chipsec items BLE, BWP, and TCO SMI lock.

Release Notes from Previous Release(s)

3.3 (3/28/2020)

1. Updated the SINIT ACM to 20190708 for Denlow Server IPU 2019.2 for INTEL-SA-00240 to address CVE-2019-0151 (7.5, High) security issue.
2. Updated SPS firmware to SPS_EN_03.00.07.060.0 for Denlow Server IPU 2019.2.
3. Updated Broadwell-E3/Broadwell-H stepping CPU microcode M2240671_00000022 (E-0 G-0 Stepping) and Haswell/Haswell-E3 stepping CPU microcode M32306C3_00000028 (C-0 Stepping) for Denlow Server IPU 2020.1.
4. Changed BIOS revision to 3.3.
5. Adjusted IPMI KCS command delay time to prevent failure to respond to get PSU info command when AC power cycle IPMI initial time is too long.
6. Enabled getting BIOS CFG file by SUM Inband.

3.2 (5/14/2018)

1. Updated BIOS revision to 3.2.
2. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 and CVE-2018-3640).
3. Added Intel SPI vulnerability patch.
4. Fixed failure of SUM TC320.