

# BIOS Release Notes Form

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<b>Product Name</b>	<b>X12SPO-(NT)F</b>
<b>Release Version</b>	<b>1.1</b>
<b>Release Date</b>	<b>4/9/2021</b>
<b>Build Date</b>	<b>4/9/2021</b>
<b>Previous Version</b>	<b>1.0a</b>
<b>Update Category</b>	<b>Critical</b>
<b>Dependencies</b>	<b>None</b>
<b>Important Notes</b>	<b>None</b>
<b>Enhancements</b>	<ol style="list-style-type: none"><li>1. Updated RC 20.P95 for PV RC update.</li><li>2. Updated SPS 4.4.4.53.</li><li>3. Updated Intel-Generic-Microcode-20210226a_NDA and Intel AE.</li><li>4. Corrected BIOS version that shows in IPMI after ROT recovery of previous version of BIOS.</li><li>5. Added support for VROC OOB.</li><li>6. Removed iSCSI option from LAN OPROM item.</li><li>7. Set PCH PCIe ASPM to disabled if CPU PCIe global ASPM is disabled.</li><li>8. Changed "SMCI PMem Formset" to "SMCI PMem Configuration".</li></ol>
<b>New features</b>	<b>N/A</b>

<b>Fixes</b>	<ol style="list-style-type: none"><li data-bbox="488 149 1235 184"><b>1. Corrected memory device number in SMBIOS type 16.</b></li><li data-bbox="488 207 1008 243"><b>2. Fixed problem with a CPU exception.</b></li><li data-bbox="488 266 1382 352"><b>3. Fixed problem of T-states always showing 15 levels even when T-state is disabled.</b></li></ol>
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## **Release Notes from Previous Release(s)**

### **1.0a (3/5/2021)**

1. Updated VBIOS and VGA EFI driver to 1.11.03.
2. Updated 5.22\_WhitleyCrb\_OACMS\_ICX\_058\_BETA for PC2 RC update.
3. Updated BIOS ACM 1.0.9 and SINIT ACM 1.0.9.
4. Updated SPS 4.4.51.
5. Updated Firmtool 1.30.21 to 1.30.22.
6. Updated SATA/sSATA EFI driver to VROC PreOS v7.5.0.1152.
7. Added PROMPT\_F1\_ON\_PWD\_TRYOUT.
8. Added IPMI OEM command 0x30 0x68 0xE0 to enhance SUM OOB flow.
9. Kept value of setup string of Manufacturer and Product according to priority "modification of AmiBcp, FUR1, and then SMBIOS Table".
10. Fixed inability of the system to boot into PXE with DVD installed.
11. Enhanced SMCI HDD Security feature.
12. Added the new type "text" of the HII data for the SMC setup modify function.
13. Set GPP\_C20 to GPIO default output high to prevent PCH throttle.
14. Added support for recovering boot status after flashing ROM through BMC/CPLD.
15. Added code to stop AFU support.
16. Added HCC Mx stepping CPU check for CPU stepping display.
17. Set default Boot Guard profile to 5.
18. Extended memory DIMM serial number information (Samsung, Micron, Hynix).
19. Enhanced SMC DCPMM feature.
20. Automatically disabled and hid ADDDC with x8 width DIMM.
21. Provided Redfish BootOptions with complete DevicePath information.
22. Removed 4G limit of Intel LAN memory if boot mode is not legacy.
23. Updated OOB Module to 1.01.22.
24. Set relation setup to restore setting after "Factory Mode" is disabled.
25. Exported AVX P1 (Config TDP) item.
26. Automatically disabled and grayed out ADDDC, UMA-Base Clustering, and mirror mode and enabled NUMA when SGX is enabled.
27. Set all OPROM control items to Legacy when boot mode is set to Dual.
28. Set PROCHOT Modes to Input-Only.
29. Fine tuned BMC LAN USB error handle and fixed asset problem when enabling BIOS debug mode and rebooting BMC under UEFI shell.
30. Fixed failure of the HDD security menu to show when more than 6 HDDs are connected on system.
31. Fixed failure of the BIOS binary to follow the unique format on the PC with Python 3 installed.
32. Fixed failure of SUT to prompt "Enter User password" screen after setting a password when plugging in SED device.
33. Corrected display of IPv4 address source status after updating BIOS.
34. Corrected display of IPv6 status after updating BIOS.
35. Modified IPv6 behavior and removed error message when IPv6 router IP is :::::.
36. Fixed problem of "Configuration Address Source" always showing "DHCP" on IPMI IPv6 page.
37. Fixed problem of BIOS initialization showing IPV6 address when IPV6 is disabled in the IPMI GUI.
38. Prevented failure of Redfish check items using items with same SGX\_PROMPT Name and different offset.
39. Fixed malfunction of AFU /n.
40. Filtered Dynamic HDD Security pages to patch failure of SUM ChangeBiosCfg.
41. Set AFU to stop if there are no parameters.
42. Fixed problem of module recovering boot status again when CMOS is clear.
43. Corrected location of RT UECC log.

44. Fixed failure of RT UECC to be mapped out.
45. Adjusted InBand RomHole size to prevent insufficient InBand Update Bios Cfg buffer size.
46. Corrected display of UEFI OS boot option name in BIOS setup.
47. Fixed failure of Secure Boot Append/Update Keys.
48. Fixed inability to upload all OOB files on the first BMC boot.
49. Corrected display of IPv6 when IPv6 status is not active.
50. Fixed mismatch of memory device in IPMI and in BIOS setup when some memory DIMMs are mapped out.
51. Corrected LAN MAC in IPMI web.