

BIOS Release Notes Form

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Product Name	X10DRH-C(T)/I(T)
Release Version	3.4 SPS: 3.1.3.72
Build Date	6/11/2021
Previous Version	3.3
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Changed BIOS revision to 3.4. 2. Updated Intel microcodes 0x46 for Haswell-EP and 0x3E for Broadwell-EP. 3. Updated for IPU 2021.1.
New features	N/A
Fixes	N/A

Release Notes from Previous Release(s)

3.3 (2/18/2021)

1. Changed BIOS revision to 3.3.
2. Updated Intel Microcode: 0x44 for Haswell-EP.
3. Updated Grantley Refresh RC IPU2020.2 to address Intel-TA-00358: CVE-2020-0591 (6.7, Medium) and CVE-2020-0592 (3, Low), and Intel-TA-00390: CVE-2020-8738 (7.5, High), CVE-2020-8740 (6.7, Medium), and CVE-2020-8764 (8.2, High).

3.2 (11/21/2019)

1. Updated BIOS version to 3.2.
2. Updated SINIT ACM 3.1.4 PW for Intel-SA00240 (CVE-2019-0151 CVSS 3.1: 7.5 High).
3. Updated Broadwell-EP B0/M0/R0 CPU microcode MEF406F1_0B000038 for Intel-SA00219 (CVE-2019-0117) and Intel-SA00220 (CVE-2019-0123).

3.1c (6/25/2019)

1. Changed BIOS version to 3.1c.
2. Updated Intel Server Platform Services 3.1.3.72 for Grantley Refresh Platforms.
3. Updated Haswell-EP/Broadwell-EP CPU microcode from INTEL-SA-00233.
4. Updated VBIOS from 1.03.01 to 1.09.00.
5. Improved tCCD_L value.

3.1a (4/9/2019)

1. Changed BIOS version to 3.1a.
2. Changed memory data width from 72 to 64 in SMBIOS type 17.
3. Updated VBIOS from 0.96.0 to 1.03.01.
4. Fixed issue of system hanging at 0xA9 when populated by 900P NVMe.

3.1 (6/18/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
2. Changed BIOS version to 3.1.
3. Rolled back Intel RC code to v4.2.0.
4. Set Descriptor Region of BIOS Region Write Access to "No".

3.0a (2/5/2018)

1. Updated CPU microcode to address CVE-2017-5715 security patch issue.
2. Updated RC to 4.3.0.
3. Updated microcode to 2A.
4. Changed BIOS revision to 3.0a.

Product Manager

Date