

BIOS Release Notes Form

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Product Name	X12DPU-6
Release Version	1.1 SPS: 4.4.4.53
Build Date	4/21/2021
Previous Version	1.0b
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Updated RC 20.P95 for PV RC update. 2. Changed the BIOS version to 1.1. 3. Updated for CPLD Signature table 101. 4. Updated Intel-Generic-Microcode-20210402_NDA. 5. Updated Dx/Mx microcode from Intel AE (synced with BKCWW14).
New features	N/A
Fixes	N/A

Release Notes from Previous Release(s)

1.0b (3/26/2021)

1. Updated RC 20.P93 for PV RC update.
2. Updated BIOS ACM 1.0.9 and SINIT ACM 1.0.9.
3. Updated SPS 4.4.53.
4. Updated x/Mx microcode from Intel AE.
5. Automatically disabled and hid ADDDC with x8 width DIMM.
6. Extended memory DIMM serial number information (Samsung, Micron, Hynix).
7. Enhanced SMC DCPMM feature.
8. Automatically disabled and grayed out ADDDC, UMA-Base Clustering, and mirror mode and enabled NUMA when SGX is enabled.
9. Set relation setup to restore setting after "Factory Mode" is disabled.
10. Removed 4G limit of Intel LAN memory if boot mode is not legacy.
11. Set all OPROM control items to Legacy when boot mode is set to Dual.
12. Updated SATA/sSATA EFI driver to VROC PreOS v7.5.0.1152.
13. Updated BPS firmware to 2.2.0.1553.
14. Changed the BIOS version to 1.0b.
15. Fixed problem of T-states always showing 15 levels even when T-state is disabled.
16. Filtered Dynamic HDD Security pages to patch failure of SUM ChangeBiosCfg.
17. Fixed inability to upload all OOB files on the first BMC boot.
18. Corrected display of UEFI OS boot option name in BIOS setup.
19. Corrected riser card SMBIOS creation.
20. Fixed problem with a CPU exception.
21. Fixed failure to hide the SmcSecureErase setup page when no HDD devices are plugged in.
22. Corrected display of NVMe LED on BPN-NVMe4-216N-S24 under VMD mode.

1.0a (1/28/2021)

1. Changed the BIOS version to 1.0a.
2. Updated BKC WW51 components.
3. Fixed inability of system to boot into PXE with DVD installed.
4. Fine tuned re-timer add-on card workaround.
5. Fixed problem of system exception occurring at 0x92 or 0xA2 after changing boot order and rebooting.
6. Corrected CPU1 memory type 20 handler point to CPU2 Memory Array Mapped Address structure.
7. Fixed failure of the HDD security menu to display when connecting more than 6 HDDs on system.

1.0 (10/22/2020)

1. First release