

BIOS Release Notes Form

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Product Name	X10DRG-H(T)
Release Version	3.4 SPS: 3.1.3.72
Build Date	8/30/2021
Previous Version	3.3
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Changed BIOS revision to 3.4. 2. Updated Intel microcodes 0x46 for Haswell-EP and 0x3E for Broadwell-EP. 3. Updated for IPU 2021.1.
New features	N/A
Fixes	N/A

Release Notes from Previous Release(s)

3.3 (2/22/2021)

1. Changed BIOS revision to 3.3.
2. Updated Intel Microcode: 0x44 for Haswell-EP.
3. Updated Grantley Refresh RC IPU2020.2 to address Intel-TA-00358: CVE-2020-0591 (6.7, Medium) and CVE-2020-0592 (3, Low), and Intel-TA-00390: CVE-2020-8738 (7.5, High), CVE-2020-8740 (6.7, Medium), and CVE-2020-8764 (8.2, High).

3.2 (12/17/2019)

1. Updated BIOS version to 3.2 for formal release.
2. Updated SINIT ACM 3.1.4 PW for Intel-SA00240 (CVE-2019-0151 CVSS 3.1: 7.5 High).
3. Updated Broadwell-EP B0/M0/R0 CPU microcode MEF406F1_0B000038 for Intel-SA00219 (CVE-2019-0117) and Intel-SA00220 (CVE-2019-0123).
4. Updated initial value of memory tccd.

3.1d (9/18/2019)

1. Updated BIOS version to 3.1d for formal release.
2. Fixed malfunction of IPXTEK DSP card in bottom slot of RSC-R1UG-2E8AR+II Riser on slot4.
3. Fixed failure of X10DRG-H to support new UUID feature.

3.1c (5/23/2019)

1. Updated BIOS version to 3.1c for formal release.
2. Updated RSTe and VROC files using VROC_PreOS_6.1.0.1017 package.
3. Updated VBIOS to v1.09.00.
4. Updated Haswell microcode to 43 and Broadwell microcode to 36.
5. Updated SPS version 05:3.1.3.72.

3.1a (4/18/2019)

1. Updated BIOS version to 3.1a for formal release.
2. Updated RSTe and VROC files using VROC_PreOS_6.1.0.1017 package.
3. Updated VBIOS to v1.03.01.
4. Updated SMBIOS Type 4 to match the CPU Sensor name with IPMI.

3.1 (6/15/2018)

1. Implemented enhancement to address 'Spectre' variant 2 (CVE-2018-3639 & CVE-2018-3640) security patch issue.
2. Updated BIOS revision to 3.1 for formal release.
3. Set Descriptor Region (Bit 0) of BIOS Region Write Access to "No".
4. Fixed failure of Test Case #320.

3.0b (4/23/2018)

1. Updated BIOS revision to 3.0b for formal release.
2. Fixed failure of add-on card (AOC-S3108L-H8iR) to display OPROM message on X9DRG-O-PCIE PCIe extended board.

3.0a (2/20/2018)

1. *Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.*
2. *Updated BIOS version to 3.0a for formal release.*
3. *Updated for Intel security patch.*
4. *Fixed problem of SMCIPMITool reporting wrong BIOS revision.*
5. *Fixed problem of dmidecode -t 34 returning invalid length of data.*