

BIOS Release Notes Form

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Product Name	X10DSC-TP4S
Release Version	3.4 SPS: 3.1.3.72
Build Date	10/1/2021
Previous Version	3.3
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Changed BIOS revision to 3.4. 2. Updated Intel microcodes 0x46 for Haswell-EP and 0x3E for Broadwell-EP. 3. Updated for IPU 2021.1.
New features	N/A
Fixes	N/A

Release Notes from Previous Release(s)

3.3 (3/17/2021)

1. Changed BIOS revision to 3.3.
2. Updated Intel Microcode: 0x44 for Haswell-EP.
3. Updated Grantley Refresh RC IPU2020.2 to address Intel-TA-00358: CVE-2020-0591 (6.7, Medium) and CVE-2020-0592 (3, Low), and Intel-TA-00390: CVE-2020-8738 (7.5, High), CVE-2020-8740 (6.7, Medium), and CVE-2020-8764 (8.2, High).

3.2 (11/21/2019)

1. Changed BIOS revision to 3.2.
2. Enabled RFC4122 UUID support.
3. Improved tCCD_L value.
4. Updated SINIT ACM 3.1.4 PW for Intel-SA00240 (CVE-2019-0151 CVSS 3.1: 7.5 High).
5. Updated Broadwell-EP B0/M0/R0 CPU microcode MEF406F1_0B000038 for Intel-SA00219 (CVE-2019-0117) and Intel-SA00220 (CVE-2019-0123).

3.1c (6/14/2019)

1. Changed BIOS revision to 3.1c.
2. Updated Intel Server Platform Services 3.1.3.72 for Grantley Refresh Platforms.
3. Updated Haswell-EP/Broadwell-EP CPU microcode from INTEL-SA-00233.
4. Updated VBIOS to 1.09.00.

3.1 (6/11/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
2. Changed BIOS revision to 3.1.
3. Set Descriptor Region (Bit 0) of BIOS Region Write Access to "No".