

BIOS Release Notes Form

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Product Name	H12DGO-6
Release Version	2.1a
Release Date	10/22/2021
Build Date	10/22/2021
Previous Version	2.1
Update Category	Recommended
Dependencies	IPMI firmware 1.00.40
Important Notes	<p>ECO #25938</p> <p>BIOS image: BIOS_H12DGO6-1C1E_20211022_2.1a_STDsp.bin</p> <p>BIOS Update package: BIOS_H12DGO6-1C1E_20211022_2.1a_STDsp.zip</p> <p>Notice:</p> <ol style="list-style-type: none">1) BIOS R2.x supports 7002 and 7003 processor.2) Flash Utility in package supports BIOS R1.x update to R2.x only, cannot roll back.3) R2.x BIOS default boot mode has been changed to EFI. If your OS is legacy, please press Del during POST to enter into BIOS setting page to change boot mode after upgrade to R2.x.
Enhancements	<ol style="list-style-type: none">1. Changed BIOS revision to 2.1a.2. Updated AGESA code.3. Revised workaround for Nvidia-smi GPU lost issue.
New features	N/A
Fixes	<ol style="list-style-type: none">1. Changed PCIE S0P2&S1P2 bifurcation to x16.2. Changed PCI/PCIE Slot2 OPR0M setup string from x16 to x8.3. Added workaround on PCI/PCIE OPR0M execution.4. Fixed issue of build date is correct.5. Fixed issue of BIOS hangs on POST code 92 with full system configuration.

Release Notes from Previous Release(s)

2.1 (07/27/2021)

1. Changed BIOS revision to 2.1.
2. Updated AGESA code for 7002 processor.
3. Updated SMC modules.
4. Set UEFI shell boot order ahead of AIOM PXE.
5. Changed bifurcation of riser slot1 from 2x8 to 1x16.
6. Fixed failure of 4TB memory to boot.
7. Fixed missing Network Configuration in SUM BIOS configuration file.
8. Updated SMBIOS Type 2 board name to H12DGO-6.
9. Fixed failure to eject NVMe4 and 5 under Windows.
10. Fixed support for SMBIOS Type 9 AIOM card.
11. Fixed insufficient resources of some GPUs under Linux.

2.0c (06/03/2021)

12. Changed BIOS revision to 2.0c.
13. Modified UEFI shell boot order ahead of AIOM PXE with Milan CPU.
14. Fixed PCIE Gen drop issue on PLX switch downstream ports.

2.0b (05/24/2021)

1. Changed BIOS revision to 2.0b.
2. Added CPU and NB menus to SUM setup template.
3. Updated Rome/Milan AGESA code to 1.0.0.A/1.0.0.1.
4. Added delay in reading PSU FRU through BMC.
5. Changed system onboard slot number to 11.
6. Added a patch to solve NVidia-smi GPU loss issue with Intel NIC installed on switch board.
7. Added a workaround to assign 32bit resource for AIOM NICs to support legacy PXE.
8. Applied BIOS setup settings pushed by SUM without entering BIOS setup.
9. Changed PCIe port PMEM size.
10. Added support for unique filename.
11. Improved UOL data transfer between BIOS to BMC.
12. Corrected help strings in PCIe/PCI/PNP configuration page.

2.0a (03/25/2021)

1. Changed BIOS revision to 2.0a.
2. Removed unused SATA ports in BIOS setup.
3. Updated SMBIOS TYPE 9 and 40 table.
4. Treated AIOM as onboard NIC.

2.0 (03/04/2021)

1. Changed BIOS revision to 2.0.
2. Added support for both AMD EPYC 7002 and next generation processors.
3. Added support for Nvidia A100 80G GPU.
4. Improved SMBIOS Type 39 PSU reading procedure.
5. Added xGMI setup options.
6. Fixed problem of NVMe hot-swap resetting the system.
7. Fixed inability to enable ECC reporting by Cburn.

1.0a (12/10/2020)

1. Changed BIOS revision to 1.0a.
2. Provided all of AIOM NIC MAC addresses to BMC.
3. Fixed issue of CPU2 dropping.

1.0 (11/13/2020)

First release.