

## BIOS Release Notes Form

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<b>Product Name</b>	<b>H12DSG-Q-CPU6</b>
<b>Release Version</b>	<b>2.3</b>
<b>Release Date</b>	<b>10/18/2021</b>
<b>Build Date</b>	<b>10/18/2021</b>
<b>Previous Version</b>	<b>2.1</b>
<b>Update Category</b>	<b>Recommended</b>
<b>Dependencies</b>	<b>IPMI firmware 1.00.40</b>
<b>Important Notes</b>	<b>ECO #25937</b> <b>BIOS image: BIOS_H12DSGQ-1C11_20211018_2.3_STDsp.bin</b> <b>BIOS Update package: BIOS_H12DSGQ-1C11_20211018_2.3_STDsp.zip</b> <b>Notice:</b> <b>1) BIOS R2.x supports 7002 and 7003 processor.</b> <b>2) Flash Utility in package supports BIOS R1.x update to R2.x only, cannot roll back.</b> <b>3) R2.x BIOS default boot mode has been changed to EFI. If your OS is legacy, please press Del during POST to enter into BIOS setting page to change boot mode after upgrade to R2.x from R1.x.</b>
<b>Enhancements</b>	<b>1. Changed BIOS revision to 2.3.</b> <b>2. Supported secure boot redfish interface.</b> <b>3. Supported SUM TLS certificate upload.</b> <b>4. Supported SUM SetHttpBoot.</b> <b>5. Updated AGESA MilanPI to 1.0.0.6.</b> <b>6. Supported Milan-X CPU.</b> <b>7. Updated CPU microcode.</b> <b>8. Enhanced OEMFID information.</b> <b>9. Updated AGESA RomePI to 1.0.0.C.</b> <b>10. Updated microcode for Milan erratum 1381.</b>
<b>New features</b>	<b>N/A</b>

<b>Fixes</b>	<ol style="list-style-type: none"><li data-bbox="493 176 1110 210">1. Fixed issue of TPM was disabled by default.</li><li data-bbox="493 216 1317 249">2. Fixed issue of system would boot loop if a TPM is disabled.</li><li data-bbox="493 256 1419 289">3. Fixed issue of DMI type 39 info was not filled in after cold booting.</li></ol>
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## **Release Notes from Previous Release(s)**

### **2.1 (2021/07/07)**

1. Added more OOB settings.
2. Added support for Redfish boot option management API.
3. Fixed issue of the system UUID generating incorrectly if multiple LAN cards are installed.

### **2.0c (2021/06/14)**

1. Added "Correctable Error Threshold" and "PCI AER" setting.
2. Added support for SUM 2.6 SetHttpBoot.
3. Removed UEFI NVMe boot option group.

### **2.0b (2021/05/20)**

1. Added "Memory Corrected Error Enabling" setting.
2. Corrected boot order.
3. Corrected setting of serial over LAN module.

### **2.0a (2021/05/06)**

1. Added timestamp data to SMBIOS type1 data.
2. Fixed in-band update.
3. Fixed type9 slot names.
4. Reported number of NVMe to CPLD.
5. Fixed version number reading in \$FID.
6. Removed AOM info from type9.
7. Updated Product Name in DMI table type 2.
8. Fixed USB device detection for JUSB1.
9. Rebuilt BIOS for Rome to have the same build date as Milan.

### **2.0 (2021/04/07)**

1. Changed BIOS revision to 2.0.
2. Updated AGESA MilanPI to 1.0.0.1.
3. Updated AGESA RomePI to 1.0.0.A.
4. Adjusted SMBIOS type9/40 information.
5. Adjusted hotplug function for NVMe/SATA backplane.
6. Added bifurcation setting for slots 1, 3, 4, and 5.
7. Fixed SMBIOS Type 39 PSU reading procedure.
8. Fixed issue of BIOS hanging at postcode 94 when plugging in AOC-STG-I2T while GPU board is connected to system.
9. Fixed issue with BIOS ECO TC221.
10. Fixed inability to change CPU settings.
11. Fixed issue of SMSTC reporting "filesize out of range" in testing.
12. Fixed issue of BIOS boot order showing duplicate entries and no additional information.
13. Fixed issue of no sensors showing with dual image BIOS.
14. Fixed failure of AOM to appear in network devices section of WebGUI.
15. Fixed duplicate setting on PCIe/PCI/PNP configuration menu.
16. Fixed failure of MAC address on board LAN1/2 to appear on WebGUI.

### **1.0a (2020/12/06)**

1. Changed BIOS revision to 1.0a.
2. Enabled LPC TPM.
3. Fixed preservation of DMI when updating BIOS.
4. Fixed SUM test case 220.

**1.0 (2020/10/28)**

*First release.*