

BIOS Release Notes Form

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Product Name	X10DDW-iN
Release Version	3.4 SPS: 3.1.3.72
Build Date	10/18/2021
Previous Version	3.3
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Changed BIOS revision to 3.4. 2. Updated CPU µcode Haswell-EP C0/C1/M1 rev. 46 and Broadwell-EP B0/M0/R0 rev. 3E. 3. Added timestamp in UUID.
New features	N/A
Fixes	N/A

Release Notes from Previous Release(s)

3.3 (4/15/2021)

1. Changed BIOS revision to 3.3.
2. Updated CPU μ code Haswell-EP C0/C1/M1 rev. 44 and Broadwell-EP B0/M0/R0 rev. 38.
3. Updated Intel MRC code.
4. Fixed UUID error using SUM.

3.2 (12/16/2019)

1. Changed BIOS revision to 3.2.
2. Updated CPU μ code Haswell-EP C0/C1/M1 rev. 43 and Broadwell-EP B0/M0/R0 rev. 38.
3. Updated SINIT ACM 3.1.4 PW for Intel-SA00240 (CVE-2019-0151 CVSS 3.1: 7.5 High).
4. Added support for RFC4122 UUID format feature.
5. Updated initial value of memory tccd.

3.1a (5/17/2019)

1. Updated CPU microcodes to Haswell-EP C0/C1/M1 rev. 43 and Broadwell-EP B0/M0/R0 rev. 36.
2. Updated Intel firmware to E5_03.01.03.072.0_WBG_REL SPS.
3. Updated Intel VROC (RSTe) SATA/sATA driver to version 6.1.0.1017.

3.1 (3/22/2019)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
2. Updated ASTVBIOS to V1.09.00 to address CVE-2019-6260/Pantsdown.
3. Added energy performance configuration to Advanced Power Management.

3.0a (3/1/2018)

1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.
2. Updated RC 4.3.0.
3. Added Intel SPI vulnerability patch.
4. Implemented tCCD_L.
5. Enabled setup options for slot1 of RSC-R2UW2E8E16 to be excluded from "Above 4G Decoding".
6. Filled SMBIOS type 11 default OEM string.
7. Fixed missing console redirection output when "Resolution 100x31" is disabled.