BIOS Release Notes Form

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| Product Name | X10DAC/X10DAi |
|------------------|---|
| Release Version | 3.4 SPS: 3.1.3.72 |
| Build Date | 10/4/2021 |
| Previous Version | 3.3 |
| Update Category | Recommended |
| Dependencies | None |
| Important Notes | None |
| Enhancements | Changed BIOS revision to 3.4. Updated CPU µcodes Haswell-EP CO/C1/M1 rev. 46 and Broadwell- EP B0/M0/R0 rev. 3E. |
| New features | N/A |
| Fixes | N/A |

Release Notes from Previous Release(s)

3.3 (3/23/2021)

- 1. Changed BIOS revision to 3.3.
- 2. Updated CPU μcode Haswell-EP CO/C1/M1 rev. 44.
- 3. Displayed LRDIMM on BIOS setup when LRDIMM is installed.
- 4. Updated MRC code revision to 4.4.0 for IPU2020.2.

3.2 (12/16/2019)

1. Changed the BIOS revision to 3.2

2. Updated the CPU microcode revisions for Haswell-EP CO/C1/M1 revision 43 and Broadwell-EP BO/M0/R0 revision 38.

3. Updated SINIT ACM to 3.1.4 for Intel -SA00240 (CVE-2019-0151 CVSS 3.1: 7.5 High).

4. Updated the initial memory value of tccd.

3.1 (6/27/2018)

1. Updated processor μ codes Haswell-EP C0/C1/M0/M1/R2 rev. 43 and Broadwell-EP B0/M0/R0 rev. 36.

2. Updated SPS firmware version to 3.1.3.72.

3. Updated Intel VROC version to 6.1.0.1017.

3.1 (6/27/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.

3.0a (2/5/2018)

1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security issue.

2. Updated RC to 4.3.0.

3. Updated microcodes to Haswell-EP CO/C1/M0/M1/R2 rev. 3C and Broadwell-EP B0/M0/R0 rev. 2A.

4. Added tCCD_L setup option.

5. Set security device support item to be hidden when this option is disabled.

6. Fixed problem of keystroke and keeping drive plugged in required for USB flash drive when HDD bit lock is enabled.