

## BIOS Release Notes Form

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<b>Product Name</b>	<b>X12DHM-6</b>
<b>Release Version</b>	<b>1.2 SPS: 4.4.4.62</b>
<b>Build Date</b>	<b>2/17/2022</b>
<b>Previous Version</b>	<b>1.1a</b>
<b>Update Category</b>	<b>Recommended</b>
<b>Dependencies</b>	<b>None</b>
<b>Important Notes</b>	<b>None</b>
<b>Enhancements</b>	<ol style="list-style-type: none"><li>1. Updated AMI 5.22_WhitleyCrb_0ACMS_ICX_070 RC27P56 for BKC 2021_WW52 (PLR1 HF)2.</li><li>2. Changed string "VMX" to "Intel Virtualization Technology".</li><li>3. Added flag [Preserve BIOS Boot Options Configuration] controlled by BMC/SUM.</li><li>4. Changed BIOS revision to 1.2.</li><li>5. Updated PLR1 HF RC27P56, microcode M87606A6_0D000332d</li></ol>
<b>New features</b>	<b>N/A</b>
<b>Fixes</b>	<ol style="list-style-type: none"><li>1. Removed 1G option from MMCFG base, to avoid system hang.</li><li>2. Fixed the SMBIOS event log ERROR CODE which is not displaying correctly under BIOS menu issue (EFI error type).</li><li>3. Fixed SpeedStep (P-States) setting change, when its default setting is set to Disable, and loaded BIOS defaults in Setup.</li><li>4. Fixed COM port resource where it can't be changed, and the item's intended function does not work.</li></ol>

## **Release Notes from Previous Release(s)**

### **1.1a (10/12/2021)**

#### *Enhancements*

1. Updated 5.22\_WhitleyCrb\_OACMS\_ICX\_067 Intel BKCWW32 2021 PV MR5.
2. Updated Dx/Mx microcode from Intel-Generic-Microcode-20210804\_NDA.
3. Updated SPS 4.4.4.58 PV MR5.
4. Changed BIOS revision to 1.1a.
5. Changed "Hard Drive Security Frozen" default setting to disabled.
6. Disabled support for EFI iSCSI.
7. Added support for SUM upload/delete HTTPS TLS certificate.
8. Updated BIOS/SINIT ACM 1.0.D.
9. Set CPU1 AIOM x16 slot OPROM control item and IIO port item to show whether AIOM1 slot has device or not.
10. Modified NVMe slot IIO port string.

#### *Fixes*

1. Fixed inability to detect BPN-SAS3-LA26A-N12 v2.00.
2. Fixed malfunction of OnBoardLanCheck workaround.
3. Fixed inability to change COM port resource and failure of item's behavior.
4. Added support for i350 UEFI PXE boot of AIOM2 slot for SYS-220HE-FTNR and SYS-620H-TN12R systems.
5. Corrected NVMe OPROM control item shown in BIOS menu with 9/16 1.1A ECO BIOS.
6. Fixed issue with POST looping at 94h with AIOM2 populated on AOM-AIOM-2X8.

### **1.1 (4/30/2021)**

1. Updated RC 20.P96 for PV RC update.
2. Changed BIOS revision to 1.1.