BIOS Release Notes Form

Product Name	**H12SSW-iNL/NTL (All)**
Release Version	**2.4**
Release Date	**04/14/2022**
BIOS Date	**04/14/2022**
Previous Version	**2.0**
Update Category	**Critical**
Dependencies	**N/A**
Important Notes	**ECO #26560** **BIOS image: BIOS_H12SSWL-1B98_20220414_2.4_STDsp.bin** **Please update BIOS with attached Flash Utility in package.**
Enhancements	1. Add Redfish/SUM Secure Boot feature, update OOB for secure boot, and reserve Key. 2. Support SUM upload/delete HTTPS TLS certificate. (Default Enabled by TOKEN "Sum_UploadTlsKey_SUPPORT"); Refer Whitley SVN 3116/3114 to update SmcHttpBoot module. 3. Per AMD's suggestion, set Relaxed Ordering default to Enabled. 4. Update AGESA RomePI to 1.0.0.D. 5. Exposed Setup item "Enhanced Preferred IO Mode", By AMD's suggestion. 6. Update AGESA MilanPI to 1.0.0.8. 7. Update Milan B0/B1/B2 stepping CPU microcode and Milan-X B2 stepping CPU microcode to patch Errata 1304, 1310, 1311, 1313, 1329, 1330, 1336, 1343, 1350, 1352, 1361, 1378, 1379, 1381, 1386, 1407, 1415. 8. Patch BMC Redfish Host Interface was named as ethX when CDN was the disabled case under Linux OS. 9. Disable Legacy/EFI iSCSI support for security concerns. 10. Exposed Setup items "BankGroupSwapAlt", "SEV-SNP Support", "Enhanced Preferred IO Mode", and "Root Complex 0x00~0xE0 LCLK Frequency" by AMD's suggestion. 11. Disable "Wait For "F1" If Error". 12. Exposed setup item "ASPM Support" in PCIe/PCI/PnP Configuration Page. 13. Add the "Factory Mode" function for the Production test. 14. Update Rome B0 stepping CPU microcode 0x08301055 to patch Errata 1161, 1176, 1179, 1183, 1214, 1268, 1386, 1417. 15. Exposed setup items "SNP Memory (RMP Table) Coverage" and "Amount of Memory to Cover" on CPU Configuration Page
New features	**N/A**
Fixes	Fix SUM cannot modify AMD CBS settings when BIOS didn't update AMD CBS tokens correctly.

Release Notes from Previous Release(s)

Revision 2.0 (02/22/2021)
1. [Rome/Milan BIOS] Change BIOS revision to 2.0.
2. [Milan BIOS] Update AGESA MilanPI to 1.0.0.1.
3. [Milan BIOS] Update A011 GN B1 microcode 0A001119.
4. [Rome BIOS] Update AGESA RomePI to 1.0.0.A.
5. [Rome/Milan BIOS] Fix IPV6 disable in the IPMI GUI but BIOS initialize will appear IPV6 address.

Revision 1.0 (2019/07/19)
First release.