BIOS Release Notes Form

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Product Name	X10DRU-I+
Release Version	3.5 SPS: 3.1.3.131
Build Date	04/27/2022
Previous Version	3.4
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancement	 Refer to AMI 5.011_MayanCity_0ACFL048 to Update RC 278R48 for IPU 2022.1. Updated Haswell-EP C0,1/M0,1/R2 CPU microcode M6F306F2_00000049. Updated BIOS ACM to v3.1.5. Updated Intel Server Platform Services 3.1.3.131 for Grantley Refresh Platforms. AddedUUID Enhancement for system without onboard LAN.
New features	N/A
Fixes	1. Checked NVMe_SUPPORT for NATIVE_NVME_ENABLE item.

3.4 (5/21/2021)

- 1. Exposed the "IPMI STATUS" string in the IPMI setup page.
- 2. Updated Grantley Refresh RC IPU2021.1 (RC 278R17) to address INTEL-TA-00463: CVE-2020-12357 (7.5 High) and CVE-2020-12360 (5.6 Medium).
- 3. Updated Haswell-EP/Broadwell-EP CPU microcode from Intel-Restricted-2021-1-IPU-20210322_20211IPU Release.

3.3 (10/24/2020)

- 1. Updated Haswell-EP/Broadwell-EP CPU microcode from 20201020 NDA Release for IPU2020.2.
- 2. Updated Grantley Refresh RC IPU2020.2 to address Intel-TA-00358: CVE-2020-0591 (6.7, Medium) and CVE-2020-0592 (3, Low), and Intel-TA-00390: CVE-2020-8738 (7.5, High), CVE-2020-8740 (6.7, Medium), and CVE-2020-8764 (8.2, High).
- 3. Updated AMI SA50072 and SA50077 security update for AHCI and WHEA modules.
- 4. Prioritized Disk SMART error event before display of disk SMART error.

3.2a (11/19/2019)

- 1. Updated SINIT ACM 3.1.4 PW for Intel-SA00240 (CVE-2019-0151 CVSS 3.1: 7.5 High).
- 2. Updated SATA/sSATA RAID OPROM/EFI driver to VROC PreOS v6.2.0.1034.
- 3. Updated Broadwell-EP B0/M0/R0 CPU microcode MEF406F1_0B000038 for Intel-SA00219 (CVE-2019-0117) and Intel-SA00220 (CVE-2019-0123).

3.2 (6/11/2019)

- 1. Changed BIOS revision to 3.2
- 2. Updated to allow SUM to change Memory Correctable Error Threshold setup item.

3.1c (5/10/2019)

- 1. Updated Intel Server Platform Services 3.1.3.72 for Grantley Refresh Platforms.
- 2. Updated SATA/sSATA RAID OPROM/EFI driver to VROC PreOS v6.1.0.1017.
- 3. Updated VBIOS and VGA EFI Driver to 1.09 to fix ASpeed CVE-2019-6260 security issue.
- 4. Updated Haswell-EP/Broadwell-EP CPU microcode from SRV_P_273 for INTEL-SA-00233.
- 5. Updated valid range of IPMI setup item VLAN ID to 1-4094.
- 6. Added support for RFC4122 UUID format feature to set RFC4122 encoding from build time produced by IPMICFG 1.29 tool or newer.
- 7. Forced a global reset if SPI descriptor is not write-protected after BIOS flash.
- 8. Implemented anti-rollback for FDT Read-Only.
- 9. Implemented multi-line IPMI page text and string.
- 10. Added support for setting IPv6 Static Router1 prefix length and value in BIOS setup menu feature.
- 11. Displayed the driver health support pages to support LSI (Broadcom 9440-8i) driver health status.
- 12. Fixed malfunction of Windows Consistent Device Naming (CDN).

- 13. Fixed problem of BIOS always retrying boot when Re-Try Boot is Disabled.
- 14. Fixed malfunction of METW if many error events are triggered within a very short time.

3.1 (6/8/2018)

- 1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
- 2. Changed BIOS version to 3.1.
- 3. Added support for IPMI IPV6.
- 4. Removed unsupported memory frequency options from setup menu.
- 5. Updated Broadwell-EP RC 4.4.0 release.
- 6. Added PCC function.
- 7. Fixed problem of Afu /O command clearing SMC SMBIOS region (\$SMC).
- 8. Fixed problem of SUM OOB GetSataInfo always showing "Configuration Type" as "AHCI" when setting "Configure SATA as" to "RAID" or "IDE".
- 9. Set Descriptor Region of BIOS Region Write Access to "No".

3.0a (2/5/2018)

- 1. Updated Broadwell-EP RC 4.3.0 PLR11 release.
- 2. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability.
- 3. Updated BIOS ACM 3.1.1 PW and SINIT ACM 3.1.1 PW.
- 4. Added SumBbsSupportFlag into DAT file.
- 5. Added Intel SPI vulnerability patch for Grantley.
- 6. Updated Intel Server Platform Services 3.1.3.50 for Grantley Refresh Platforms.
- 7. Updated CPU microcode to address CVE-2017-5715 security patch issue.
- 8. Added support for JEDEC NVDIMM.
- 9. Fixed problem of system resetting or hanging after Watch Dog function is enabled during BIOS update.
- 10. Fixed problem of MWAIT being disabled on BSP only.
- 11. Fixed inability to get correct Memory CECC DIMM location via SD5.
- 12. Fixed inability to log Patrol Scrub UCE.
- 13. Fixed problem of system hanging at POST 0xA9 when entering setup menu while Intel P4800X uEFI driver is loaded.
- 14. Fixed problem of microcode update not updating FIT table after using INT15 microcode update tool.
- 15. Fixed inability of SUM utility get "Setup Prompt Timeout" setup item.