

## BIOS Release Notes Form

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<b>Product Name</b>	<b>X12DPU-6</b>
<b>Release Version</b>	<b>1.4 SPS: 4.4.4.202</b>
<b>Build Date</b>	<b>7/12/2022</b>
<b>Previous Version</b>	<b>1.2</b>
<b>Update Category</b>	<b>Recommended</b>
<b>Dependencies</b>	<b>None</b>
<b>Important Notes</b>	<b>None</b>
<b>Enhancements</b>	<ol style="list-style-type: none"><li>1. Updated 5.22_WhitleyCrb_0ACMS_ICX_72 Intel BKC WW23 PLR3.</li><li>2. Updated VROC SATA/sSATA EFI driver to VROC PreOS v7.7.6.1004 to adress INTEL-TA-00692, CVE-2022-29919(7.8 High), CVE-2022-30338(6.7 Medium), CVE-2022-29508(6.3 Medium), CVE-2022-25976(5.5 Medium).</li><li>3. Updated VROC SATA/sSATA legacy driver to VROC PreOS v6.3.5.1003 Hot Fix to fix 10TB or higher volume drive issue.</li><li>4. Added "CSM Support" setup item into SMCISBForm page.</li><li>5. Fixed Dynamic TCG Security Pages to patch SUM ChangeBiosCfg failure problem. SUM cannot parse dynamic pages that only update form when enter bios setup menu.</li><li>6. Added support for IPMI PXE boot to all LAN port feature for both Legacy and UEFI PXE. When using the IPMI system boot option command to boot to Legacy PXE or UEFI PXE, the system should scan all LAN ports.</li></ol>

	<p>7. Fixed Linux OS showing incorrect CPU max freq. Customer reported the PLR1 BIOS showing the wrong CPU max speed while previous BIOS does not have such issue.</p> <p>8. [SmcOOB]: Updated SmcOOB to the version "_SMCOOBV1.01.25_" to fix the unexpected system-resetting when loading NVRAM defaults. Updated SmcOOB to the version "_SMCOOBV1.01.25_" to fix the unexpected system-resetting when loading the BIOS default.</p> <p>9. If chassis type of FRU0 is not 1(other) or 2(unknown), sync it to SMBIOS type 3. SMBIOS template rule update</p> <p>10. Disable Link Re-train in BIOS to avoid secondary Bus Reset following intel MOW 22WW27 and expose Link Re-train per port in BIOS.</p>
<p><b>New features</b></p>	<p>N/A</p>
<p><b>Fixes</b></p>	<p>1. Fixed SUM ChangeBiosCfg command which cannot update PchSetup variable related BIOS items. BIOS settings such as "Configure SATA should be able to be updated by SUM (Supermicro Update Manager).</p>

## **Release Notes from Previous Release(s)**

### **1.2 (2/15/2022)**

1. Changed BIOS revision to 1.2.
2. Updated PLR1 HF RC27P56, ucode M87606A6\_OD000332.
3. Changed string "VMX" to "Intel Virtualization Technology".
4. Added flag [Preserve BIOS Boot Options Configuration] controlled by BMC/SUM.
5. Updated AMI 5.22\_WhitleyCrb\_OACMS\_ICX\_070\_BETA RC27P52 for BKC 2021\_WW52 (PLR1).
5. Removed 1G option from MMCFG base to avoid system hang.
6. Fixed the SMBIOS event log ERROR CODE, which did not display correctly under BIOS menu (EFI error type).
7. Fixed COM port resource that can't be changed, item behavior failure.
8. Fixed the IIO menu of onboard P1\_NVMe1 slot, it does not show under BIOS menu.

### **1.1a (8/21/2021)**

1. Updated 5.22\_WhitleyCrb\_OACMS\_ICX\_067 Intel BKCWW32 2021 PV MR5.
2. Updated Dx/Mx microcode from Intel-Generic-Microcode-20210804\_NDA.
3. Updated SPS 4.4.4.58 PV MR5.
4. Changed BIOS revision to 1.1a.
5. Changed "Hard Drive Security Frozen" default setting to disabled.
6. Disabled support for EFI iSCSI.
7. Added support for SUM upload/delete HTTPS TLS certificate.
8. Set absence of onboard LAN to be reported to BMC when there is no onboard LAN on system.
9. Fixed failure of SUM TC 221 when installing multiple NICs on system.

### **1.1 (4/21/2021)**

1. Updated RC 20.P95 for PV RC update.
2. Changed the BIOS version to 1.1.
3. Updated for CPLD Signature table 101.
4. Updated Intel-Generic-Microcode-20210402\_NDA.
5. Updated Dx/Mx microcode from Intel AE (synced with BKCWW14).

### **1.0b (3/26/2021)**

1. Updated RC 20.P93 for PV RC update.
2. Updated BIOS ACM 1.0.9 and SINIT ACM 1.0.9.
3. Updated SPS 4.4.53.
4. Updated x/Mx microcode from Intel AE.
5. Automatically disabled and hid ADDDC with x8 width DIMM.
6. Extended memory DIMM serial number information (Samsung, Micron, Hynix).
7. Enhanced SMC DCPMM feature.
8. Automatically disabled and grayed out ADDDC, UMA-Base Clustering, and mirror mode and enabled NUMA when SGX is enabled.
9. Set relation setup to restore setting after "Factory Mode" is disabled.
10. Removed 4G limit of Intel LAN memory if boot mode is not legacy.
11. Set all OPROM control items to Legacy when boot mode is set to Dual.

12. Updated SATA/sSATA EFI driver to VROC PreOS v7.5.0.1152.
13. Updated BPS firmware to 2.2.0.1553.
14. Changed the BIOS version to 1.0b.
15. Fixed problem of T-states always showing 15 levels even when T-state is disabled.
16. Filtered Dynamic HDD Security pages to patch failure of SUM ChangeBiosCfg.
17. Fixed inability to upload all OOB files on the first BMC boot.
18. Corrected display of UEFI OS boot option name in BIOS setup.
19. Corrected riser card SMBIOS creation.
20. Fixed problem with a CPU exception.
21. Fixed failure to hide the SmcSecureErase setup page when no HDD devices are plugged in.
22. Corrected display of NVMe LED on BPN-NVMe4-216N-S24 under VMD mode.

#### **1.0a (1/28/2021)**

1. Changed the BIOS version to 1.0a.
2. Updated BKC WW51 components.
3. Fixed inability of system to boot into PXE with DVD installed.
4. Fine tuned re-timer add-on card workaround.
5. Fixed problem of system exception occurring at 0x92 or 0xA2 after changing boot order and rebooting.
6. Corrected CPU1 memory type 20 handler point to CPU2 Memory Array Mapped Address structure.
7. Fixed failure of the HDD security menu to display when connecting more than 6 HDDs on system.

#### **1.0 (10/22/2020)**

1. First release

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Product Manager

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Date