

IPMI Firmware / BIOS Release Notes Form

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Product Name	H12SSFR-AN6
Release Version	2.4
Release Date	04/22/2022
Previous Version	2.3
Update Category	Recommended
Dependencies	N/A
Important Notes	BIOS Image: BIOS_H12SSFR-1C14_20220422_2.4_STDsp.bin BIOS Update Package: BIOS_H12SSFR-1C14_20220422_2.4_STDsp.zip
Enhancements	1. [Rome/Milan BIOS] Change BIOS revision to 2.4. 2. [Rome/Milan BIOS] Exposed setup item "ASPM Support" in PCIe/PCI/PnP Configuration Page. 3. [Rome/Milan BIOS] Add "Factory Mode" function for Production test. 4. [Rome BIOS] Update AGESA RomePI to 1.0.0.D. 5. [Rome BIOS] Update Rome B0 stepping CPU microcode 0x08301055 to patch Errata 1161, 1176, 1179, 1183, 1214, 1268, 1386, 1417. 6. [Milan BIOS] Update AGESA MilanPI to 1.0.0.8. 7. [Milan BIOS] Update Milan B0/B1/B2 stepping CPU microcode and Milan-X B2 stepping CPU microcode to patch Patch for Errata 1304, 1310, 1311, 1313, 1329, 1330, 1336, 1343, 1350, 1352, 1361, 1378, 1379, 1381, 1386, 1407, 1415. 8. [Milan BIOS] Exposed setup item "SNP Memory (RMP Table) Coverage" and "Amount of Memory to Cover" in CPU Configuration Page.
New features	N/A
Fixes	N/A

Release Notes from Previous Release(s)

2.3 (10/21/2021)

1. [Rome/Milan BIOS] Change BIOS revision to 2.3.
2. [Rome BIOS] Update AGESA RomePI to 1.0.0.C.
3. [Rome BIOS] Exposed Setup item "Enhanced Preferred IO Mode".
4. [Milan BIOS] Update AGESA MilanPI to 1.0.0.6.
5. [Rome/Milan BIOS] Patch BMC Redfish Host Interface was named as ethX when CDN was disabled case under Linux OS.
6. [Rome/Milan BIOS] Disable EFI iSCSI support.
7. [Milan BIOS] Exposed Setup items "BankGroupSwapAlt", "SEV-SNP Support", "Enhanced Preferred IO Mode" and "Root Complex 0x00~0xE0 LCLK Frequency".
8. [Rome/Milan BIOS] According to PM's request, change default of "Wait For "F1" If Error" to Disable.
9. Update Milan B0/B1 stepping CPU microcode and Milan-X B2 stepping CPU microcode to patch Erratum #1381 Processor May Hang When Coherency Probe Hits Instruction Cache Line While Evicted.
B0 Milan microcode 0x0A00104C, B1 Milan microcode 0x0A001143, B2 Milan-X microcode 0x0A001223.

2.2 (9/3/2021)

1. [Rome/Milan BIOS] Change BIOS revision to 2.2.
2. [Rome/Milan BIOS] Add Redfish/SUM Secure Boot feature, update OOB for secure boot and reserve Key.
3. [Rome/Milan BIOS] Support SUM upload/delete HTTPS TLS certificate. (Default Enabled by TOKEN "Sum_UploadTlsKey_SUPPORT")
4. [Rome/Milan BIOS] Per AMD's suggestion, set Relaxed Ordering default to Enabled.
5. [Milan BIOS] Update AGESA MilanPI to 1.0.0.4.
6. [Milan BIOS] Update A010 GN B0 microcode 0A001046.
7. [Rome BIOS] Update AGESA RomePI to 1.0.0.C.
8. [Milan BIOS] Fix SUM cannot modify AMD CBS settings.
9. [Milan BIOS] Fix IPMI Web GUI No Sensor info

2.0 (03/12/2021)

1. [Rome/Milan BIOS] Change BIOS revision to 2.0.
2. [Milan BIOS] Update AGESA MilanPI to 1.0.0.1.
3. [Milan BIOS] Update A011 GN B1 microcode 0A001119.
4. [Rome BIOS] Update AGESA RomePI to 1.0.0.A.
5. [Rome/Milan BIOS] Fix IPV6 disable in the IPMI GUI but BIOS initialize will appear IPV6 address.
6. [Rome/Milan BIOS] Per Platform Management FRU Information Storage Definition v1.1 to enhance SMBIOS Type39 System Power Supply information.
7. According to PM request, disabled NVDIMM support.

1.3 (02/08/2021)

1. Implemented AH25G-m2S2TM Intel X550 Lan Drop recovery scheme
2. Issue 120294, AOC-AH25G-m2S2TM system information incorrect
MAC address for 25G

1.0 (12/28/2020)

First release.