BIOS Release Notes Form

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

Product Name	H12SSG-AN6
Release Version	2.4
Release Date	6/23/2022
Build Date	6/23/2022
Previous Version	2.3
Update Category	Recommended
Dependencies	IPMI firmware R 00.01.12
Important Notes	BIOS Image: BIOS_H12SSG-1C2C_20220623_2.4_STDsp.bin BIOS Update Package: BIOS_H12SSG-1C2C_20220623_2.4_STDsp.zip
Enhancements	 Changed BIOS revision to 2.4. Added setup item, "ASPM Support" in PCIe/PCI/PnP Configuration Page. Added "Factory Mode" function for Production test. Updated AGESA RomePI to 1.0.0.D. Updated Rome B0 stepping CPU microcode 0x08301055 to patch Errata 1161, 1176, 1179, 1183, 1214, 1268, 1386, 1417. Updated AGESA MilanPI to 1.0.0.8. Updated Milan B0/B1/B2 stepping CPU microcode and Milan-X B2 stepping CPU microcode to patch for Errata 1304, 1310, 1311, 1313, 1329, 1330, 1336, 1343, 1350, 1352, 1361, 1378, 1379, 1381, 1386, 1407, 1415. B0 Milan microcode 0x0A001058, B1 Milan microcode 0x0A001173, B2 Milan-X microcode 0x0A001173, B2 Milan-X microcode 0x0A001229. Added setup item, "SNP Memory (RMP Table) Coverage" and "Amount of Memory to Cover" in CPU Configuration Page. Implemented work around solution for AOC-ATGC-i2TM PCIE gen drop problem on H12SSG-AN6.
New features	N/A
Fixes	N/A

2.3 (10/22/2021)

- 1. Changed BIOS revision to 2.3.
- 2. Updated AGESA RomePI to 1.0.0.C.
- 3. Exposed Setup item "Enhanced Preferred IO Mode".
- 4. Updated AGESA MilanPI to 1.0.0.6.
- 5. Patched case of BMC Redfish Host Interface being named ethX when CDN is disabled under Linux OS.
- 6. Disabled EFI iSCSI support.
- 7. Exposed Setup items "BankGroupSwapAlt", "SEV-SNP Support", "Enhanced Preferred IO Mode", and "Root Complex 0x00~0xE0 LCLK Frequency".
- 8. Changed default of "Wait For "F1" If Error" to Disable.
- 9. Updated B0 Milan microcode 0x0A00104C, B1 Milan microcode 0x0A001143, and B2 Milan-X microcode 0x0A001223 to patch Erratum #1381 problem of processor hanging when coherency probe hits instruction cache line while evicted.

2.2 (7/31/2021)

- 1. Changed BIOS revision to 2.2.
- 2. Set all OPROM control items to Legacy when boot mode is set to Dual.
- 3. Added Redfish/SUM Secure Boot feature and updated OOB for secure boot and reserve key.
- 4. Removed legacy iSCSI support of H12 BIOS.
- 5. Added force next boot to UEFI Shell support.
- 6. Added support for SUM upload/deletion of HTTPS TLS certificate (default enabled by TOKEN "Sum_UploadTlsKey_SUPPORT").
- 7. Set Relaxed Ordering default to Enabled.
- 8. Updated AGESA MilanPI to 1.0.0.4.
- 9. Updated A010 GN B0 microcode 0A001046, A011 GN B1 microcode 0A001137, and A012 GN B2 microcode 0A00121D.
- 10. Updated AGESA RomePI to 1.0.0.C.
- 11. Fixed inability of SUM to modify AMD CBS settings.
- 12. Fixed missing sensor information on IPMI WebGUI.

2.0 (5/05/2021)

Initial Release