BIOS Release Notes Form

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Product Name	X13SEW-(T)F
Release Version	1.1 SPS: 6.0.4.025
Release Date	1/19/2023
Build Date	1/19/2023
Previous Version	1.0a
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	 Updated Intel BKC 2022 WW51 (5.29_EagleStreamCrb_0ACOR_071_BETA). Updated SPS to 6.0.4.25 2S PV Hotfix Release. Enhanced the memory map out. Exposed "CXL Security Level" and "CXL Header Bypass" in setup for CXL. Updated the SMCOOB module to 2.00.19. Followed the latest BIOS setup template, and hid the UPI3 feature in setup for UP projects. Fixed FWTS fail relative to SRIO. Followed the latest BIOS setup template and exposed "Consistent Device Name Support" in setup.

New features	None
Fixes	1. Updated and enabled the PRM package to resolve the issue with
	the PRM test device, which could not be recognized under
	Windows device manager.
	2. Resolved the CXL memory which could not be reconfigured from
	DAX device to memory.
	3. Removed CPU2, CPU3, and CPU4, which were displayed under IIO
	Configuration in setup after restoring optimized defaults.
	4. Removed CrowPass numa node information.

Release Notes from Previous Release(s)

1.0a (10/19/2022)

- 1. Updated Intel BKC WW41 from AMI label 5.29_EagleStreamCrb_0ACOR_065_BETA.
- 2. Enabled the "SMC_HECI_HIDE_DELAY" token (HECI-x hide delay) for all projects to fix the BSOD issue.
- 3. Added the token ENABLE_SRIO_ASL_PORTING to control ASL porting for ASL device SRIO.
- 4. Updated the DBX file to fix the Secure Boot Bypass issue.
- 5. Added enhancements to run the SUM TC: 220/271/356/457 Windows OS and fix the boot order compare fail after flash BIOS.
- 6. Enabled the password to be cleaned after flashing the BIOS and then resetting the system.
- 7. Fixed COM1/COM2 console redirection causing a system to hang when disabled.
- 8. Fixed HTTP/HTTPS boot failure.
- 9. Fixed the SUM updating the DMI data causing every DMI value to be set.
- 10. Removed the BMC and the TPM smbios type 45 to fix WHQL smbios failure.
- 11. Enabled saving the password after the NVRAM module is updated.